

Date – 05/24/2011

Attendees: CJ Clark, Bill Tuthill, Brian Turmelle, Carl Barnhart, Ken Parker, Carol Pyron, Adam Cron, John Braden, Craig Stephan, Matthias Kamm, Roland Latvala , Adam Ley, Ted Eaton, Wim Driessen, Dave Dubberke, Francisco Russi,

Missing with pre-excuse

Missing: Lee Whetsel, Neil Jacobson, Mike Richetti, Bill Eklow, Ted Cleggett, Heiko Ehrenberg,

Agenda:

- 1) **11:00** Patent Slides and Rules of Etiquette
- 2) **11:05** PDL Annex C
 1. Discussion
- 3) **11:35** BSDL update
- 4) **11:50** Call for New Business (if any)

Meeting Called to order at 11:00 am EST

Minutes:

Review Patent Slide – Reminder sent out over email during the last week.
Review of Working Group Meeting Guidelines

Carl: Annex C 5 section is intended to be the PDL half of it.
Half of the example is in the register field in BSDL
Will intended to have an integrated example that is in both Annex C and Annex B

Ken: Wants to know what is contained in the package files called out. This is where he gets lost

CJ: Package file includes the attribute register fields and mnemonics. Same information in the BSDL you can find I the package file but it is targeted for the package that is being provided. Vendor is just describing small TDR register

Adam C: If building top level chip, is that a “USE” or “Include” statement, or is there some collection of stuff that needs to be in the BSDL

CJ: Use statement in BSDL tells you what to bring in. BSDL file calls out the instances of whatever it is that you decide to use.

Ken: would be nice to see a recent version of Annex B available. In order to understand Annex C you need to take in some of the newer information from Annex B

Noted that there are more examples that need to be done in Annex B.C
And these will be added once Annex B and Annex C are in better shape

At Friday's meeting, Cisco had pointed out that PDL 0 is limited due to not having while and for loops

Carl: feels that PDL 0 is more than what we had when we started.

Annex B discussion

Register Mnemonics

CJ doesn't feel that the parenthesizes are being handled correctly in BSDL

Has changed a couple of the rules and brought in the < > and used it for info tags

CJ has also changed text to indicate left and right parenthesis.

Carol: this way is probably more accurate. But might make it harder to read.

Carol: a lot of languages use () as part of syntax so there should be examples.

Ken: doesn't have a problem with what CJ has written.

Ken: Line 423 says "they may appear in any order" . Not something you would find in BNF.

Also Line 432 - states that items enclosed in () are grouped.

In example there is use of | which adds some confusion as to what the OR is ORing. Either from := or the groups in the ()

CJ: just reusing what is available from industry capabilities that are there .

Carl: would it help if there was a "(" after component name and after package")" before is in this attribute? Would at least define the choices.

CJ has changed the text to make the OR clearer and removed the ()

CJ: do we need the key word "info" in tag? Do we require the extra text?

Carl: it is enclosed by chevron's which make it unique. So no we don't need the extra text

Pattern shorthand

Is there any value in having the short hand in the mnemonics field description?

Carol: it is to break up repeating chunks.

Adam C: be useful with long binary values to make a smaller more readable string.

CJ: needs something to tell parser that this is shorthand.

Ted: can you use a comma separated list of shorthand patterns ?

CJ: Where do we draw the line in terms of the complexity?

Carol: this may not essential but is it hard to implement? Would it be easily managed by customers?

CJ: thinks it would be another syntax for the customer to need to learn and possibly make a mistake with.

Bit field string

CJ: do we limit that

Carl: doesn't want to put quotes around a bit field name.

CJ: don't have to

Carl: it is if it is a string. String is defined as having quotes around it.

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Carl: need to create a rule and name for it. Might as well stay with VHDL identifiers

CJ: it makes you do weird things like not starting with a number. (i.e 125MHz = F125MHz)

Ken: you need a lexical description of what it is but don't have one. String is not the right thing.

CJ: anything that is TCL compatible would seem to be ok right?

Carl: even PDL identifiers are legal in TCL

Adam L: Pattern Shorthand was to allow chip or ip provider to provide a string without needing to document how it broke down.

CJ: pattern shorthand is a continuous representation of bits

Adam L: if you have to name fields you can obfuscate them. Naming starts to correlate to documentation. Pattern shorthand is a way to hand a value to a BIST that is not documented.

CJ: Pattern Short hand as it is now is not for that.

Adam C: All 0's all 1's or all X's as pattern shorthand?

Likes Adam L's reasoning

Carl: the way it is written now no.

Ken would like the syntax from B.8.17 in an email so we can look at it.

CJ will send it out.

Meeting adjourned: 12:05 EST.

Next Meeting: 5/31/2011 11:00 AM EST

0 Motions Made

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Meeting time: Tuesdays 11:00 AM (EST) (Recurring)

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