Date – 05/31/2011

Attendees: CJ Clark, Bill Tuthill, Brian Turmelle, Carl Barnhart, Wim Driessen, Craig Stephan, Ken Parker, Dave Dubberke, Adam Cron, John Braden, Francisco Russi, Carol Pyron, Roland Latvala, Adam Ley, Heiko Ehrenberg.

Missing with pre-excuse


Agenda:

1) **11:00** Patent Slides and Rules of Etiquette
2) **11:05** PDL Annex C
   a. Short review of Carl’s updates
3) **11:35** BSDL discussion
   a. Need another week for BSDL work
   b. B.8.4.1.3 Semantic checks
4) **11:50** Call for New Business (if any)

Meeting Called to order at 11:05 am EST

Minutes:

Review Patent Slide – Reminder sent out over email during the last week.
Review of Working Group Meeting Guidelines

Carl hasn’t made any significant changes to review at this time. Has incorporated Annex C into the main document but has not added any significant new content to Annex C

Review of the numbering of BSDL semantic checks
There is concern that the rules and syntax changes will affect the numbering of paragraphs in Annex B and will not easily map to previous versions of the draft.

examples

B8.1.1 Specifications
Syntax
Then rules
   Rules previously called semantic checks.
   These rules are in 8.1.1
B8.3.1 Now points specifications which has syntax under it. This still lines up with previously coded parsers.

B8.4.1.3 This number doesn’t exist in new document. Rule is now B8.4.1.a. Numbering could be a problem with other standards that reference 1149.1, but they would be referencing a certain version of standard. So when they update standard then they could reference new 2011 1149.1 standard.

Should we create a reference document to map the changes of BSDL rules between 2011 version?

Need something to indicate that duplicate cells/identifiers/cell contents, we need a rule how to handle the duplicates. Do we take the latest package listed? Or the First one?

Carl: the problem with the first package listed is that other packages are reference through other standards called out in the BSDL. Such as 1149.6 calling the 1149.1 standard could cause problems.
Ken: syntax doesn’t allow you to order things that way.

CJ: we need a B8.4.1.d rule that says to ignore duplicates. Use first one only. Multiple different years can cause conflicts.

CJ: B8.5.1.a User-defined VHDL package – added ‘use-defined in front.

Adam C: first one or most recent by date
Carl: use the one that the BSDL calls out. Could have a component that was designed to a specific version.

Adam L: User defined packages state that there is a package level use statement. See B.10.
Carl: Left over from VHDL
CJ: not going to change that. Should just allow any standard package and have duplicates.

BSDL work needs another week.

Any news on the ECID?
Carl: ECID have not contacted Carl. Not sure where it is.

Example in B8.18.2 should not be the first example to show. This one has Hierarchy in it. Might want a simpler example here.
Carol: examples should be snippets without hierarchy.
Carl: section B.11 BSDL example apps, we need a new section there with a complete Hierarchical example that matches what we are doing with PDL.
So the package file and BSDL are coded for the same device used in the PDL example.
CJ: should have a mix of example of hierarchy and how to define the bits.
IEEE 1149.1-2011 Boundary Scan Working Group Minutes

CJ: do we mix and match register fields with hierarchy.
Carol: we do want to show hierarchy. Very powerful
    Should show a register with and without hierarchy
    Should show simple examples to complex examples
CJ: wasn’t suggesting getting rid of hierarchy but whether it should be with
    Register_Fields.
CJ: make it your homework to review hierarchy. Review Annex B.
Roland: Register_Assembly? Is this new?
CJ: it has been there for a while
Carl: was something that CJ was creating it.
CJ:Register_Field calls out all the bits.
    Register_Assembly lets use few number of bits used relative to the large number of
    bits.
    * allows you not to have to set the length.

Wim: B8.17.1 makes reference to the key word - “Others”, What is that used for
Carl: values that have not yet been named.
    Draft needs example to show it used in other cases.
CJ: what we need for a rule. If we select nothing as the target mnemonic, should the tool
    pick any of the values?
Carl: tool should give an error.

Wim: is “:=” the correct syntax?
Carl: “:=” is a VHDL assignment statement.

Meeting adjourned: 12:05 EST.

Next Meeting: 6/7/2011 11:00 AM EST

0 Motions Made

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`ppppp11491p*pp03820#`

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