Date – 06/21/2011

Attendees: CJ Clark, Bill Tuthill, Brian Turmelle, Carl Barnhart, Heiko Ehrenberg, Ken Parker, Craig Stephan, Dave Dubberke, Josh Ferry, Francisco Russi, Adam Ley, Ted Eaton, John Braden, Roland Latvala , Bill Eklow,

Missing with pre-excuse Adam Cron, Wim Driessen, Carol Pyron,

Missing: Lee Whetsel, Neil Jacobson, Mike Richetti, Ted Cleggett, Matthias Kamm,

Agenda:

1. *11:00* Patent Slides and Rules of Etiquette
2. 11:05 Annex B
    a) Review of B.8.17
3. 3) 11:40 BSDL discussion
    a) Register_Fields
    b) Homework assignments

Meeting Called to order at 11:05 am EST
Minutes:

Review Patent Slide – Reminder sent out over email during the last week.
Review of Working Group Meeting Guidelines

Review of B.8.17
      Ken feels that the BNF is parse-able
      Dave Dubberke has provided Carl some feedback through email
      Others key word has to be the last assignment and represents patterns that have not been stated so far
         Question about rule about the use of others if all patterns have been enumerated – rule i
         If rule i is removed than in rule h the "at least one" would need to be removed.
         i is removed and h is modified per above.
      Rule e modified to add that every pattern shall be different in at least one bit position where both contain only 0 or 1's
      Group does not have any complaints about Register mnemonics.

      Examples in B.17.2 have been added
      Mnemonic is not a VHDL identifier so it can be named different. The group name is a VHDL identifier so it is more restrictive as to what you can call it.

Motion made to adopt section B.17 with amendments made today without prejudice to changes in the future
> **Motion seconded**
> **Motion passes unanimously**

Register Fields
> Key words Cap and UPD are already defined in BSDL, how register are defined in package files. CJ would like to use those to give the designer a method of indicating if they used capture-only or update-only register.
>> *Carl's definition*
>> UPD means that only the parallel output is used of register
>> Cap means that only the parallel input is used.
>> Default is that both are used.
>> Carl would prefer key words of PI and PO
>> CJ thinks this is a mistake
>> *CJ's definition*
>> UPD means it has an update stage without a capture.
>>> Driving out of the update register
>> CAP means that there is a capture stage without an update
>>> Driving out of the capture register
> Carl thinks the definitions should be clearer and describe if the CAP or UPD are affecting anything.
>> Are we describing the structure or behavior of the register?
>> CJ thinks we are describing the structure.
>> CJ – if we put UPD than the value will change at that state. No UPD there is no change in the state.
>> Ted – not sure why we need this either
>> CJ – determine the different between full register that capture and update and registers that don't capture
>>> Easier to understand what the logic is doing .
>>> The IREADs and IWRITEs are optional.
>>> Without the IREAD or IWRITE you still don't know how to display the data if the
>> Ted – displaying TDO data not necessarily just capture data.
>> CJ – when we go through capture the TDO is data from capture.
>> Ken- data will come out just undefined.
>> CJ – right. If there is no capture than the data would be meaningless.
>> Ted – if someone put this text in than the tool would say "UNKNOWN"
>> CJ – would be exactly what we would want to
>> Adam L - capture only, capture scan flow that still has a PO and then there is a full capture scan update.. No third characteristics
>> Carl – we need to figure out how to "type" the registers
>> CJ - agreed
>> Francisco – there are figured for Capture and Update. Need to consider asynchronous and synchronous.

CJ – not trying to describe every cell. Just what is necessary for the PDL writer or tool looking at the PDL

Carl – feel free to send in emails how you would like to see the register cells/fields described.

Meeting adjourned: 12:05 EST.

Next Meeting: 6/21/2011 11:00 AM EST

1 Motion Made
Motion made to adopt section B.17 with amendments made today without prejudice to changes in the future
Motion passed unanimously

NOTES:

1149.1 working group website - http://grouper.ieee.org/groups/1149/1/

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ppppp11491p*pp03820#

JOIN the meeting as GUEST – will have to ask to present

 Meeting time: Tuesdays 11:00 AM (EST)   (Recurring)

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