

Date – 11/22/2011

Attendees(18): Adam Cron, Adam Ley, Bill Bruce, Bill Eklow, Brian Turmelle, Carl Barnhart, Carol Pyron, CJ Clark, Craig Stephan, Dave Dubberke, Dharma Konda, Francisco Russi, Heiko Ehrenberg, Jeff Halnon, John Braden, Ken Parker, Ted Eaton, Wim Driessen

Missing with pre-excuse(0):

Missing(13): Lee Whetsel, Neil Jacobson, Mike Richetti, Ted Cleggett, Matthias Kamm , Josh Ferry, Peter Elias, Roland Latvala, Brian Erickson, Roger Sowada, John Seibold, Wim Driessen, Sankaran Menon

Agenda:

- 1) Patent Slides and Rules of Etiquette
- 2) Review of Draft changes, Q&A on draft
- 3) Brief look at 'lost paragraph'
- 4) PDL Level 1
- 5) IDCODE/USERCODE
- 6) Turkey

Meeting Called to order at 10:30am EST (new starting time)

Minutes:

1)

Review Patent Slide – Reminder sent out over email.

Solicited input from anybody who is aware of patents that might read on our standard.

No responses.

Review of Working Group Meeting Guidelines

2)

Carl reviewed the changes for 9.4

Carl: Add a rule to have domain control request

Adam Cron: What's the difference between this signal gating the output of the power controller rather than being an input to the controller?

CJ: Want to be sure that JTAG rules the power. It is not just a request it is an override.

Carol: What if Domain A can't be on if B is on?

CJ: If a request for Domain A being on is requested, then B should be turned off and A turned on.

Bill Bruce: Can a request be made that might never be granted?

Carol/CJ: Yes. Power can be off permanently if external or a fault in the circuit.

Carl: Add documentation request for domain power sequences that conflict or are not allowed.

Ken: Is this all controlled by TCK?

Carl: Yes

CJ: There will be some wait time with an iRunLoop.

CJ: Will need some normative text as to why a power domain will not come on, such as a missing system clock. Same as for init_setup/init_run.

Jeff Halnon: What's wrong with a rule mandating just TCK and no system clocks?

Carl: Once the PLL is in a 1:1 mode is it an insurmountable issue?

Carol: I'm afraid it would be too complex to require that. A recommendation is in order. Some power domains are more static and tied with a POR. Some are dynamic and require more complex handshaking with a PLL and possibly security monitor.

CJ: Unrealistic to force TCK only control when system clocks are so integral to the chip function. It may alienate the design community.

Carl: B.8.20 semantic rules. Excludable segments in the boundary can only control segments in the boundary and not other registers.

Continue discussion in email for the finer details.

3)

CJ: Descriptive paragraph for figure B.9 added.

4)

Pushed to next meeting.

5)

CJ: Device ID checks can be done with PDL. Need examples

Bill Bruce: Is my work on IDCODE done? The document has been handed off to Carl.

CJ: We still need to vote on it.

New topic:

Ken: The <cell number> question for subsidiary standards. 1149.4(no par), 1149.6, 1149.8.1(currently in ballot)

Action Item: Ken to act as liaison for 1149.8.1 and 1149.4. Carl for 1149.6. Does 1149.1 need to take any action or will the other standards have to?

Meeting adjourned: 12:00 EST.

1 Motion Made

Motion1 : Adjourn – Carl : Passed

NOTES:

1149.1 working group website - <http://grouper.ieee.org/groups/1149/1/>

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