

Date – 3/16/2012

Attendees(11): Adam Ley, Brian Turmelle, Carl Barnhart, Carol Pyron, CJ Clark, Craig Stephan, Dave Dubberke, Dharma Konda, Francisco Russi, John Braden, Josh Ferry, Ted Eaton, Wim Driessen.

Agenda:

1. If Ted is ready, discussion on a contextually procs or procs with namespace
2. How to handle specifying moving from TLR to RTI (iRunLoop email thread this week)
3. Topic s left over from Tues as time allows:
4. Rule needed for DOMAIN_EXTERNAL that requires a P_P_A description
5. iSync command – synchronizing IC to IC EXTEST operations (do we need it?)

**Meeting Called to order at 11:30am EDT (Eastern Daylight Savings Time)
Minutes:**

1) Postposed to Tuesdays meeting.

2)

Carl) Tentative changes to iRunLoop from Tuesdays meeting were removed.

CJ) Does our PDL language need to duplicate all of the SVF language? Or do we leave that for PDL Extensions or tool extensions. Do we need to support things like scan bridge?

Carol) The tool will handle the board level and PDL will handle the chip level.

Francisco) The language should have some freedom for board and chip test. Looping at TLR or RTI is important.

Wim) has an issue with an exact number of TCK in a state. It should be a minimum to accommodate syncing.

Adam Ley) Would prefer PDL to be limited to defined DFT structures and architectures.

CJ) Resets are a minimum requirement for verification.

Carol) I need verification capability for the chip level.

Carl) The 2 reset commands and iEndState are low level commands intended for supporting verification of DFT structure, and not intended for higher level function like board test.

CJ) iEndState is there for Read/Modify/Write. Reading a register and modifying a register requires PauseDR. Carl) is there an objection to moving those commands to a separate section as low level commands?

Adam Ley) would prefer to see these commands “walled off” in a separate area. iApply – RMW?

Carol) would like explicit manipulation of all state transitions in PDL.

Wim) the advantage of PDL is you can create procedures that work at the board and system level. Not all components in the chain are 2012 components.

Carol) is reset and idle enough for making sure the components are in the same state?

Francisco) in the beginning we were reluctant to adopt PDL. Now that it's adopted we need PDL to be more robust. Need more state transitions to support 1149.7.

CJ) 1149.7 can extend PDL for its own needs. We're not responsible for other entity standards in 1149.1. We have support for PDL extensions.

Adam) concerning the move to run test idle, if we must have it, can we have a new command? iTMSReset -RTI?

Ted) Why not allow ending in Pause IR?

Carl) iApply does the instruction manipulation behind the scenes.

Carol) Withdraw the request for additional lower level TAP state transition manipulation.

CJ) Can we get consensus on the command for RTI. iTMSIdle?

Carl) will move the low level commands into a separate section. iTMSReset, iTMSIdle

4)

Carol) Do external domains need a rule for POWER_PORT_ASSOCIATION description?

Carl) Do switch able domains have hundreds of power pins?

Carol) Not that many but generally more than 1.

Carl) Will add the rule.

5)

Carl) iSync command has been proposed. With 5 minutes left we need to defer to Tuesday.

6) iGet discussion to align with P1687s requested modifications.

Carl) We're not going to change -si, -so at this time. It's not critical for MEC so will be deferred for later.

New topics:

Meeting adjourned: 1:16pm EDT.

Next meeting: 3/20/2012 10:30am EDT. (Eastern Daylight Savings Time)

NOTES:

1149.1 working group website - <http://grouper.ieee.org/groups/1149/1/>

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