Six new instructions:  
**INIT_SETUP/INIT_RUN**  
- configure on-chip I/O & resources for test  
**CLAMP_HOLD/CLAMP_RELEASE**  
- hold pins & isolate for in-situ on-chip tests  
**IC_RESET** - Reset isolation and control  
- Reset of IC or domains through JTAG  
**ECIDREAD** - Read Unique TAP ID

BSDL attributes for Internal JTAG TDR registers  
- for BIST/PLLs/SERDES IP blocks  
MNEMONICS for JTAG register values  
- Easy to remember words start/stop, on/off  
Package files for on-chip IP blocks  
- self-contained definitions from IP providers  
PDL script files for device initialization/access  
- Read/write registers via Mnemonics  
New <input spec> for input pins  
- Improve diagnostics, coverage, reduce noise