IP level PDL can be re-targeted to IC

attribute REGISTER_FIELDS of Example : entity is
"init_data[505]  ( "&
"(Clock[5] IS (504 DOWNTO 500) DEFAULT(Clockset(100Mhz))),"&
"(Protocol[3] IS (302 DOWNTO 300) DEFAULT(Protocol(off))),"&
"(Voltage[2] IS (101 DOWNTO 100)),"&
"(Reserved[20] IS ( 19 DOWNTO 0))),"&
"(Address[64] IS (163 DOWNTO 100)),"&
"(Data[64] IS (227 DOWNTO 164)),"&
"(WE[1] IS (228)));"

attribute REGISTER_MNEMONICS of SerdesH : entity is
"BSTERM  (Norm      (100) <Normal RX Term. RX threshold @800mV >, "&
"         Dis       (111)  <Termination disabled >, "&
"         rsvrd     (Others) <Reserved - Undefined behavior>), "&
"BSCM    (Norm_cm   (1)  <Normal TX Boundary Scan Common Mode>, "&
"         Diag_cm   (0)  <Diagnostic mode.  Low TX common mode>), "&
"BSSWING (1160mV    (11) <Boundary Scan Output Swing, mVdfpp>, "&
"         1030mV    (10)  , "&
"         890mV     (01)  , "&
"         740mV     (00) ), "&
"CHPMFG  (Test      (100110) <Chip level manufacturing test > )";

end SerdesH;

end SerdesH;