

Date – 01/14/2011

Attendees:

Carl Barnhart,
John Braden,
Dave Dubberke,
Adam Ley,
Ken Parker,
Carol Pyron,
Roland Latvala
Francisco Russi,
Brian Turmelle

Excused: Heiko Ehrenberg, CJ Clark, Ted Eaton

Agenda:

- 1) Continue review of IC-Reset Instruction.

Meeting Called to order at 11:30 am EST

Minutes:

Overview: Carol presented CJ's rewrite of the IC_Reset strawman sent out last night.

- Carl summarized the email discussion focusing on the differences between his and CJ's approaches (primarily the difference of whether the reset-select register was test logic or system logic.)
- The group worked it's way through the drawing in the document which helped clarify the issues.
- The group started a discussion of the rules in the document.
- Ken asked to back off and discuss reasons/requirements for IC_RESET to put the discussion in context. The basic purpose is to provide a means to assert the chip resets with 1149.1.
 - The reset pins might be blocked by the boundary register
 - The reset pins/nets on the board may not be accessible with tester probes.
 - However, concerns have been expressed within and without the group over restricting the designer's choices. The designer may want additional resets beyond those provided by pins, or additional granularity. The designer may want control over the power-up state of the resets, etc.
- Carol: The next extension to Ken's requirement was to have a bit that blocks the Reset pins.
- Adam: Another addition was to have a bit to enable the bit in the register.
- Carl: What is the justification for these additional restrictions? Who is going to use them and how? How is automation going to make any sense out of this?
- Carl: Instead of trying to define the bits of the register, why not reserve a PDL procedure name to be used following an invasive test to reset the chip? It does not even need to use a Standard instruction, then.

- Adam: Automation could be driven from the PDL.
- Adam: We need to remove the TLR reset of the update register since we agree that it is system function.

Carol requested that members list items of consensus. We documented and agreed to the following short list:

- The reset-select (update) register is system function in the meaning of 9.3.1c)
- TLR should not reset the register at all (it will be preserved except at power-up and when loaded explicitly by the IC_RESET instruction.)
- The register and bit definitions need more work:
 - Concern over restrictions.
 - Do we use test power-up resets (testPOR or TRST*) to initialize the update register or leave initialization means to the designer?
 - Do we mandate an initial state of the update register, or leave that to the designer?

Meeting adjourned: 1:00pm EST.

Action Items:

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Next Tiger Team Meeting:

- Next meeting Jan 21, 2011