CSUK = Capture_<tdr>
Shift_<tdr>
Update_<tdr>
clock_<tdr>

System Clock

From SIPO

Packet Decoder/Encoder with Distribution Matrix

To PISO

Packet Decode/Encode

PISO SIPO

Mux And Gating Logic

Clock Control

PISO SIPO

Mux And Gating Logic

Version 10/14/2013

40 bit

40 bit

P149.10_Enable

Reset*

Mode*

Reset'

Mode'

SI/CSUK_Boundary

SI/CSUK_DEVICE_ID

SI/CSUK_Bypass

SI/CSUK_ECID

SI/CSUK_INIT_DATA

SI/CSUK_WSP1

SI/CSUK_WSP2

CSUK_Parallel

SI_PP[9:3]

SO_PP[9:3]
In bound Packets

SCAN - Interleaved Scan Packet

TARGET - Packet enables 1 of N or Any P1149.10 IC to process packets or ignore

SCANR - Ignored, forwarded to outbound packet

Outbound Packets

SCANR - Interleaved Scan Packet response

TARGETR - Target Packet Response

XOFF - Tell ATE to hold off on sending more packets
XON - Tell ATE to resume
IDLE <N> - Tell ATE to insert N IDLE packets