Date – 9/22/2014
Attendees: CJ Clark, Adam Ley, Bill Tuthill, Bob Gottlieb, Brian Turmelle, Craig Stephan, Dharma Konda, Frans de Jong, Gobinathan Athimolom, Jon Colburn, Josh Ferry, Steve Sunter, Tapan J Chakraborty,
Absent with Excuse :
Not Present for ¾ of meeting:
Missing: Bill Huott, Carol Pyron, Jim Wilson, Kent Ng, Kevin Gorman, Tom Wayers, Heiko Ehrenburg, Dave Armstrong, Roger Sowada, Dwayne Burek, Zahi Abuhanmdeh, Mike Ricchetti, Saman Adham, Gurgen Harutyunyan, Teresa McLaurin, Marc Hutner, Ismed Hartanto, Philippe Lebourg,

Agenda:

1) Patent Slide
2) Discussion of this proposal and IEEE standardization
3) Quick review of V38.
   a. Feedback from group/comments
4) New business.

Meeting Called to order at 11:00 am EDT

Minutes:
   Solicited input from anybody who is aware of patents that might read on our standard.
   No Response

Discussion of this proposal and IEEE standardization
   Discussion by the group on whether this Working Group effort would continue under the IEEE standardization group or go its own way and produce a copyright free standard. Two people said they wouldn’t continue in the Working Group if it wasn’t under the IEEE, one person suggested a European standards body and one person said they would continue with the meetings. The others did not express an opinion in the matter.
   There is no action currently being taken.

7.2.1 review
   Table 7-2
   Updated enable to Select
   Added Align attribute specifies from the chip designers prospective what channel bonding would require.
Added to BSDL coding ALIGN_1149_10

7.6.2 specification
   Group_Association _string. Changed GROUP
   Updated 7.6.3 example to reflect change

Needs to update section 8 PDL to show what the PDL would look like

Bob – CoreVoltage in examples, are they 2 different TDRs?
CJ – same one. If you are using the word CoreVoltage it would be the same one.
Bob – is that a use model that you see that they could be in different locations?
   Thinking of it more as a way to describe the physical setup and our responsibility
to group channels
CJ – not thinking it was infinitely flexible. Only ones that would make sense.
Bob – maybe some intermediate step
CJ – think of it without CoreVoltage without being in both groups. You still have the
   separation between instructions and scan data registers.
Bob – agree. We need to do what you are trying to show here.
CJ – please look it over and see if you can poke holes in it.

Send New Business request to reflector

Please use reflector to review what is in the Draft.
Please send comments to reflector.
   Anything that needs to be updated or you would like discussed

Motion to Adjourn:   Bob
Seconded:    Brian
Meeting adjourned: 12:00 pm EDT

Next Meeting:
Sept 29th, 2014 11:00am

Motion Summary

   0    motions made

Action Items

Bill Tuthill—10-21-2013—Add minutes and Attendance spreadsheet to the website.
CJ——11-11-2013——Reach out to ATE industry and Probe Industry to get
   update on future of ATE equipment to see which data speeds and protocols they are
   heading towards.
Philippe – Look into alternative method to create control information (pause, start,
terminate, etc.) rather than using K characters in packet.
Bob – create a case study to show use of Attributes
IEEE 1149.10 High Speed JTAG Working Group Minutes

Frans – will start some block diagrams of a simple use case to help illustrate the current architecture
Dwayne—present to the group his ideas for a simplified scheme—Direct Interface.

NOTES:

1149.10 working group website - [http://grouper.ieee.org/groups/1149/10/](http://grouper.ieee.org/groups/1149/10/)

Here is the WebEx conference link.


You can use VOIP on your computer or dial-in using the phone number below.
Audio Connection
+1-415-655-0001
Access code: 194 196 960