Date – 7/6/2015

Attendees: CJ Clark, Adam Ley, Bill Tuthill, Bob Gottlieb, Brian Turmelle, Craig Stephan, Dharma Konda, Gobinathan Athimolom, Jon Colburn, Josh Ferry, Mike Ricchetti, Tapan J Chakraborty,

Absent with Excuse:
Not present for ¾ of meeting:

Missing: Bill Huott, Carol Pyron, Jim Wilson, Kent Ng, Kevin Gorman, Tom Wayers, Heiko Ehrenburg, Dave Armstrong, Roger Sowada, Zahi Abuhanmdeh, Saman Adham, Teresa McLaurin, Philippe Lebourg, Ismed Hartanto, John Braden, Gurgen Harutyunyan, Steve Sunter, Frans de Jong, Dwayne Burek, Marc Hutner,

Agenda:

1) Patent Slides
2) review of v61
   - Can we be ready next week to make a motion on this?
3) new business
4) adjourn

Meeting Called to order at 11:07 am EST

Minutes:
   Solicited input from anybody who is aware of patents that might read on our standard.
   No other responses noted.

Review of changes to the draft.
Updated formatting of packets to support changes suggested by Josh and Dharma.
   Changed Verilog formatting to 1149.1 formatting.
   Added byte sizes to fields in packet formats figures.
Josh and Dharma were satisfied with the way that the editor made the changes.

No New business

Would like to vote in the changes to version 61 of the draft next week.
Voting eligibility will be sent out to the reflector. Please let me know if there are any discrepancies in attendance.
Attendance is also uploaded to the website if you want to check your eligibility at any time.
CJ will add version 62 to the website also to incorporate some of the changes that Adam had pointed out during the meeting.

Motion to adjourn:  Bob
Seconded:   Tapan
Meeting adjourned: 11:28am EST

Next Meeting:
July 13th, 2015 11:00am

Motion Summary
0 motions made

Action Items
Bill Tuthill—10-21-2013—Add minutes and Attendance spreadsheet to the website.
CJ———11-11-2013——Reach out to ATE industry and Probe Industry to get update on future of ATE equipment to see which data speeds and protocols they are heading towards.
Philippe—Look into alternative method to create control information (pause, start, terminate, etc.) rather than using K characters in packet.
Bob—create a case study to show use of Attributes
Frans—will start some block diagrams of a simple use case to help illustrate the current architecture
Dwayne—present to the group his ideas for a simplified scheme—Direct Interface.
Adam—invite someone from IEEE to speak on IEEE benefits of standardization at WG meeting

Call for Essential Patent notes
Adam Ley 12/1/2014
PN, TTL, AN
7348796, METHOD AND SYSTEM FOR NETWORK-ON-CHIP AND OTHER INTEGRATED CIRCUIT ARCHITECTURES, DAFCA INC.

Steve Sunter 11/17/2014
1. US 7610532 “Serializer/de-serializer bus controller interface” Avago, granted 2009
2. US 7739567 “Utilizing serializer-deserializer transmit and receive pads for parallel scan test data” Avago, granted 2010
3. US 8543876 “Method and apparatus for serial scan test data delivery” Altera, granted 2014

NOTES:
IEEE 1149.10 High Speed JTAG Working Group Minutes

1149.10 working group website - http://grouper.ieee.org/groups/1149/10/

Here is the WebEx conference link.


You can use VOIP on your computer or dial-in using the phone number below.
Audio Connection
+1-415-655-0001
Access code: 194 196 960