

Date – 12//07/2015

Attendees: CJ Clark, Adam Ley, Bill Tuthill, Bob Gottlieb, Brian Turmelle, Craig Stephan, Dharma Konda, Jon Colburn, Ismed Hartanto ,JF Cote, Josh Ferry, Marc Hutner, Mike Ricchetti, Steve Sunter, Tapan J Chakraborty,

Absent with Excuse: Saman Adham,

Missing: Bill Huott, Carol Pyron, Jim Wilson, Kent Ng, Kevin Gorman, Tom Wayers, Heiko Ehrenburg, Dave Armstrong, Roger Sowada, Zahi Abuhanmdeh, Teresa McLaurin, Philippe Lebourg, Gurgen Harutyunyan, Frans de Jong, John Braden, , Dwayne Burek, Gobinathan Athimolom,

Agenda:

- 1) Patent Slides
- 2) Comments on MEC and ballot group formation.
- 3) Review of Clause 10 changes for compliance verification. We're maybe 60% of the way there.
- 4) Clause 4. Added a figure 4-3 which goes with the text description for recommendation h)
(As editor I consider this an editorial change.)
- 5) Clause 7 suggested changes for SCAN_CHANNEL_ASSOCIATION. I added description for MAX_CLOCK and updated the grammar to support the CH_SELECT and variable position MAX_CLOCK, with a set of semantic checks. While somewhat extensive, as editor I believe this can be considered an editorial change since the original capability is unchanged. There is no requirement to number the CH_SELECTs sequentially. When cores and TDRs appear in multiple scan_groups, a solver tool can still pick the bits in the CH-SELECT packet for the enable which simplifies decoding (no barrel shifting is required).

Meeting Called to order at 11:05 am EST

Minutes:

Review Patent Slide – Slides Presented to the Group.

Solicited input from anybody who is aware of patents that might read on our standard.

No responses noted.

MEC/ballot group information

Mandatory Editorial Coordination.

Should be ready to go to MEC in January timeframe.

Wait until end of Holidays to do ballot formation.

Aiming for approval in Fall timeframe

IEEE 1149.10 High Speed JTAG Working Group Minutes

Review of Clause 10 changes

Added to HSTAP definition

 IDLE_CHAR_REQUIRED

Step 6 new material started. (In red)

 Verify that CONFIG TARGET AND RESET are functioning

Reviewed new steps added to validate

 Step 6 thru step 16

60 % of the way there.

Informative section to get people on track to check for compliance.

No comments from group on changes.

Section 4

Figure 4-3 added to show use and recommendation for compliance character

Gives example on how a system would operate

(Editorial Change)

Compliance Enable is optional character. If it is not described than you don't have it.

Discussed Steve's email about security for compliance char.

Steve is concerned about security of leaving DOT10 TAP enabled

DOT1 didn't have a method recommended in standard for security

Discussed the need for security for tap

Brought up that "security" is outside the PAR

Important that the standard doesn't prevent you from doing something. But the industry will come up with something and important not to have something that would block that.

Possibly **Internal Enable** signal could be hook to be used. Possible permission/recommendation clarify it to be allowed as a hook

Key not to prevent something from happening. Up to user to create security method as in other Boundary Scan standards.

Clause 7 Review

Discussed changes

 Max Clock defined

 CH_Select

 Updated rule i)

 Added rule j)

Permissions k) and l) added

Example updated with CH_SELECT keyword and MAX_CLOCK keyword

Motion to Adjourn – JF
Seconded – Bill

Meeting adjourned: 12:03pm EST

Next Meeting:
December 14th 2015 11:00am

Motion Summary
0 motion made

Action Items

~~*Bill Tuthill—10-21-2013—Add minutes and Attendance spreadsheet to the website.*~~
~~*CJ—11-11-2013—Reach out to ATE industry and Probe Industry to get update on future of ATE equipment to see which data speeds and protocols they are heading towards.*~~
~~*Philippe—Look into alternative method to create control information (pause, start, terminate, etc.) rather than using K characters in packet.*~~
~~*Bob—create a case study to show use of Attributes*~~
~~*Frans—will start some block diagrams of a simple use case to help illustrate the current architecture*~~
~~*Dwayne—present to the group his ideas for a simplified scheme—Direct Interface.*~~
~~*Adam—invite someone from IEEE to speak on IEEE benefits of standardization at WG meeting*~~

Call for Essential Patent notes

Adam Ley 12/1/2014

PN, TTL, AN

7348796, METHOD AND SYSTEM FOR NETWORK-ON-CHIP AND OTHER INTEGRATED CIRCUIT ARCHITECTURES, DAFCA INC.

Steve Sunter 11/17/2014

1. US 7610532 "Serializer/de-serializer bus controller interface" Avago, granted 2009
2. US 7739567 "Utilizing serializer-deserializer transmit and receive pads for parallel scan test data" Avago, granted 2010
3. US 8543876 "Method and apparatus for serial scan test data delivery" Altera, granted 2014

NOTES:

IEEE 1149.10 High Speed JTAG Working Group Minutes

1149.10 working group website - <http://grouper.ieee.org/groups/1149/10/>

Here is the WebEx conference link.

<https://meetings.webex.com/collabs/meetings/join?uuid=MAG12PB7HN5W24AM2EOKIOM9KS-KERT>

You can use VOIP on your computer or dial-in using the phone number below.

Audio Connection

+1-415-655-0001

Access code: 194 196 960