

# IEEE 1149.4 Mixed-Signal Test Bus Working Group Meeting Minutes

for  
April 24<sup>th</sup>, 2007

7:30 AM – 8:40 AM PDT

## Meeting Agenda:

Time	Topic	Responsibility
7:30-8:40 AM	1. Review the meeting minutes for March 28 <sup>th</sup> , 2007  2. Update/Review on ABSDL for: <ul style="list-style-type: none"> <li>a. Straw Dog example.</li> <li>b. STA400 chip.</li> </ul> 3. Update/Review on action items for ABSDL documentation draft. <ul style="list-style-type: none"> <li>a. Review further the following attribute MST_PORT_GROUPING rules:               <ul style="list-style-type: none"> <li>i. 10.4.4.5.2d.</li> <li>ii. 10.4.4.5.2e.</li> <li>iii. 10.4.4.5.2f.</li> <li>iv. 10.4.4.5.2g.</li> </ul> </li> <li>b. Provide a note for differential output in attribute MST_PORT_GROUPING.</li> <li>c. Provide ABSDL introduction at the top level of section 10.4.</li> </ul> 4. Other issues.	   Adam Ley Bambang    Adam Ley     Adam Ley Bambang
8:40 AM	Meeting adjourned	Bambang

## Meeting Attendees:

Name	Company
Adam Ley	Asset-Intertech
Heiko Ehrenberg	Goepel Electronics LLC
Ken Parker	Agilent Technologies
Zafar Quadri	Solectron Corp.
Roger Sowada	Honeywell
Bambang Suparjo	Mentor Graphics

**1. Review the Meeting Minutes for March 28<sup>th</sup>, 2007**

- a. The meeting minutes for March 28<sup>th</sup>, 2007 have been approved, suggested by Heiko and seconded by Adam Ley.
- b. Minute 3a: The updated ABSDL example has been reviewed with no further modification. The example will be included in the documentation. A diagram showing the internal boundary scan circuitry based on the example will be provided by Bambang.
- c. Minutes 3e and 3f: Bambang will communicate with Adam Cron regarding the status.

**2. Update/Review on ABSDL for:**

- a. Revise Straw Dog example.  
  
The status is still pending.
- b. ABSDL for STA400 chip.  
  
The status is still pending.

**3. Update/Review on action items for ABSDL documentation draft.**

- a. Review further the following attribute MST\_PORT\_GROUPING rules (Adam Ley):
  - i. 10.4.4.5.2d.
  - ii. 10.4.4.5.2e.
  - iii. 10.4.4.5.2f.
  - iv. 10.4.4.5.2g.

Adam Ley will provide more information on the above rules based on the comments in minute 3b, March 28<sup>th</sup>, 2007.

- b. Provide a note for differential output in attribute MST\_PORT\_GROUPING (Adam Ley).  
  
The note is still not available but Adam Ley will work on it.
- c. Provide ABSDL introduction at the top level of section 10.4 (Bambang).
  - i. The introduction has been reviewed and the updated version is shown below:

The purpose of developing BSDL (Boundary Scan Description Language) extensions is to provide the boundary-scan description of a mixed-signal device designed to be compliant with the IEEE Std. 1149.4. The description includes information about the interconnection between cells in the device's

boundary-scan chain and the device pins. It also defines the control structures for Test Bus Interface Circuit (TBIC) and Analog Boundary Modules (ABM). This information can then be used to generate patterns for interconnect and discrete component testing at the board level. The language is compatible with 1149.1 BSDL, so both digital and mixed-signal interconnect can be tested simultaneously.

- ii. While reviewing the introduction above, Adam Ley initiated the group to review the subtopics in Chapter 10. After referring to the format in 1149.6 and considering the existing subtitles in Chapter 10, the group agreed to change the title for 10.4 from “BSDL” to “BSDL Documentation”.

#### **4. Other Issues**

- a. The next meeting will be on May 22<sup>nd</sup>, 2007 at 7:30 AM PDT.
- b. Bambang will communicate with Adam Cron regarding the new draft version.

5. The meeting adjourned at 8:40 AM PDT.