

Attendees

Adam Cron – Synopsys (Editor)
Adam Ley – Asset-Intertech
Ami Gorodetsky – Testability
Anthony Sparks – JTAG Technologies
Azi Machtiger – Testability
Bambang Suparjo – Mentor Graphics
Bill Aronson – National Semiconductor
Christian Sebeke - Robert Bosch
Heiko Ehrenberg – Goepel Electronics
Ken Parker - Agilent
Michael Wahl - University of Siegen
Pete Collins – JTAG Technologies
Someshwar Gatty – Mentor Graphics
Stephen Sunter – LogicVision (Vice-Chair)
Regrets: Adam Osseiran (Chair)

Election of officers

Ken Parker nominated Bambang Suparjo, seconded by Adam Cron and Heiko Adam nominated Heiko Ehrenberg
No others were nominated.
Both nominees agreed to accept the positions.

Previous teleconference meeting minutes

Minutes were sent to the group and/or posted on 1149.4 web site.
Jan 18, 2005 - discussed initial proposal for ABSDL – initial requirements.
- Adam Ley proposed version 2.0 strawdog.
Feb 22, 2005 - discussed “straw dog” and mixed-signal test extensions.
Apr 5, 2005 - discussed update to version 2.0 → 2.1.
- need more complex version – “kitchen sink”.
May 23, 2005 - reviewed kitchen sink proposal.
- Heiko volunteered to produce semantic check list.
Oct 20, 2005 - reviewed semantic checks.

Summary of latest ABSDL Proposal

(the 50 slides reviewed will be attached)

- “Kitchen sink” is actually a simplified version of “straw dog”.
- Explains where the switches are and how to turn on/off.
- May conform with 1149_1_1993.
- Typo slide 25 – last line should say “ ... D2 and D2N”.
- Typo slide 28 – “prentices” should be replaced to “parenthesis”.
- Steve asked why disable result for BIDIR shall be Z. The group will review this setting.

- Adam Ley proposed that we remove “ATAP” from “MST_ATAP_AT1” etc. and make it MST_AT1 etc. (MST = mixed-signal test).
- Slide 42 – “The first cell is referring to the bit order in Table 2 in the standard”.
- Slide 45 – MST_ABM_Pins → MST_Pins ? - to be discussed.
- No check that non-inverting pins connected to TBIC, and inverting pins connected to TBICN – not a rule in 1149.1 (but is spec’d in 1149.6).
- Bill Aronson or Steve Sunter to provide NSC chip’s pseudo BSDL.

Meeting adjourned 6:00pm