

Criteria	MSA ACJTAG	AC EXTEST	Peak Detector
Robustness – chip, board and system level			
Scalability/Interoperability <ul style="list-style-type: none"> - IC Vendors - IC Process - Rising Frequencies - On-chip capacitor - Compatibility with existing boundary scan components 			
Impact on chip design <ul style="list-style-type: none"> - Power consumption - Area - Complexity - Libraries - Drivers/Receivers 			
Software/Tools support			
Fault Coverage			
Clarity – ability to describe proposal in a standard document			
Impact on board/system design			