

Minutes of 15-Dec-99

John Fuller as substitute chair.

Minutes of Aug. 99 meeting were approved.

Future meetings:

Feb 2-4: need to discuss on Friday, Dec. 17

March 14-16, 2000: Location TBD, talk again this Friday.

Liaison reports:

?? Peter Johansson: 1394a. RevCon meets end of January in Singapore, at least one member intends to vote against approval.

?? David Wooten: 1394b. Finished page turners. Ballot invitations going out.

?? Dave James: 1212. Administrative hurdles have slowed balloting, but are now taken care of and balloting should happen early 2000.

?? Dave James: BRAN proposed Dr. James as liaison from BRAN to 1394.1 (approved). Considered "BRAN #16 liaison to p1394.1."

- Isochronous clock jitter: we don't know if the proposed maximum cumulative deviation will suffice for audio applications, more work is necessary. We will ask audio experts.
- Asynchronous stream coding: We believe that all traffic should have the same bandwidth and reliability implications for consistency with current 1394 implementations.
- Absolute time synchronization: It is too early to make such a constraint, we suggest that BRAN use a relative time which will work regardless of whether or not global time is synchronized.
- Cycle master locations: not resolved.
- Leaf bus bridge: defer to after Philips' Phase 1 Bridge presentation.

Technical topics:

a. Phase 1 Bridge ("phase 1 bridge.ppt" and "extended ph1 bridge.ppt")

Response to BRAN "Leaf bus bridge:" We propose to define the following encoding in self-ID packet:

00b – non-bridge device

01b – reserved

10b – device with bridge functions (not defined by 1394.1)

11b – bridge as defined by 1394.1

BRAN and others may define simplified bridges using the code 10b. Peter will compose a letter to the developer community (to be sent under the signature of p1394.1 chair) to ask if using the "delay" field in self-ID packets according to this scheme will cause any problems.

b. Clock Sync Protocol (BR065R02): Dr. James will make some modifications as per discussion and submit as BR065R03. Bridges will be required to support a deviation of clocks of 1/2 cycle from one portal to the other.

- c. Peak Bandwidth Averaging (BR076R00): Parameters for isochronous setup are maximum bandwidth, average bandwidth, and maximum credit (in bandwidth-cycle units). Reject delay distribution.
- d. Reduced bit-mapped routing tables (BR078R00): Need specific examples and need C code for routing manager before the committee can evaluate this.

---End of Dec. 15---

---Start of Dec. 16---

- e. Additional proposed text (multiple documents)
  - 1) BR074R00: Bridge-aware node requirements: a good discussion paper but many open issues remain.
  - 2) BR075R00: Bus discovery through portal-register accesses: accepted in principle, the editor will incorporate into the draft.
  - 3) BR066R00: Maintaining Integrity by Assigning new busIDs on each bus reset: further review needed.
  - 4) BR060R01: Net Reset and Refresh Sequences: *Note: an error on the web site prevented this document from being available for the full two weeks prior to the meeting, no vote will be taken.*
  - 5) BR067R00: Revised Vocabulary Terms for Improved Clarity: work with Peter offline. NetRefresh becomes NetUpdate.

---End of Dec. 16---

---Start of Dec. 17---

- 6) BR065R02: Clock Sync Protocols: Dave James will revise according to the discussion, creating BR065R03. The editor is instructed to incorporate BR065R03 into the draft.
- 7) BR076R01: Peak bandwidth averaging: Bandwidth will be in units of quadlets/cycle. Change *averageBandwidth* to *totalQuadlets*, the total number of quadlets to be transmitted in an averaging interval. We will also add a parameter called *averagingInterval*, which will be the number of cycles over which *totalQuadlets* are transmitted. Dave James will create a BR076R02, which the editor will incorporate into the draft.
- 8) BR068R00: Event Messages: This document doesn't reflect current thinking. Only one courtesy event is needed, NetUpdate.
- 9) BR062R01: Isochronous Resource Management: A brief discussion of this topic revealed that a bridge-aware IRM needs to be defined, need to coordinate with 1394a's root hierarchy. DVJ to work on this.
- 10) BR063R00: Surreal Interconnect Requirements: A lively discussion ensued and numerous revisions will be made by DVJ.

Future meetings revisited:

Request Dick Scheel to move Feb. meeting because we don't have a host and it conflicts with BRAN. Suggest 3<sup>rd</sup> week of Feb.

Set following meeting to Apr. 27-28 in New Orleans (overlap last day of WinHEC).

Adjourned at 1:55pm Dec. 17.

Attendance list:

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