

# 1. Toning and Signal Detect

P1394b connectivity management requires the intermittent transmission of a tone and a signal detect function.

## 1.1 Connection tone

The PHY constant TONE\_FREQUENCY is specified as between 48MHz and 64MHz, to allow ready derivation from either BASE\_RATE or BASE-RATE \* 10/8.

Each port has a PHY variable `pmd_tone_on` which is used by the connectivity management state machine to control the transmission of the tone by the port. When `pmd_tone_on` is TRUE, the port shall transmit a clock signal as specified in table 1-1. Note that, to allow for lowest power, a more relaxed specification permits the tone to be generated during the start-up phase of an internal oscillator (e.g. PLL).

**Table 1-1—Connection Tone**

Parameter	Units	Min	Max
Initial tone frequency ( <code>pmd_tone_on</code> set TRUE within last 100 $\mu$ sec)	MHz		TONE_FREQUENCY
Frequency	MHz	TONE_FREQUENCY	
Duty cycle	%	40	60
Electrical Parameters	As per 5.2 Transmitter Electrical Specifications		

## 1.2 PMD signal detect function

Each port has a PHY variable `signal_detect` which is used to indicate to the connectivity management state machine whether or not a valid input signal is being received. It is continuously monitored by appropriate code in the connectivity management state machine.

The Signal Detect function shall set `signal_detect` to TRUE when the PMD circuitry receives a valid electrical signal. `signal_detect` shall be set to FALSE when the received differential amplitude is below 80 mV (the No Signal condition). Under all other conditions, the state of `signal_detect` is unspecified. These conditions are specified in Table 1-2

**Table 1-2—`signal_detect` value definition**

Receive conditions	<code>signal_detect</code> value
$V_{input}$ Receiver < 80 mV differential amplitude <sup>a</sup> (No Signal)	FALSE
Receiving a tone or encoded 8B/10B characters at a frequency within the operating range of the port <sup>b</sup> AND Minimum differential sensitivity < $V_{input}$ Receiver < Maximum differential input <sup>c</sup> (Valid signal)	TRUE
Other conditions Examples 1) Receiving neither a tone nor a non-8B/10B encoded data stream 2) Other end of the link undergoing power-on-reset (POR) transients 3) 80 mV differential amplitude < $V_{input}$ Receiver < Minimum differential sensitivity 4) One of the differential lines is open	Unspecified

<sup>a</sup> This implies that the link is open, or the transmitter on the other end of the link is OFF (see Table xxx for definition of OFF transmitter). See below for discussion on the No Signal budget.

<sup>b</sup> This implies the transmitter on the other end of the link must be transmitting encoded 8B/10B characters from the port or is toning, and is functioning normally.

c. This implies that the transmitter on the other end of the link is operating within specifications and the link is within specifications

Under all valid operating conditions there shall be no false positive TRUE indications. Though unspecified, this implies that there must be adequate margin between the `signal_detect` trip point and the inherent noise level of the receiver due to cross talk, power supply noise, etc. Under all valid operating conditions, an incoming signal at or above the minimum receive threshold (200 mV differential amplitude) shall not indicate FALSE. Though unspecified, this implies that there must be adequate margin between the `signal_detect` trip point and the receiver minimum differential sensitivity.

The parameters for the response times are defined in figure 1-1 and specified in Table 1-3

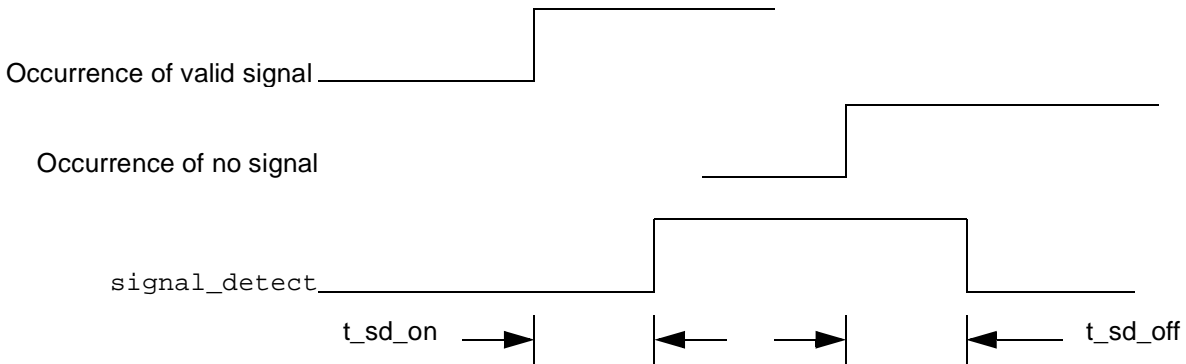


Figure 1-1—`signal_detect` timing parameters

Table 1-3—`signal_detect` timing

Symbol	Parameter	Unit	Min	Max
<code>t_sd_on</code>	Delay from valid signal to assertion of <code>signal_detect</code>	$\mu$ sec		100
<code>t_sd_off</code>	Delay from no signal to negation of <code>signal_detect</code>	$\mu$ sec		100

The Signal Detect mechanism shall be designed such that the value of `signal_detect` does not rapidly change state with small variations in received power. This may be accomplished by an implementation dependent detection circuit that may use one or more of the following mechanisms:-

- a) signal level measurement hysteresis
- b) signal level measurement averaging over a sufficient period to remove pattern dependent amplitude variations
- c) analysis of the preferred state of the detection circuitry
- d) other appropriate mechanisms

1 Examples of a No Signal condition are when the link is unplugged or the transmitter to which it is attached is turned off.  
2 However, small levels of signal may be present under these conditions from various sources. The No Signal level is based  
3 on the budget given in Table 1-4.  
4

5 **Table 1-4—No Signal budget**

6 <b>Parameter</b>	<b>unit</b>	<b>Value</b>
7 Off transmitter	mV	20
8 NEXT via connector and cable (5% of 800mV)	mV	40
9 Noise	mV	15
10 Margin	mV	5
11 Total	mV	80

12  
13  
14  
15  
16  
17  
18  
19  
20  
21  
22  
23  
24  
25  
26  
27  
28  
29  
30  
31  
31  
32  
33  
34  
35  
36  
37  
38  
39  
40  
41  
42  
43  
44  
45  
46  
47  
48  
49  
50  
51  
52  
53  
54  
55  
56  
57  
58  
59  
60  
61  
62  
63  
64  
65  
66