

From: David Instone
 To: Colin Whitby-Strevens
 Date: 14 November 1997

At the last meeting I seem to remember you saying that capacitors are required in the Rx input for the Bmode.

Now it seems to me that these caps must be bypassed for A mode so they must be 'on-chip'caps in the PHY. If so you may have a problem with the required value. I base my arguments on my experience with SSA. SSA PH2 (400/200Mbps) requires the use of 1nF series caps. At the 200Mbps speeds this value of cap shows considerable differentiation effects on a K28.5 character, it sort of precompensates for the HF loss of the cables (I've attached a TIF file showing the effect). This suggests that at 1Gb the cap should not be less than 200pF. Can you get this value 'on silicon'?

