

**Notes of the:
P1394b Ballot Review Committee Meeting
August 29-30, 2000**

IEEE P1394b Ballot Review Committee Members: David Wooten – Cypress Semiconductor (Chair); Colin Whitby-Stevens - Zayante, Inc.; Dave Thompson - Lucent Microelectronics; Eric Hannah - Intel; Jerry Hauck - Zayante, Inc.; Jim Skidmore - Texas Instruments; Max Bassler - Molex; Mike Teener - Zayante, Inc.; Peter Johansson - Congruent Software; Sean Killeen - SSL; Steve Bard - Intel; Victoria Teng – NEC

The group nearly came together around 9:00 AM and work began on BRAT review (08-24-00 edition).

Reasonable progress ensued prior to drilling down on the PIL/FOP interface restore process. The following BRAT ID's were addressed: 205, 209, 311-335, 346, 353, 359, 366-368, and 410-412.

The group reached consensus that the following steps would be applied for all variants of restore from standby on the PIL/FOP interface or Nephew/Uncle interface:

- 1) Tones exchanged
- 2) One node begins sending training
- 3) Other node begins sending training
- 4) When Uncle/FOP are synchronized, Uncle begins arbitrating for serial bus – **what is the Uncle/FOP sending the Nephew/PIL and what is the Nephew/PIL sending to the Uncle/FOP? – Both are sending “favorite” requests to each other.** Point-to-Point packets shall work during this step.
- 5) When Uncle/FOP wins serial bus, it sends Restore PHY packet to Nephew/PIL but sends DATA_NULL on other active ports
- 6) When Uncle/FOP sends Restore PHY packet it waits as long as it is receiving a None request of the wrong phase

It appears there is a need for a PH_RESTORE_NO_RESET point-to-point packet.

The remainder of the meeting (post 1:30 PM) was devoted to PHY DOGS discussion – Eric Hannah and Peter Johansson were excused.

Wednesday, 30th August, 2000

Some progress made on the BRAT today. Colin was able to generate a fair-dinkum SCAT list from his work on the 'C-Code'. The next BRC will be hosted by Cypress (San Diego venue) on Thursday/Friday, September 21st & 22nd. Four blocks of work were identified that, when completed, signify completion of draft standard (ready for next ballot). These areas are: 1) closure on all BRAT items, 2) closure on all SCAT issues, 3) Ballot Comment Response process/procedure, and 3) the actual edits to the draft itself.

The group came to consensus that whether the draft is to be submitted as a stand-alone standard or as an amendment is moot to our work effort. The plan is to technically complete the draft in its current format – thus enabling those poised to develop silicon to proceed post haste! When technically complete, worry will then be given to the actual format required by the IEEE for hard

copy publication – regardless of the number of circulation ballots that may subsequently be required.

Disappointment was expressed (by more than one) that progress by the various chapter owners has been less than robust. Concern: What and how are we going to get this draft standard completed sooner than later?

Agreement was reached that a serial PHY/Link interface will be established between a PIL and a FOP during the speed negotiation phase. Three bits are currently left in reserve of which two will be used for this purpose. One bit will be set by a PIL looking for a FOP while a FOP looking for a PIL will set the other bit. When a connection exists between two such devices, the serial PHY/Link relationship will be established – assuming successful completion of all ACK processes). If a PIL sees a set PIL bit in response then the two devices will come up as separate nodes – that is, a PIL connected to a PIL will not establish a serial PHY/Link interface.

Peter Johansson will e-mail the latest BRAT to the group.

IEEE P1394b Ballot Review Committee Chapter Owners:

CHAPTER	TITLE	OWNER
	Introduction	
1	Overview	EH
2	References	EH
3	Definitions and abbreviations	DW
4	Summary description	MJT
5	Copper physical medium dependent cable media attachment	MB
6	Short-Haul copper physical medium dependent electrical specification	EH
7	Glass optical fiber physical medium dependent specification	MS/DT
8	Plastic optical fiber physical medium dependent specification	DT
9	Category 5 UTP physical medium dependent specification	DT
10	Beta mode port specification	CWS, JH
11	Connection management	CWS, JH, SB
12	B PHY-link Interface (parallel)	DT,VT
13	PIL-FOP Serial Interface	DW, SB,VT
14	PHY register map	CWS
15	Data routing, arbitration and control	CWS, JH, MJT
16	C code	CWS, JH, MJT
Annex A	Annex K supplement - bulk cable specification	MB
Annex B	Jitter measurements	CWS

MB = Max Bassler

MS = Michael Shinkarovsky

CWS = Colin Whitby-Strevens

JH = Jerry Hauck

MJT = Mike Johas Teener

DW = David Wooten

DT = David Thompson

SB = Steve Bard (Steve has offered to help with specific portions of these chapters)

VT = Victoria Teng (Victoria has offered to help with specific portions of these chapters)

IEEE P1394b Ballot Review Committee Meeting (June 23, 2000):

	Name	Company	Email	Phone	29 th	30 th
1	Bard, Steve	Intel	steve.bard@intel.com	503-264-2923	✓	✓
2	Bassler, Max	Molex				
3	Hannah, Eric	Intel	eric.hannah@intel.com	408-765-4441	✓	✓
4	Hauck, Jerry	Zayante, Inc.	jhauck@zayante.com	510-668-1006	✓	
5	Johansson, Peter	Congruent Software	pjohansson@acm.org	510-527-3926	✓	✓
6	Shinkarovsky, Michael	Lucent Microelectronics	mshinkarovsky@lucent.com			
7	Skidmore, Jim	Texas Instruments	j_skidmore@ti.com	972-480-2094	✓	✓
8	Teener, Michael Johas	Zayante	mike@zayante.com	831-461-4901		
9	Teng, Victoria	NEC	victoria_teng@el.nec.com	408-969-2861	✓	✓
10	Thompson, David	Lucent Microelectronics			✓	
11	Whitby-Strevens, Colin	Zayante, Inc.			✓	✓
12	Wooten, David	Compaq	wd@cypress.com		✓	✓

Not Present: Max Bassler - Molex; Mike Teener - Zayante; Sean Killeen - SSL

Adjournment - 5:00 PM
