

**Notes of the:  
P1394b Ballot Review Committee Meeting  
December 13-14, 2000**

IEEE P1394b Ballot Review Committee Members: David Wooten – Cypress Semiconductor (Chair); Colin Whitby-Strevens - Zayante, Inc.; Dave Thompson - Lucent Microelectronics; Eric Hannah - Intel; Jerry Hauck - Zayante, Inc.; Jim Skidmore - Texas Instruments; Max Bassler - Molex; Mike Teener - Zayante, Inc.; Peter Johansson - Congruent Software; Sean Killeen - SSL; Steve Bard - Intel; Victoria Teng – NEC

Members present: David Wooten – Cypress Semiconductor (Chair); Dave Thompson - Lucent Microelectronics; Eric Hannah - Intel; Jerry Hauck - Zayante, Inc.; Jim Skidmore - Texas Instruments; Steve Bard – Intel (Secretary); Victoria Teng – Mike Teener – Zayante; Colin Whitby-Strevens - Zayante, Inc.;

Special guests:

**Kazuki Nakamura** (Mitsubishi Rayon Co., Ltd.);

The committee convened at 9:00 AM and the meeting began at 9:20 AM.

First order of business was to traverse the open items in the combo SCAT bugs data base (dated 13-13-00).

Colin captured the resolution for Combo SCAT items 270, 271, 272, 273, and 278. Jerry and Colin took an action item to resolve issue 280 offline from the BRC – no committee discussion on issue 280.

Discussion continued with issue number 269.

The PHY configuration packet, was agreed to be modified to incorporate the PS bit. A Q-bit was added that is a copy of the E bit in the sending PHY. The detailed information was incorporated into the SCAT (item # ) with a status of not yet done.

SCAT 292 was created to cause the PHY to assert link on after the PHY power on resets. Status is set to Not Yet Done.

Clause 13.3 still contains less than robust text – a simple indent of a bunch of stuff shall resolve this issue.

A discussion of minimum packet spacing ensued.

SCAT 280 was the concluding item discussed.

Thursday was spent editing the various sections and incorporating the resolutions specified in the “NOT YET DONE” entries found in the SCAT document.

Victoria Teng and Steve Bard spent the time reviewing async interval, isoch interval, subaction gaps, isoch gaps, cycle start, bus diameter timing and a number of other historic technologies surrounding 1394.

The next Ballot Review Committee meeting will be co-located with the 1394 Trade Association Conference to be held in Hawaii during January 15<sup>th</sup> through 18<sup>th</sup> – details will follow.

IEEE P1394b Ballot Review Committee Chapter Owners (draft revision 1.01 chapter numbers):

CHAPTER	TITLE	OWNER
	Introduction	
1	Overview	EH
2	References	EH
3	Definitions and abbreviations	DW
4	Summary description	MJT
5	Copper physical medium dependent cable media attachment	MB
6	Short-Haul copper physical medium dependent electrical specification	EH
7	Glass optical fiber physical medium dependent specification	MS/DT
8	Plastic optical fiber physical medium dependent specification	DT
9	Category 5 UTP physical medium dependent specification	DT
10	Beta mode port specification	CWS, JH
11	Connection management	CWS, JH, SB
12	B PHY-link Interface (parallel)	DT,VT
13	PIL-FOP Serial Interface	DW, SB,VT
14	PHY register map	CWS
15	Data routing, arbitration and control	CWS, JH, MJT
16	C code	CWS, JH, MJT
Annex A	Annex K supplement - bulk cable specification	MB
Annex B	Jitter measurements	CWS

MB = Max Bassler

MS = Sam Khoo (assumed from Michael Shinkarovsky)

CWS = Colin Whitby-Stevens

JH = Jerry Hauck

MJT = Mike Johas Teener

DW = David Wooten

DT = Sam Khoo (assumed from David Thompson)

SB = Steve Bard (Steve has offered to help with specific portions of these chapters)

VT = Victoria Teng (Victoria has offered to help with specific portions of these chapters)

IEEE P1394b Ballot Review Committee Membership present December 13<sup>th</sup> and 14<sup>th</sup>, 2000:

	Name	Company	Email	Phone	13 <sup>th</sup>	14 <sup>th</sup>
1	Bard, Steve	Intel	<a href="mailto:steve.bard@intel.com">steve.bard@intel.com</a>	503-264-2923	✓	✓
2	Bassler, Max	Molex	<a href="mailto:mbassler@molex.com">mbassler@molex.com</a>	630-527-4490		
3	Hannah, Eric	Intel	<a href="mailto:eric.hannah@intel.com">eric.hannah@intel.com</a>	408-765-4441	✓	✓
4	Hauck, Jerry	Zayante, Inc.	<a href="mailto:jhauck@zayante.com">jhauck@zayante.com</a>	510-668-1006	✓	✓
5	Farhoomand, Firooz	Panasonic	<a href="mailto:farhoomandf@panasonic.com">farhoomandf@panasonic.com</a>	408-653-4059		
6	Johansson, Peter	Congruent Software	<a href="mailto:pjohansson@acm.org">pjohansson@acm.org</a>	510-527-3926		
7	Kawaguchi, Teruaki	Sumitomo Electric Industries, LTD				
8	Khoo, Sam	Lucent Microelectronics	<a href="mailto:Skhoo1@lucent.com">Skhoo1@lucent.com</a>	610-712-2589		
9	Morikura, Susumu	National/Panasonic (MEI)	<a href="mailto:morikura@isl.mei.co.jp">morikura@isl.mei.co.jp</a>	+81 6 906 4896		
10	Nakamura, Kazuki	Mitsubishi Rayon Co, Ltd.	<a href="mailto:kazuki-n@dtinet.or.jp">kazuki-n@dtinet.or.jp</a>	+81 8275 3 5320	✓	
11	Sean Killeen	SSL	<a href="mailto:sean.killeen@ssl.ie">sean.killeen@ssl.ie</a>	+353 1 402 5700		
12	Skidmore, Jim	Texas Instruments	<a href="mailto:j_skidmore@ti.com">j_skidmore@ti.com</a>	972-480-2094	✓	✓
13	Sogabe, Kazuki	Sumitomo Electric Industries, LTD	<a href="mailto:dw750134@jnet.sei.co.jp">dw750134@jnet.sei.co.jp</a>	+81 6 466 5539		
14	Teener, Michael Johas	Zayante	<a href="mailto:mike@zayante.com">mike@zayante.com</a>	831-461-4901	✓	✓
15	Teng, Victoria	NEC	<a href="mailto:victoria_teng@el.nec.com">victoria_teng@el.nec.com</a>	408-969-2861	✓	✓
16	Thompson, David	Lucent Microelectronics	<a href="mailto:davethompson@lucent.com">davethompson@lucent.com</a>	610-712-2730	✓	
17	Whitby-Stevens, Colin	Zayante, Inc.	<a href="mailto:colin@zayante.com">colin@zayante.com</a>	831-461-4948	✓	✓
18	Wooten, David	Compaq	<a href="mailto:wd@cypress.com">wd@cypress.com</a>		✓	✓

Not Present: David Thompson – Lucent;

### Scheduled Meetings:

Adjournment –

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