

Agenda

P1394b Editorial Working Group

(Draft Specification Page Turner)

August 11, 1999

Those in attendance: Eric Hannah (Intel), Gerald Marazas (IBM), David Wooten, Victoria Teng (NEC), Jerry Hauck (Zayante), Michael Teener (Zayante), Steve Bard (Intel)

Action Items assigned at this meeting:

#1) David Wooten will make certain the copperhead connector drawings are compliant to the appropriate industrial design standards.

#2) Eric Hannah has an Intel colleague that will assist him in resolving a good number for differential transmit skew at S1600. The current spec. calls for 12 pico-seconds - David Wooten believes he may be able to hold 25 pico-seconds but not 12.

#3) Sean Killeen: Is there a timing issue with ARB_RESET_GAP and SUBACTION_GAP serial interrupt notification? PIL is receiving PHY_SOLICITED register contents and a subaction or arb gap occurs, which cannot be reported immediately because FOP_INT is BUSY. There seems to be a timing issue here that must be resolved - OR an opportunity for Sean to explain why everything is okay! This was entered into the SCAT issue data base as issue #60.

As the draft specification was examined Eric Hannah took notes in the form of Acrobat "post-it" notes on the PDF. It was decided that the minutes would not capture the detailed minutia of all changes. Rather, the meeting minutes would be used to capture any wisdom discovered by the group (assuming that the group discovered any).

There was some discussion regarding term definitions in the glossary. Eric pointed out that he did review many of the terms common between 1394b and 1394-1995 and not too much with 1394a. David suggested that a bit more diligence be spent with the 1394a glossary of terms. Gerald pointed out that the IEEE editor (Mary Lynne Nielsen) takes her position very serious (as she should) and reserves the right to have the final word in the final edited document. Eric took an action item to develop a good working relationship with Mary so as to facilitate a more rapid completion of the draft.

Jerry Hauck took an action item to contact Peter Johansson and have Peter send a zip file of the 1394a draft in frame source to Eric. Eric will make use of it to insure proper frame formats for 1394b. Eric will also reconcile term definition variance between 1394a glossary and 1394b glossary - the intent is that terms common between both will have the 1394a definition applied in the 1394b draft specification.

Jerry suggested we come to agreement on a set of terms associated with the "bilingual-ness" of the draft specification. The following are the notes of that transient activity:

Much discussion about the term "node" as used in the 1394b draft. As used in the "B" draft, it doesn't correlate with the "B" term definition of "node", however, if the 1394a definition of node is used, the use of the word node in the "B" draft correlates.

Discussion about the capabilities of something (e.g. a port or a PHY) and the operational mode of something (e.g. a port or a PHY).

PHY modes of operation are: 1) "Border"; 2) "BOSS"; 3) "Legacy". A PHY with either an "A" link port or a cable port operating in DS-Mode is operating in "Border-Mode"

A border node is a node that has a PHY operating in border mode.

A PHY with bilingual ports may function as a border node.

Capabilities of a Cable Port (as opposed to a Link Port): 1) bilingual; 2) Beta-only; 3) DS-Only.

A port may be operating in only one mode: 1) Beta-mode or 2) DS-Mode

There are three types of link interfaces:

- 1 "A": a parallel interface as defined in p1394a draft 3.0 clause 6
- 2 "B": a parallel interface as defined in p1394b draft 0.75 clause 12
- 3 "???: a serial interface operating in beta mode with side-band signals

	LINK	CABLE PORTS	PHY CAPABILITY	PHY MODE
1	A	Beta-Mode Only	Border Capable	Border,
2	A	Beta & DS Mode	Border Capable	Border, Legacy
3	A	Bilingual	Border Capable	Border, Legacy
4	B,???	Beta-Mode Only	BOSS Only	BOSS
5	B,???	Beta & DS Mode	Border Capable	Border, BOSS
6	B,???	Bilingual	Border Capable	Border, BOSS

Captured wisdom: Clause 5.5.1 (Compensation networks) in draft 0.75 shall be deleted because beta cables shall S400, S800 and S1600 speeds making it impossible to compensation networks.

Considerable discussion surrounding the use of the word "legacy". It was determined that the word to be used would be "Legacy" (note the capital "L") and that "Legacy" would be included in the technical glossary.

Review of clause 12.11 yielded many questions. SCAT issue #60 was created - specific to FOP_INT signaling. David Wooten crafted a rather nice e-mail to send off to Sean to see if there might be a better way to handle the information needed from PHY Interrupt, arb reset and subaction gap, etc.

Chapter 14 is a bit scary! Mike Teener assures the group that chapter 14 will be finished prior to the Rennes, France meeting.

Sean and Tony must do all they can to complete the content of chapter 12 prior to the September 16 page turner. David Wooten expects to take the editorial pen for Chapter 12 to insure all content change for chapter 12 that comes up at the page turner is incorporated.

Victoria Teng (NEC) suggested that all of the 'C-Code' be located in one location. There was some discussion about this. Eric pushed back indicating that there would be a tremendous amount of work and time needed to accomplish such a task. Mike Teener suggested he might be

able to assist and, in fact, believed it might lend value to do such a thing. Mike is to investigate into a way to make it much easier to import 'C-Code' into frame maker.

September 10 will be release 0.85. Last inputs are due to Eric by 8:00 AM on 9/10/99. Folks attending the page turner must read 0.85 in preparation for the page turner on 9/16 & 17 (to be held at Zayante).

Friday, August 20 will be the publish date for draft 0.80.

Draft 0;9 will be published on October 4. THis allows folks to spend a bit of time reviewing it prior to the Rennes meeting.

Adjourned: 4:37 PM