

IEEE P1394b SCAT (Sorted by Number)

SCATNo	Title	Owner	DateEntered	StatusDate
1	1394a start-up Alignment	Upstarts	12/10/98	3/22/99
Description				
Fix start-up to align with P1394a when it has been completed				
Status				
WIP				
NextActions				
Waiting for 1394a port state machine to stabilize; 2) what to do on disable; 3) Is toning operational during disable or is INT_ENABLE used to control toning during disable 4) better distinction between port state machine and arbitration state machine				

SCATNo	Title	Owner	DateEntered	StatusDate
2	Root Contention Time	Eric Hannah	12/10/98	3/22/99
Description				
Verify that root contention time values will work with long haul. The time constants chosen were selected for 50 meters. Beyond 50 meters, new constants will need to be determined.				
Status				
Open				
NextActions				
Subject to Upstart support, the method suggested in Alistair's e-mail of March 15, 1999 (Subject: Re: Continue of Loop Breaking in 1394b) could be used to resolve root contention. Tentatively, increase the time until a better solution comes later.				

SCATNo	Title	Owner	DateEntered	StatusDate
3	PHY Register Map	PHY-Link	12/10/98	2/10/99
Description				
Complete design of register map (to include speed limit bits in port registers - settable by firmware, as well as other required bits)				
Status				
DONE				
NextActions				
None				

SCATNo	Title	Owner	DateEntered	StatusDate
4	Standby Proxy Self-ID	B-Low Power	12/10/98	3/22/99
Description				
Proxy self-ID packets for "leaf" nodes in standby				
Status				
AIP				
NextActions				
Write-up complete. Will be delivered at 3/23 plenary. Ratification to occur at April meeting				

SCATNo	Title	Owner	DateEntered	StatusDate
5	Port Interface	B-Port	12/10/98	3/22/99
Description				
Fix & Document Port interfaces (and filtered input from Simulations); Control Codes delivered to Mike and Steve.				
Status				
WIP				
NextActions				
Update as appropriate as information comes forth from simulations				

SCATNo	Title	Owner	DateEntered	StatusDate
6	Signal Detect Plus	UTP	12/10/98	2/10/99
Description				
Resolve signal detect and related issues for UTP-5				
Status				
AIP				
NextActions				
Alistair to update next draft				

SCATNo	Title	Owner	DateEntered	StatusDate
7	High Speed Copper Cables	Max Bassler	12/10/98	3/22/99
Description				
Copperheads have verified that an "Enhanced Plug/Socket" 1995 cable functions at S800 for both Data Strobe & Beta Mode. A question exists as to whether a standard plug/cable will function with an enhanced socket at S800 for Beta mode.				
Status				
AIP				
NextActions				
Verify that an enhanced socket and standard plug and cable (compliant to Annex B of 1394a) which functions at S400 in Data Strobe will function properly at S800 Beta modes.				

SCATNo	Title	Owner	DateEntered	StatusDate
8	UTP Electrical	UTP	12/10/98	2/10/99
Description				
Complete UTP electrical Specs				
Status				
WIP				
NextActions				
Alistair to get numbnrs, Eric to simulate/validate them				

SCATNo	Title	Owner	DateEntered	StatusDate
9	Jitter Budgets	Eric Hannah	12/10/98	3/22/99
Description				
Finalize jitter budget and specifications for all speeds and media (except UTP5).				
Status				
WIP				
NextActions				
Colin to deliver a new template for numbers to be plugged into				

SCATNo	Title	Owner	DateEntered	StatusDate
10	Standby Start-up	Upstarts	12/10/98	2/10/99
Description				
Upstarts complete standby interaction				
Status				
WIP				
NextActions				
Review stand-by technical proposal and validate restore start-up procedure is accurate - report at next meeting				

SCATNo	Title	Owner	DateEntered	StatusDate
11	Standby Codes	B-Port	12/10/98	2/10/99
Description				
Standby encoding signaling				
Status				
WIP				
NextActions				
Alistair will dispense control codes as appropriate - complete by next meeting				

SCATNo	Title	Owner	DateEntered	StatusDate
12	Arbitration Tokens	B-Port	12/10/98	2/10/99
Description				
Arbitration Token Completion				
Status				
WIP				
NextActions				
Mike will use that which is available and will contact Alistair if additional codes are needed				

SCATNo	Title	Owner	DateEntered	StatusDate
13	PHY-Link electrical specs.	PHY-Link	12/10/98	2/10/99
Description				
PHY-Link electrical specs.				
Status				
WIP				
NextActions				
Chris Dorsey will take request for electrical specifications to the ST PHY designers in Grenoble, request numbers, and return with those numbers in a report to be delivered at the next meeting.				

SCATNo	Title	Owner	DateEntered	StatusDate
14	A/B PHY-Link Interoperability	PHY-Link	12/10/98	2/10/99
Description				
A-Link to B-PHY operating model				
Status				
DONE				
NextActions				
None				

SCATNo	Title	Owner	DateEntered	StatusDate
15	B.O.S.S. Port & PHY-Link	Accelerations	12/10/98	2/10/99
Description				
Interface with B-Port connections & PHY-Link sub-layer				
Status				
WIP				
NextActions				
Group is meeting later today. Report will be available at the next meeting				

SCATNo	Title	Owner	DateEntered	StatusDate
16	Beta mode media dependent speed	Colin Whitby-Strevens	12/10/98	3/22/99
Description				
Shall Beta mode support S100 through S400 on all media types? S100 & S200 not wanted on GOF. The issue is regarding a Bi-lingual PHY capable of only S400 or, perhaps S200, it would be nice if it were able to operate in Beta mode when able.				
Status				
WIP				
NextActions				
Presented at 2/9/99 Plenary. Tabled for further discussion in a future Plenary. What is the lowest-speed the Consumer Electronic folks would like to see on Beta mode only?				

SCATNo	Title	Owner	DateEntered	StatusDate
17	Beta PHY ID	Jerry Hauck	12/10/98	2/10/99
Description				
Need a mechanism to identify Beta capable PHYs at self-ID time				
Status				
WIP				
NextActions				
Colin will document a PHY register which will capture the highest link speed of a legacy path - Jerry will provide a write-up to Colin by the end of the day today.				

SCATNo	Title	Owner	DateEntered	StatusDate
18	Beta Copper Connector	Max Bassler	12/10/98	3/22/99
Description				
Two connectors: 1) Beta only with no power; 2) Beta only with power. Need to know what the fastest low-speed the Consumer Electronic folk would like to see for a Beta only copper connector.				
Status				
WIP				
NextActions				
SCAT group to provide input to Max for the several questions he brought up during his presentation to the SCAT group. Max's AR is to write down those questions so the SCAT group may respond to them.				

SCATNo	Title	Owner	DateEntered	StatusDate
19	Loop Healing	Michael D. Johas-Teener	12/10/98	2/10/99
Description				
Changes are needed in Tree-ID such as to support loop healing				
Status				
WIP				
NextActions				

SCATNo	Title	Owner	DateEntered	StatusDate
20	Cycle Start Tokens	Michael D. Johas Teener	12/10/98	2/18/99
Description				
Send out a conventional Cycle Start packet appended onto it is a cycle start token. The token is there to help the PHYs keep their phase straight (and if helps on border node) - need two tokens from Alistair (or a better way to do this).				
Status				
WIP				
NextActions				
Mike will get tokens from Alistair. This is expected to be documented by [standard answer: soon] end of next week (2/26). March 1st will be the date when ALL tokens have been requested of Alistair.				

SCATNo	Title	Owner	DateEntered	StatusDate
21	Bridge Liaison	David Wooten	12/10/98	2/18/99
Description				
1394.1 liaison as required				
Status				
OPEN				
NextActions				
Keep on keeping on - this is going to be open until the lights go out on IEEE P1394b or until it is "too damn late" to keep working with 1394.1 because they are taking too long and 1394b needs to close.				

SCATNo	Title	Owner	DateEntered	StatusDate
22	Speed Map	David Wooten	12/10/98	2/18/99
Description				
Resolve speed map issues				
Status				
AIP				
NextActions				
David will bring up in the plenary that 1394b will not support a speed map. A call from the plenary attendees will be with regard to resolving speed discovery.				

SCATNo	Title	Owner	DateEntered	StatusDate
23	DC BIAS	Steve Bard	12/10/98	2/18/99
Description				
Complete DC BIAS Specification				
Status				
AIP				
NextActions				
Steve Bard post to reflector and request clarification - if none, this will be dropped from the SCAT				

SCATNo	Title	Owner	DateEntered	StatusDate
24	IRM	David Wooten	12/10/98	2/18/99
Description				
Changes to IRM (band-width & channels allocation)				
Status				
OPEN				
NextActions				
It is possible that this has been obsoleted. Peter J. is to write up the proposal from Unibrain. Action on this SCAT to be delayed until Peter's informative write-up can be reviewed. This shall be taken back to the plenary for review and decision.				

SCATNo	Title	Owner	DateEntered	StatusDate
25	PHY Test modes	Lou Fasano	12/10/98	2/18/99
Description				
Develop and document PHY Test Modes				
Status				
WIP				
NextActions				
Lou will contact Eric for his thoughts and will also recruit silicon vendors to help provide documentation as to what standard tests should be developed and the methods/process which should be used to perform the test at the PHY cable interface.				

SCATNo	Title	Owner	DateEntered	StatusDate
26	Error handling	PHY-Link	12/10/98	2/18/99
Description				
Delayed error detection & reporting (receiving a packet, data looks correct (good CRC), but the data end reveals that it is, in fact, incorrect - a method is needed to report the event across the PHY-Link interface).				
Status				
OPEN				
NextActions				
Do we care? No, then close. Yes: Then: A PHY test mode is needed. David will ping Sean Killeen to pick this up. Alistair will provide a problem description and bring forward (again) Eric's work on this.				

SCATNo	Title	Owner	DateEntered	StatusDate
27	Summary Clause	David Wooten	12/10/98	2/18/99
Description				
Write clause three (Summary)				
Status				
Open				
NextActions				
David will find an owner before the spec closes.				

SCATNo	Title	Owner	DateEntered	StatusDate
28	Beta Speeds Less than S800 on Copper	Copperheads	12/10/98	2/18/99
Description				
When connecting a Bi-Lingual socket (new?) to a Beta only socket via a "special" cable, it may be a good thing to support Beta communication at speeds less than S800				
Status				
Open				
NextActions				
Decide: Do we need a bi-lingual socket & plug? Is a beta plug to 6-pin plug cable to be allowed? Jerry to provide interconnectivity write-up and forward to David David to take to plenary AR to define speed limits for bilingual ports.				

SCATNo	Title	Owner	DateEntered	StatusDate
29	"B" Root	David Wooten	2/10/99	2/10/99
Description				
How does one go about making certain a "B" node is TOOT (if a "B" node exists on the bus)				
Status				
WIP				
NextActions				
David will return with a proposal at the next meeting				

SCATNo	Title	Owner	DateEntered	StatusDate
30	B.O.S.S. State machine & Code	Accelerations	2/10/99	2/10/99
Description				
The state machine for B.O.S.S. and the 'C-Code" for B.O.S.S. needs to be completed				
Status				
WIP				
NextActions				
Summary update to be published on February 19th				

SCATNo	Title	Owner	DateEntered	StatusDate
31	B.O.S.S. Border Node	Accelerations	2/10/99	2/10/99
Description				
The behavior and operation of a border node needs to be determined				
Status				
WIP				
NextActions				
Publish a summary on February 19th				

SCATNo	Title	Owner	DateEntered	StatusDate
32	B.O.S.S. Validation	Accelerations	2/10/99	2/10/99
Description				
Validate that B.O.S.S. will support border node functionality				
Status				
WIP				
NextActions				
Publish summary on February 19th				

SCATNo	Title	Owner	DateEntered	StatusDate
33	Beta Only Copper Speed	Colin Whitby-Strevens	2/10/99	2/10/99
Description				
Speed ranges for new Beta copper connector				
Status				
WIP				
NextActions				
Colin will socialize this topic on the reflector - with influence toward "faster is better"				

SCATNo	Title	Owner	DateEntered	StatusDate
34	Max Packet Size - Async	Eric Hannah	3/12/99	3/12/99
Description				
Place into the draft specification the decision to limit asynchronous maximum packet size to 4K bytes				
Status				
OPEN				
NextActions				

SCATNo	Title	Owner	DateEntered	StatusDate
35	Crossover	Upstarts/UTP	3/23/99	
Description				
Upstart algorithm to include crossover resolution				
Status				
OPEN				
NextActions				