

# IEEE P1394b SCAT (Sorted by Number)

SCATNo	Title	Owner	DateEntered	StatusDate
1	1394a start-up Alignment	Upstarts	12/10/98	4/28/99
<b>Description</b>				
Fix start-up to align with P1394a when it has been completed				
<b>Status</b>				
DONE				
<b>NextActions</b>				
4/28/99: Incorporated the Hard Disable bit in the draft; The hard disable bit is set/reset via software; 4/8/99: Deferred until Colin is able to report. 3/24/99: Waiting for 1394a port state machine to stabilize; 2 ) what to do on disable; 3) Is toning operational during disable or is INT_ENABLE used to control toning during disable 4) better distinction between port state machine and arbitration state machine				

SCATNo	Title	Owner	DateEntered	StatusDate
2	Root Contention Time	Eric Hannah	12/10/98	4/28/99
<b>Description</b>				
Verify that root contention time values will work with long haul. The time constants chosen were selected for 50 meters. Beyond 50 meters, new constants will need to be determined.				
<b>Status</b>				
OPEN				
<b>NextActions</b>				
4/28/99: Current draft does not have the changes incorporated. The next draft will contain new values for root contention times for distances less than 50 meters. 4/8/99: Eric will change next draft. ROOT_CONTEND_SLOW will be 3.2 us Min and 3.22 us max.; 3/24/99: Subject to Upstart support, the method suggested in Alistair's e-mail of March 15, 1999 (Subject: Re: Continue of Loop Breaking in 1394b) could be used to resolve root contention. Tentatively, increase the time ur				

SCATNo	Title	Owner	DateEntered	StatusDate
3	PHY Register Map	PHY-Link	12/10/98	2/10/99
<b>Description</b>				
Complete design of register map (to include speed limit bits in port registers - settable by firmware, as well as other required bits)				
<b>Status</b>				
DONE				
<b>NextActions</b>				
None				

SCATNo	Title	Owner	DateEntered	StatusDate
4	Standby Proxy Self-ID	B-Low Power	12/10/98	4/28/99
<b>Description</b>				
Proxy self-ID packets for "leaf" nodes in standby				
<b>Status</b>				
AIP				
<b>NextActions</b>				
4/28/99: Standby is now in the draft; There are some outstanding issues on standby: 1) the draft must now incorporate the notion of proxying the node-id for the leaf node in standby; 2) getting restore information to the restoring leaf node; 4/8/99: Awaiting approval of proposal at April meeting. 3/24/99: Write-up complete. Presentation given at 3/23 plenary. Next draft to include Standby				

SCATNo	Title	Owner	DateEntered	StatusDate
5	Port Interface	B-Port	12/10/98	4/8/99
<b>Description</b>				
Fix & Document Port interfaces (and filtered input from Simulations); Control Codes delivered to Mike and Steve.				
<b>Status</b>				
WIP				
<b>NextActions</b>				
4/8/99: Alistair has 'living' document. Chapter has been extensively revised. Awaiting review by Jerry. Will put something in the next rev. of the spec. 3/24/99: Update as appropriate as information comes forth from simulations				

SCATNo	Title	Owner	DateEntered	StatusDate
6	Signal Detect Plus	UTP	12/10/98	4/8/99
<b>Description</b>				
Resolve signal detect and related issues for UTP-5				
<b>Status</b>				
AIP				
<b>NextActions</b>				
4/8/99: Not yet done. May get done before the next draft of the spec. 3/24/99: No change - Alistair to update contents of next draft				

SCATNo	Title	Owner	DateEntered	StatusDate
7	High Speed Copper Cables	Max Bassler	12/10/98	4/8/99
<b>Description</b>				
Copperheads have verified that an "Enhanced Plug/Socket" 1995 cable functions at S800 for both Data Strobe & Beta Mode. A question exists as to whether a standard plug/cable will function with an enhanced socket at S800 for Beta mode.				
<b>Status</b>				
CLOSED				
<b>NextActions</b>				
4/8/99: Mike make this irrelevant if we approve new connector scheme with Beta-only and Bi-lingual plug. 3/24/99: No longer a need based upon events occurring the 3/23/99 Plenary				

SCATNo	Title	Owner	DateEntered	StatusDate
8	UTP Electrical	UTP	12/10/98	4/8/99
<b>Description</b>				
Complete UTP electrical Specs				
<b>Status</b>				
WIP				
<b>NextActions</b>				
04/08/99: When provided by Alistair, Eric will update spec and mask. 3/24/99 No Change - 2/10/99: Alistair to get numbers, Eric to simulate/validate them				

SCATNo	Title	Owner	DateEntered	StatusDate
9	Jitter Budgets	Colin Whitby-Strevens	12/10/98	4/8/99
<b>Description</b>				
Finalize jitter budget and specifications for all speeds and media (except UTP5).				
<b>Status</b>				
WIP				
<b>NextActions</b>				
4/30/99: Need to decide whether the arb state machine or the port takes responsibility for controlling the scrambler and generating the NOP control symbol in order to emit the SB-JTPAT (as controlled by the "disable scrambler" bit in the PHY register map). 4/8/99 - On Agenda for 4/22/99; 3/24/99 - Colin has posted jitter tables to web site. Folks to validate that numbers are correct. Colin needs to verify test patterns. Will plagiarize from fibre channel test patterns.				

SCATNo	Title	Owner	DateEntered	StatusDate
10	Standby Start-up	Upstarts	12/10/98	4/8/99
<b>Description</b>				
Upstarts complete standby interaction				
<b>Status</b>				
WIP				
<b>NextActions</b>				
4/8/99 - Colin Colin to do implementation proposal upon completion upon completion of SCAT #1				

SCATNo	Title	Owner	DateEntered	StatusDate
11	Standby Codes	B-Port	12/10/98	2/10/99
<b>Description</b>				
Standby encoding signaling				
<b>Status</b>				
DONE				
<b>NextActions</b>				
Codes have been distributed				

SCATNo	Title	Owner	DateEntered	StatusDate
12	Arbitration Tokens	B-Port	12/10/98	3/24/99
<b>Description</b>				
Arbitration Token Completion				
<b>Status</b>				
DONE				
<b>NextActions</b>				
If new codes are needed they will be given or their need will be negotiated				

SCATNo	Title	Owner	DateEntered	StatusDate
13	PHY-Link electrical specs.	Tony Foster	12/10/98	4/28/99
<b>Description</b>				
Include 802.3 spec information for S800 and below; Document integrated link/Beta-PHY; The strategy is:a) reuse the 1394a link-phy electrical if possible, including isolation, up to speed s800 (100Mhz); b) leverage the 802.3Z GMII spec and change signal names to cover un-isolated to S800; c) initial assumption is that speeds above S800 will use integrated link and Phy and the protocol is sufficient.				
<b>Status</b>				
WIP				
<b>NextActions</b>				
4/28/98: Tony plans to finish this up next week and send it off to Eric; David Wooten suggested he socialize it among a few SCAT folks first; 4/8/99: We may recommend adoption of "A" electricals for S800 PHY/Link interface (this may mean giving up on isolation); Tony will have this complete by next Thursday (4/15/99); 3/24/99: Three Actions to be taken: 1) Evaluate the current 1394a link-phy electrical spec and determine if it will suffice or can be edited for use up to S800. If so				

SCATNo	Title	Owner	DateEntered	StatusDate
14	A/B PHY-Link Interoperability	PHY-Link	12/10/98	4/8/99
<b>Description</b>				
A-Link to B-PHY operating model				
<b>Status</b>				
CLOSED				
<b>NextActions</b>				
4/8/99 - If B-PHYs work with A-Links, they must be able to step down to 50 MHz. B-PHY & A-Link is an OPTIONAL implementation. If such an implementation is to be done, then it will be requisite on the implementor to use the A Spec to insure a successful implementation. The B-PHY would then be limited to S400 maximum communication to the link. The PHY can only process legacy formatted packets when running in the "A" compatible mode.				

SCATNo	Title	Owner	DateEntered	StatusDate
15	B.O.S.S. Port & PHY-Link	Accelerations	12/10/98	3/24/99
<b>Description</b>				
Interface with B-Port connections & PHY-Link sub-layer				
<b>Status</b>				
WIP				
<b>NextActions</b>				
Write it up and send it to Sean - to be completed 4/7/99				

SCATNo	Title	Owner	DateEntered	StatusDate
16	Beta mode media dependent speed for new beta & unspecific	Colin Whitby-Strevens	12/10/98	6/6/99
<b>Description</b>				
Shall Beta mode support S100 through S400 on all media types? S100 & S200 not wanted on GOF. The issue is regarding a Bi-lingual PHY capable of only S400 or, perhaps S200, it would be nice if it were able to operate in Beta mode when able.				
<b>Status</b>				
DONE				
<b>NextActions</b>				
4/28/99: Minimum speed for 1394b beta only keyed copper connector shall be S400 (same as GOF); Maximum allowed speed shall be S3200; Other connectors (out of scope for this standard) have a minimum of S400 and maximum is undefined; 3/24/99: Max Bassler is to discover what is the lowest-speed the Consumer Electronic folks would like to see on Beta mode only?				

SCATNo	Title	Owner	DateEntered	StatusDate
17	Beta PHY ID	Jerry Hauck	12/10/98	4/28/99
<b>Description</b>				
Need a mechanism to identify Maximum Legacy Path Speed to be used at self ID time				
<b>Status</b>				
WIP				
<b>NextActions</b>				
4/28/99: The draft spec must be cleaned up such that an "A-Link" interfaced to a "B-PHY" shall not run faster than S400. A beta-only PHY mated with an "A-Link" shall not be faster than S400; 3/24/99: Colin: add a bit identifying connection to a "B" PHY. Alistair: S100 legacy packet format, beta originators will include a speed code, A B PHY generating an S100 packet when connected to an A link will not generate a speed code.				

SCATNo	Title	Owner	DateEntered	StatusDate
18	Beta Copper Connector	Max Bassler	12/10/98	4/28/99
<b>Description</b>				
Two connectors: 1) Lower Cost than existing 4-pin 2) No power; 3) With power. 4) Neither no larger than existing 4-pin 5) More robust than existing 4-pin 6) better EMI/RFI characteristics than current 4-pin 7) S800 and higher 8) LOW COST				
<b>Status</b>				
WIP				
<b>NextActions</b>				
4/28/99: Proposed plug/socket is a bit larger than existing 4-pin connector; Other requirements have yet to be examined; Some members of Copperheads continue believe beta only copper plug/socket requirements have still not been communicated to the Copperheads task group; 3/24/99: Max's AR is to write down Copperhead guidance questions for SCAT group to respond to. Copperheads to respond to constraints listed in this SCAT description.				

SCATNo	Title	Owner	DateEntered	StatusDate
19	Loop Healing	David Wooten	12/10/98	6/6/99
<b>Description</b>				
Changes are needed in Tree-ID such as to support loop healing				
<b>Status</b>				
WIP				
<b>NextActions</b>				
4/30/99: Per e-mail from Colin, the secretary was reminded that the owner for this issue item was transferred to David Wooten at the Plenary; 4/28/99: Ed admits that there never were any real notes (other than a brief list of requirements - which were published) taken of the initial debate of loop healing methods (occurring at Tempe) and, since all those donating to the confusion have not yet brought forward any written (documented) proposal which contains anything of real solution				

SCATNo	Title	Owner	DateEntered	StatusDate
20	Cycle Start Tokens	Michael D. Johas Teener	12/10/98	3/24/99
<b>Description</b>				
Send out a conventional Cycle Start packet appended onto it is a cycle start token. The token is there to help the PHYs keep their phase straight (and if helps on border node) - need two tokens from Alistair (or a better way to do this).				
<b>Status</b>				
DONE				
<b>NextActions</b>				
Mike will get tokens from Alistair. This is expected to be documented by [standard answer: soon] end of next week (2/26). March 1st will be the date when ALL tokens have been requested of Alistair.				

SCATNo	Title	Owner	DateEntered	StatusDate
21	Bridge Liaison	David Wooten	12/10/98	3/24/99
<b>Description</b>				
1394.1 liaison as required				
<b>Status</b>				
OPEN				
<b>NextActions</b>				
Keep on keeping on - this is going to be open until the lights go out on IEEE P1394b or until it is "too damn late" to keep working with 1394.1 because they are taking too long and 1394b needs to close.				

SCATNo	Title	Owner	DateEntered	StatusDate
22	Speed Map	David Wooten	12/10/98	6/6/99
<b>Description</b>				
Resolve speed map issues				
<b>Status</b>				
DONE				
<b>NextActions</b>				
1) Plenary Announcement: 1394b will not support a speed map. 2) Seek volunteer to write a white paper (informative annex) on speed discovery which does use a speed map. Not required for draft closure.				

SCATNo	Title	Owner	DateEntered	StatusDate
23	DC BIAS	Steve Bard	12/10/98	3/24/99
<b>Description</b>				
Complete DC BIAS Specification				
<b>Status</b>				
CLOSED				
<b>NextActions</b>				
4/8/99: No Action Taken; 3/24/99: Steve Bard has requested the contributor of this SCAT item to identify the issue. If no one speaks up, this item will be CLOSED				

SCATNo	Title	Owner	DateEntered	StatusDate
24	IRM	David Wooten	12/10/98	3/24/99
<b>Description</b>				
Changes to IRM (band-width & channels allocation)				
<b>Status</b>				
OPEN				
<b>NextActions</b>				
3/24/99 Same 2/18/99 -It is possible that this has been obsoleted. Peter J. is to write up the proposal from Unibrain. Action on this SCAT to be delayed until Peter's informative write-up can be reviewed. This shall be taken back to the plenary for review and decision.				

SCATNo	Title	Owner	DateEntered	StatusDate
25	PHY Test modes	Lou Fasano	12/10/98	6/6/99
<b>Description</b>				
Develop and document PHY Test Modes				
<b>Status</b>				
WIP				
<b>NextActions</b>				
4/30/99: Per e-mail from Colin, some of this requirement is now met by the jitter test patterns which are in the 0.6 draft. Is more needed?; 4/28/99: Lou will contact Eric Hannah and solicit Eric for a formal proposal; Lou pointed out that the extensions to this SCAT item were advanced a bit with Eric Hannah's suggestion of 4/8/99 - what Eric suggested is not what Dave Johnson (TI) originally had in mind; The question is, do the PHY vendors want to go the extra step and				

SCATNo	Title	Owner	DateEntered	StatusDate
26	Error handling	PHY-Link	12/10/98	6/6/99
<b>Description</b>				
Delayed error detection & reporting (receiving a packet, data looks correct (good CRC), but the data end reveals that it is, in fact, incorrect - a method is needed to report the event across the PHY-Link interface).				
<b>Status</b>				
CLOSED				
<b>NextActions</b>				
4/30/99: Per e-mail from Colin, no action will be taken on this issue item. There will be no such error reporting; 3/4/99: This will be resolved upon completion of SCAT #37				

SCATNo	Title	Owner	DateEntered	StatusDate
27	Summary Clause	David Wooten	12/10/98	3/24/99
<b>Description</b>				
Write clause three (Summary)				
<b>Status</b>				
OPEN				
<b>NextActions</b>				
3/24/99 - No Change. 2/28/99: David will find an owner before the spec closes.				

SCATNo	Title	Owner	DateEntered	StatusDate
28	Beta Speeds Less than S800 on Copper	Copperheads	12/10/98	3/24/99
<b>Description</b>				
When connecting a Bi-Lingual socket (new?) to a Beta only socket via a "special" cable, it may be a good thing to support Beta communication at speeds less than S800				
<b>Status</b>				
DONE				
<b>NextActions</b>				
03/24/99: Yes - a bilingual plug/socket is goodness. It may or may not be good to do beta less than S800. Jerry provided his interconnectivity write-up. David presented at the Plenary at work is under way to ratify bilingual speed limits. If the model understood on 2/18/99 is to be followed, Beta only must start at S400 and go upwards. Discussions at the 3/23/99 plenary may leave the 2/18/99 model no longer valid. There will lbe, however, a lowest common denominator (speed)				

SCATNo	Title	Owner	DateEntered	StatusDate
29	"B" Root	David Wooten	2/10/99	3/24/99
<b>Description</b>				
How does one go about making certain a "B" node is ROOT (if a "B" node exists on the bus). Does it make sense to have a "B" root if there is no "B" link?				
<b>Status</b>				
CLOSED				
<b>NextActions</b>				
The root hold-off bit works fine in a managed bus environment. It is not expected that performance optimizations are needed in an unmanaged bus. The only concern is root hold-off bit "wars." Deferring further action to "Peter Johansson type activity" for any further resolution. 2/10/99: David will return with a proposal at the next meeting				

SCATNo	Title	Owner	DateEntered	StatusDate
30	B.O.S.S. State machine & Code	Accelerations	2/10/99	3/24/99
<b>Description</b>				
The state machine for B.O.S.S. and the 'C-Code" for B.O.S.S. needs to be completed				
<b>Status</b>				
WIP				
<b>NextActions</b>				
'C'-code yet to be done prior to the June plenary meeting. Presentation given at 3/23/99 plenary. Group to review description.				

SCATNo	Title	Owner	DateEntered	StatusDate
31	B.O.S.S. Border Node	Accelerations	2/10/99	3/24/99
<b>Description</b>				
The behavior and operation of a border node needs to be determined				
<b>Status</b>				
WIP				
<b>NextActions</b>				
See next actions in #30.				

SCATNo	Title	Owner	DateEntered	StatusDate
32	B.O.S.S. Validation	Accelerations	2/10/99	3/24/99
<b>Description</b>				
Validate that B.O.S.S. will support border node functionality				
<b>Status</b>				
WIP				
<b>NextActions</b>				
See Next Actions in SCAT #30				

SCATNo	Title	Owner	DateEntered	StatusDate
33	Beta Only Copper Speed	Colin Whitby-Strevens	2/10/99	3/24/99
<b>Description</b>				
Speed ranges for new Beta copper connector				
<b>Status</b>				
CLOSED				
<b>NextActions</b>				
Because this subject has been covered rather adequately in other SCAT entries. 2/10/99: Colin will socialize this topic on the reflector - with influence toward "faster is better"				

SCATNo	Title	Owner	DateEntered	StatusDate
34	Max Packet Size - Async	Eric Hannah	3/12/99	6/6/99
<b>Description</b>				
Place into the draft specification the decision to limit asynchronous maximum packet size to 4K bytes				
<b>Status</b>				
DONE				
<b>NextActions</b>				
04/30/99: Per an e-mail from Colin, this issue item is closed (albeit Eric used the value of 2K instead of 4K - the 4K number is the correct number and the draft spec shall be updated to reflect this correction).; 03/12/99: Eric to include this information in draft 0.18				

SCATNo	Title	Owner	DateEntered	StatusDate
35	Crossover	Upstarts/UTP	3/23/99	3/24/99
<b>Description</b>				
Upstart algorithm to include crossover resolution				
<b>Status</b>				
OPEN				
<b>NextActions</b>				
Colin to prepare an implementation for Upstarts and put it into the spec. draft 0.18				

SCATNo	Title	Owner	DateEntered	StatusDate
36	Fan-out PHY Protocols	PHY-Link	3/24/99	4/8/99
<b>Description</b>				
A PHY may serve as a "port fan-out" device which interconencts with an integrated beta-only PHY & link to in such a fashion to consume a single node ID. Resolve for LinkOn, etc.				
<b>Status</b>				
OPEN				
<b>NextActions</b>				
4/8/99: LinknOn will be sent to the Fan-Out PHY; Proposals: a) Fan-Out PHY includes an optional LinkOn signal pin; b) Node ID's will not be shared (one for each PHY); c) When FOP receives anything that would cause its LinkOn to assert it will assert the LinkOn signal; FOP will deassert LinkOn upon establishing communication over a vendor defined FOP Port (where communication could be as early as the first tone received from that port); 3/24/99: External PHY/Link				

SCATNo	Title	Owner	DateEntered	StatusDate
37	Packet truncation	B-Port (Alistair)	3/24/99	3/24/99
<b>Description</b>				
How do we truncate a packet and when?				
<b>Status</b>				
OPEN				
<b>NextActions</b>				
Continue discussion off line.				

SCATNo	Title	Owner	DateEntered	StatusDate
38	Hard_Disable - Default State	Colin Whitby-Strevens	4/13/99	6/6/99
<b>Description</b>				
Colin's proposal is for the hard-disable flag to be false, so that the behaviour from the software point of view is as close as possible to 1394a. Seeing a point from Nyu-San, there may be a desire to have the default save more power in the				
<b>Status</b>				
AIP				
<b>NextActions</b>				
04/30/99: Per an e-mail from Colin, this issue item is nearly done - all left to reach DONE status is to decide whether to change the default value of the Hard Disable bit.				

SCATNo	Title	Owner	DateEntered	StatusDate
39	Normalize Snnn Terminology	Eric Hannah	4/22/99	4/22/99
<b>Description</b>				
Use of Speed Transmission numbers and designators are inconsistent.				
<b>Status</b>				
OPEN				
<b>NextActions</b>				

SCATNo	Title	Owner	DateEntered	StatusDate
40	Delay after any PHY packet	Alistair Coles	4/28/99	4/28/99
<b>Description</b>				
Because PHY packets are wrapped with an extra quadlet (T-Code E), it is not good to be too speedy with another packet. There needs to be a short delay after a PHY packet to allow for the wrapper to be created and the subsequent packet passed forward.				
<b>Status</b>				
OPEN				
<b>NextActions</b>				
4/28/99: All PHY packets shall be sent with legacy S100 timings. Alistair shall document this requirement in clause 10.				

SCATNo	Title	Owner	DateEntered	StatusDate
41	Interpacket delay	David Wooten	4/28/99	4/28/99
<b>Description</b>				
The link needs time to complete processing of a packet before it must deal with the next packet (isoch followed by isoc, for example); One issue to be dealt with here is how fast can a link deal with a series of packets (isoch, for example); how closely can the packets be spaced so that the link can perform its interpacket processing - how much time must they have? The proposal is a single quadlet.				
<b>Status</b>				
OPEN				
<b>NextActions</b>				

SCATNo	Title	Owner	DateEntered	StatusDate
42	Errant Reset of Error Counter	Alistair Coles	4/28/99	4/28/99
<b>Description</b>				
The error counter is reset when one re-syncs after losing synchronization. Loss of synchronization may be the result of an error. This doesn't sound very useful and probably should be fixed.				
<b>Status</b>				
OPEN				
<b>NextActions</b>				

SCATNo	Title	Owner	DateEntered	StatusDate
43	Loss of Sync	Jerry Hauck	4/28/99	4/28/99
<b>Description</b>				
Jerry Hauck believes there are some open issues with the loss of sync. He requested that this SCAT item be entered as a reminder to him to make certain all corner cases have been examined. His concern is centric to the deafening silence he received in response to his e-mail message on the reflector in which he made a statement that he believed the timeout specified for detection of loss of synch does not function.				
<b>Status</b>				
OPEN				
<b>NextActions</b>				

SCATNo	Title	Owner	DateEntered	StatusDate
44	Beta Cable Construction	Copperheads	4/30/99	4/30/99
<b>Description</b>				
The draft shall include two examples of beta cable construction – one which shows parameter compliance using a short distance cable and one which demonstrates parameter compliance using a longer distance (not long haul) cable. The short distance cable should be as close in length to the predominate cable length available in the 4-pin market today. The shorter cable may be in the neighborhood of 1.6 meters. Another method may be to simply use Kirchoff's law for 28 AWG power				
<b>Status</b>				
OPEN				
<b>NextActions</b>				
04/30/99: Dick Scheel to obtain Sony 1394 cable data (e.g. what cable configurations does Sony make available to the market). Dick will also bring a Sony cable the next SCAT meeting.				

SCATNo	Title	Owner	DateEntered	StatusDate
45	Standby Response when B_Link==0	Colin Whitby-Strevens	4/30/99	4/30/99
<b>Description</b>				
A B-PHY shall always return OK == 0 when receiving a Standby command if B-Link == 0				
<b>Status</b>				
OPEN				
<b>NextActions</b>				
04/30/99: Colin will submit appropriate wording in the next submittal to Eric for inclusion in the next revision of the draft.				

SCATNo	Title	Owner	DateEntered	StatusDate
46	Table 12-11 Modifications	Sean Killeen	4/30/99	4/30/99
<b>Description</b>				
Table 12-11 shall be modified to include restore with and without notification of a previous bus reset. As part of this, any time the PHY sends the contents of an internal register it identifies whether it is autonomous or not (e.g. table 12-11 value 1001 is the same as 1000 except the register being sent is being sent autonomously)				
<b>Status</b>				
OPEN				
<b>NextActions</b>				

SCATNo	Title	Owner	DateEntered	StatusDate
47	Depracation of Max_speed in PHY register MAP	Upstarts	4/30/99	6/6/99
<b>Description</b>				
Upstarting negotiates speed on per port basis. Max_speed is a node number. Why is Max_speed needed in a 1394B PHY?				
<b>Status</b>				
DONE				
<b>NextActions</b>				
06/06/99: Colin has completed his action here (on 4/30/99) and as of 06/06/99 there has been no scream response, therefore, this issue item is DONE - Max_speed has been deprecated; 4/30/99: Deprecate Max_speed in the next revision of the draft. Socialize this effort on the reflector. Wait for "scream" response - if none, leave deprecated. .				

SCATNo	Title	Owner	DateEntered	StatusDate
48	Eliminate Enab_multi & Enab_accel	Sean Killeen	4/30/99	4/30/99
<b>Description</b>				
Is Enab_multi and Enab_accel required in a P1394b PHY register map?				
<b>Status</b>				
OPEN				
<b>NextActions</b>				