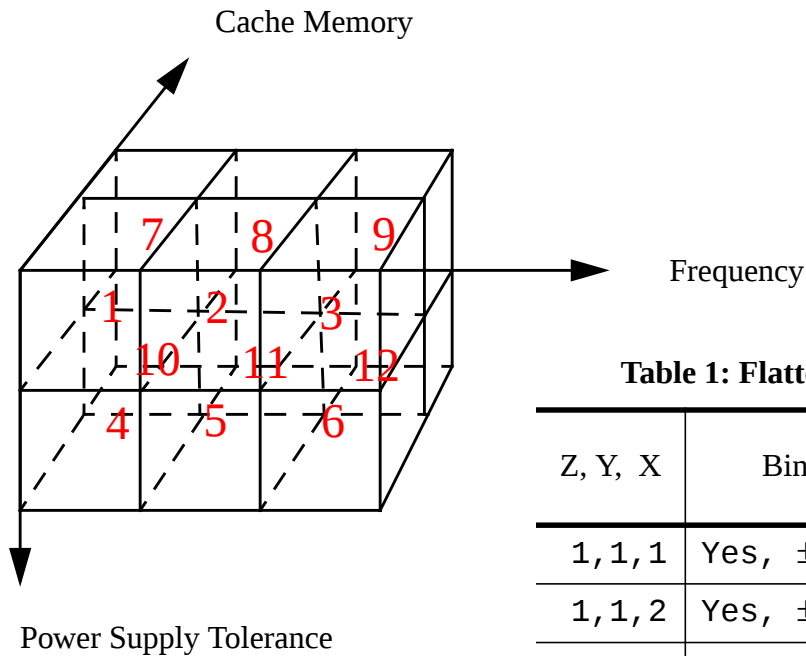


	1	2	3
X. Frequency Bins (lsd)	150Mhz	125Mhz	100Mhz
Y. Power Supply Tolerance Bins	±10%	±5%	
Z. Cache Memory Bins (msd)	Yes	No	

N Arbitrary Length Good Device Classification Axes



N Dimensional Software/Hardware Bin Map

Table 1: Flattened Software/Hardware Bin Map

Z, Y, X	Bin Description	Software Bin	Hardware Bin ^a
1, 1, 1	Yes, ±10%, 150Mhz	1	1
1, 1, 2	Yes, ±10%, 125Mhz	2	2
1, 1, 3	Yes, ±10%, 100Mhz	3	3
1, 2, 1	Yes, ±5%, 150Mhz	4	4
1, 2, 2	Yes, ±5%, 125Mhz	5	5
1, 2, 3	Yes, ±5%, 100Mhz	6	6
2, 1, 1	No, ±10%, 150Mhz	7	7
2, 1, 2	No, ±10%, 125Mhz	8	8
2, 1, 3	No, ±10%, 100Mhz	9	9
2, 2, 1	No, ±5%, 150Mhz	10	10
2, 2, 2	No, ±5%, 125Mhz	11	11
2, 2, 3	No, ±5%, 100Mhz	12	12

a. Shows one-to-one mapping to make cube readable: software bins are sequential, hardware bins can be any positive integer.

Good Device Software to Hardware Bin Map