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1: STIL 1.0 { Design D19; Flow D11; }
2: History {
3:     Title "Example Flow Code";
4:     Ann {* 9/23/2004 - code adapted from Don's modified Stylus doc - June 28, 2004 *}
5: }
6: Variables {
7:     IntegerConstant PASS := 1;
8:     IntegerConstant FAIL := 0;
9: }
10: // ===== BinDefs =====
11: BinDefs {
12:     Bin Pass "PassAll" 1;
13:     Bin Fail "Fail_Opens" 11;
14:     Bin Fail "Fail_Shortcs" 12;
15:     Bin Fail "Fail_Icc" 13;
16:     Bin Fail "Fail_InputLeakage" 14;
17:     Bin Fail "Fail_dcStuckCore" 21;
18:     Bin Fail "Fail_acPathCore" 31;
19:     Bin Fail "Fail_Functional" 41;
20:     Bin Fail "Fail_PLL" 51;
21:     Bin Fail "Fail_Iddq" 15;
22: }
23:
24: // ===== TestMethod =====
25: TestMethod ContactTest {
26:     Out Integer FailOpens;
27:     Out Integer FailShortcs;
28: }
29: TestMethod "Pattern" {
30:     In Signal "SignalRefExpr";
31:     In PatternExec "PatternExec";
32: }
33: TestMethod "SpecContext" {
34:     In Selector "CategorySelectorExpression";
35: }
36: TestMethod "Search" {
37:     In Signal "SignalRefExpr";
38:     In PatternExec "PatternExec";
39:     In Signal "ResourceSignals";
40:     In Real "Search Start";
41:     In Real "Search Stop";
42:     In Real "Upper Limit";
43:     In Real "SearchExpression";
44:     Out Real "ResultVariable";
45: }
46:
47: // ===== TestNode =====
48: TestNode "acPathTests" {
49:     Execute {
50:         "SpecContext" {
51:             "CategorySelectorExpression" Engineering+SpeedGrade;
52:         }
53:         "Search"{
54:             "SignalRefExpr" '_po_-"S0[7]"-"S0[10]'';

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55:      "PatternExec" "acPathExec";
56:      "ResourceSignals" "CLK";
57:      "Search Start" '1/30MHz';
58:      "Search Stop" '1/130MHz';
59:      "Upper Limit" '1/InternalFreq';
60:      "SearchExpression" "acPathDelay";
61:      "ResultVariable" #;
62:  }
63: }
64: PostActions {
65:   If ??? { Exit PASS; }
66:   Else { Exit FAIL; }
67: }
68: }
69: TestNode "Start" {
70:   Title "Segment <>";
71:   Position 120,80;
72:   Execute { ConnectResources; }
73:   PostActions { Else { Exit PASS; }}
74: }
75: TestNode "Finish" {
76:   Title "Segment <>";
77:   Position 120,80;
78:   Execute { RemoveResources; }
79:   PostActions { Else { Exit PASS; }}
80: }
81: TestNode "Load" {
82:   Title "Spec Selection";
83:   Position 120,80;
84:   Execute {
85:     "SpecContext" {"CategorySelectorExpression" Engineering+SpeedGrade;}
86:   }
87:   PostActions { Else { Exit PASS; }}
88: }
89: TestNode "ConnectResources" { }
90: TestNode "Contact" { }
91: TestNode "RemoveResources" { }
92: TestNode "Load_dcFault1" { }
93: TestNode "LeakageTests" { }
94: TestNode "Device_2" { }
95: TestNode "Device_5" { }
96: TestNode "Device_4" { }
97: TestNode "Device_1" { }
98: TestNode "Device_6" { }
99: TestNode "Device_3" { }
100: TestNode "FunctionalTests" { }
101: TestNode "PllTests" { }
102: TestNode "IddqTest" { }
103:
104: // ===== TestFlow =====
105: TestFlow "Main" {
106:   TestNode N11 {
107:     Title "Opens / Shorts Test";
108:     Position 120,80;

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109:     Execute {ContactTest;}    // test-method
110:     PostActions {
111:         If ContactTest.FailOpens { Bin 11; Exit; }
112:         If ContactTest.FailShorts { Bin 12; Exit; }
113:         Else { Next N7; }
114:     }
115: }
116: TestNode N7 {
117:     Title "Device Programming";
118:     Position 360,80;
119:     Execute {"DeviceConfig";}    // test-node
120:     PostActions {
121:         Else {Next T14;}
122:     }
123: }
124: TestNode N14 {
125:     Title "Leakage Tests";
126:     Position 200,200;
127:     Execute { LeakageTests; }
128:     PostActions {
129:         If (LeakageTests == FAIL) { Bin 14; Exit FAIL; }
130:         Else { Next N21; }
131:     }
132: }
133: TestNode N21 {
134:     Title "DC Stuck At Tests";
135:     Position 120,320;
136:     Execute { "Pattern" {    // test-method
137:         "SignalRefExpr" := "_so";
138:         "PatternExec" := "dcStuckAtExec";
139:     }
140:     PostActions {
141:         If "Pattern" == FAIL { Bin 21; Exit FAIL; }
142:         Else { Next N31; }
143:     }
144: }
145: TestNode N31 {
146:     Title "Path Delay Tests";
147:     Position 320,320;
148:     Execute { "acPathTests"; }
149:     PostActions {
150:         If "acPathTests" { Bin 31; Exit FAIL; }
151:         Else { Next N51; }
152:     }
153: }
154: TestNode N41 { }
155: TestNode N51 { }
156: TestNode N15 { }
157: }
158:
159: TestFlow "DeviceConfig" {
160:     TestNode N101 { }
161:     TestNode N102 { }
162:     TestNode N103 { }

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163:   TestNode N104 { }
164:   TestNode N105 { }
165:   TestNode N106 { }
166: }
167:
168: // ===== TestProgram =====
169: TestProgram "Inovys Example Flow" {
170:   Device "Mystery Device";
171:   Position 100 100;
172:   OnLoad "Start";
173:   OnInitFlow "Load";
174:   OnStart "Main";
175:   OnPowerDown "Finish";
176: }
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