

## Status Report

# P1500 Documentation Task Force

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IEEE P1500 WG Meeting  
International Test Conference (ITC'02)  
October 7, 2002 - Baltimore, MD, USA

# Task Force Organization

- **Mission**

Produce first full draft document of P1500 standard for non-merged digital logic and memory cores

- **Target**

Document progress of technical Task Forces with at most one WG meeting delay

- **Members**

- Rohit Kapur (Synopsys)
- Erik Jan Marinissen\* (Philips Research)
- Nilanjan Mukherjee (Mentor Graphics)
- Mike Ricchetti (Intellitech)
- Jon Udell (Mentor Graphics)

- **Meetings**

- Monthly teleconference meetings of 1.5 hours  
Thanks to Mentor Graphics (Nilanjan) for providing facilities!
- Password-protected web site at IEEE computer

# Draft Releases

- P1500/D0.1 released January 31, 2000
- P1500/D0.2 released May 5, 2000 (VTS'00)
- P1500/D0.3 released January 4, 2001
- P1500/D0.4 released June 15, 2001 (DAC'01)
- P1500/D0.5 released October 29, 2001 (ITC'01)
- P1500/D0.6 released October 2002 (ITC'02)

## Distribution

- PDF document available at CTAG and CTL-TF password-protected web sites
- Selected 'experts' (see under Review)

# IEEE P1500/D0.6 Disclaimers

- This is an unapproved draft of a proposed IEEE standard
- All text is preliminary and subject to change
- Certain sections are still empty
- This document cannot be well understood without proper introduction

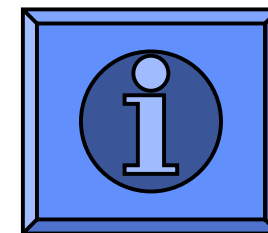
In order not to create misunderstanding for those previously not involved in IEEE P1500 or for those unaware of the current status of IEEE P1500, we do not yet release this document to the general public!

# P1500/D0.6 Changes

- First text in ‘Compliance Definition’ section
  - Section to be renamed ‘Information Model’?
- Technical updates in all sections of document
- Handling of editorial review comments
  
- Many last-minute changes made at Task Force meetings here at ITC’02
- Release of document in October 2002

# Reviewers P1500/D0.5

- **External (P1500/D0.4)**
  - Samy Makar (Transmeta + VSIA) done
  - Greg Maston (Synopsys + IEEE1450) done
- **External (P1500/D0.5)**
  - Ben Bennetts (Bennetts Associates) done
  - Young Wha Oh (Lumictech) not done
  - Bob Stevenson (CMC) done
  - Prab Varma (Veritable) done
  - Rob Aitken (Agilent Technologies) not done
- **Internal**
  - Saman Adham (LogicVision)
  - Douglas Kay (Cisco Systems)



# Expected for P1500/D0.7

- DocTF will start writing introductory texts
- Work on long-standing action items on terminology
  - Section 3.1: Definitions
  - Section 3.2: Acronyms
  - Appendix A: Glossary
- Technical improvements from various CTAG Tiger Teams
- Technical improvements by CD-TF
- Improvements based on Reviewers Comments