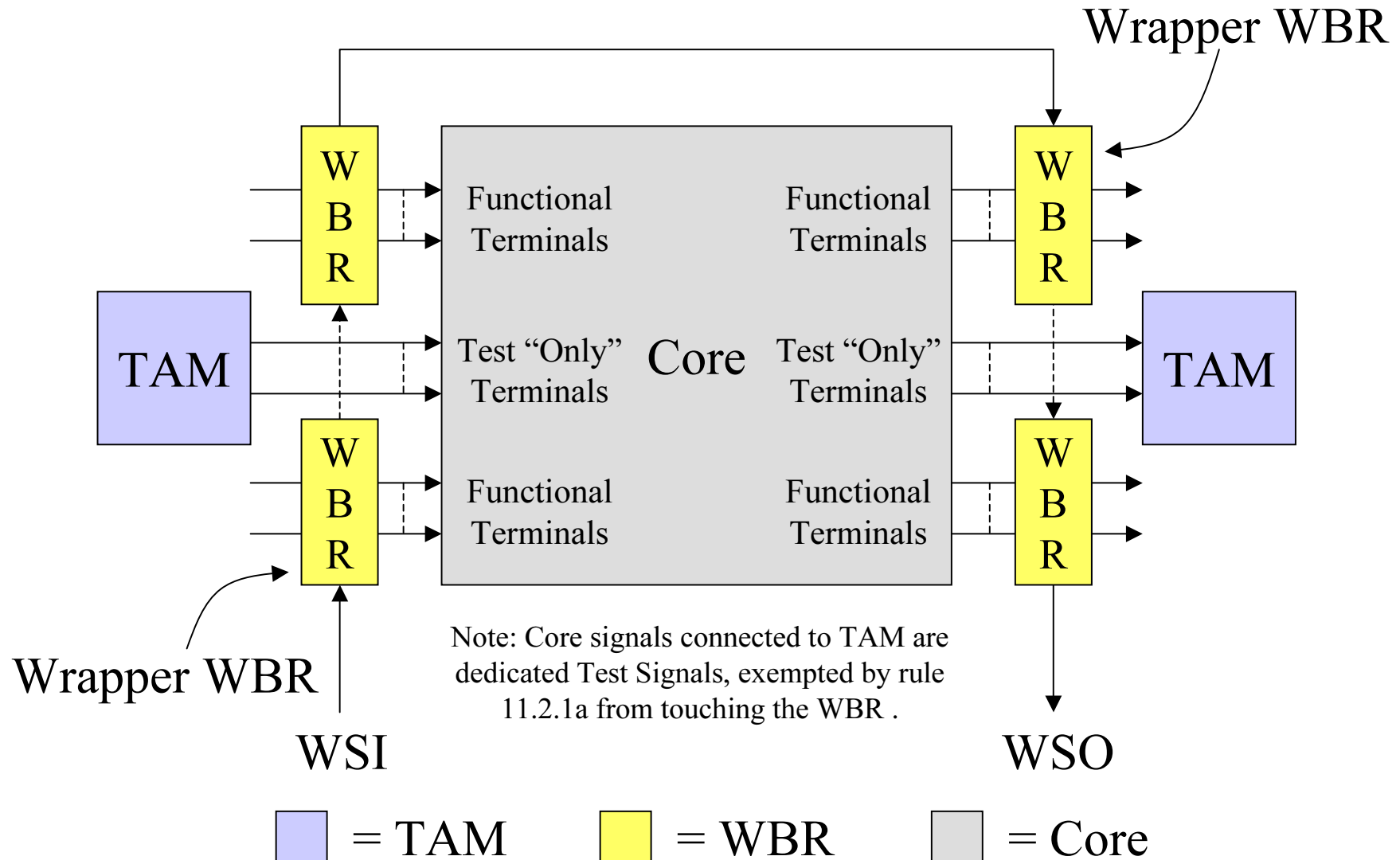


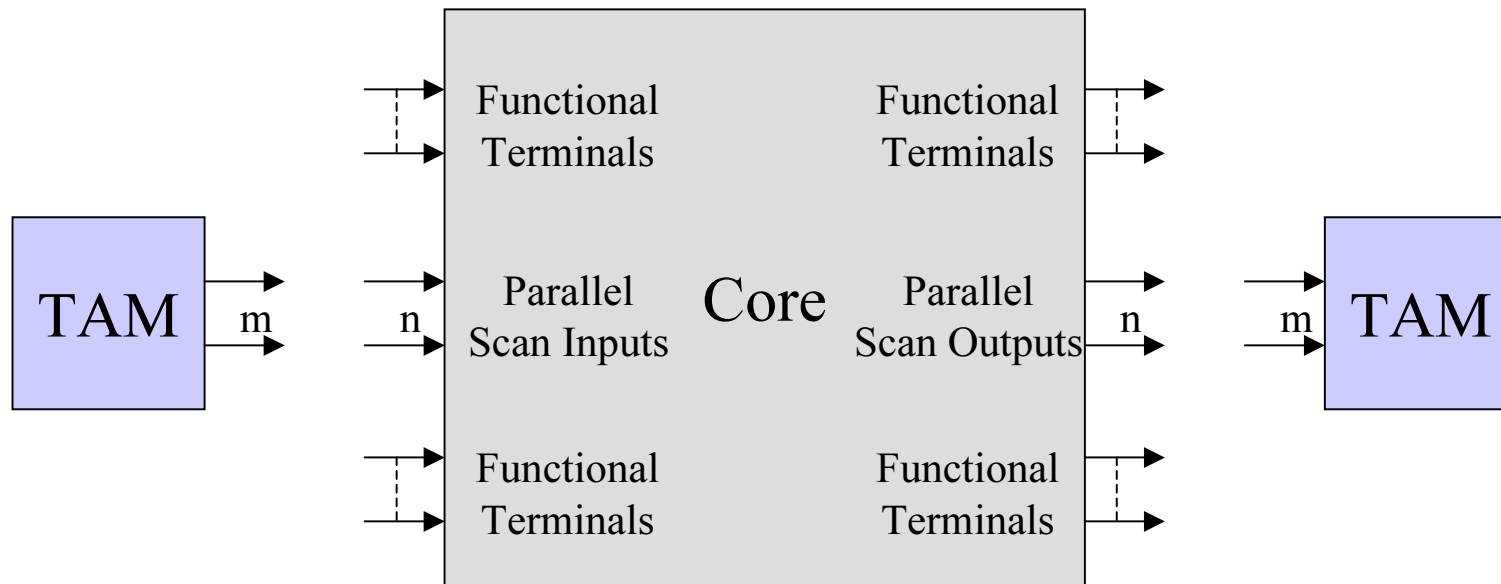
Wrapper Model # 1

Direct Core to TAM Harnessing



Wrapper Model # 2

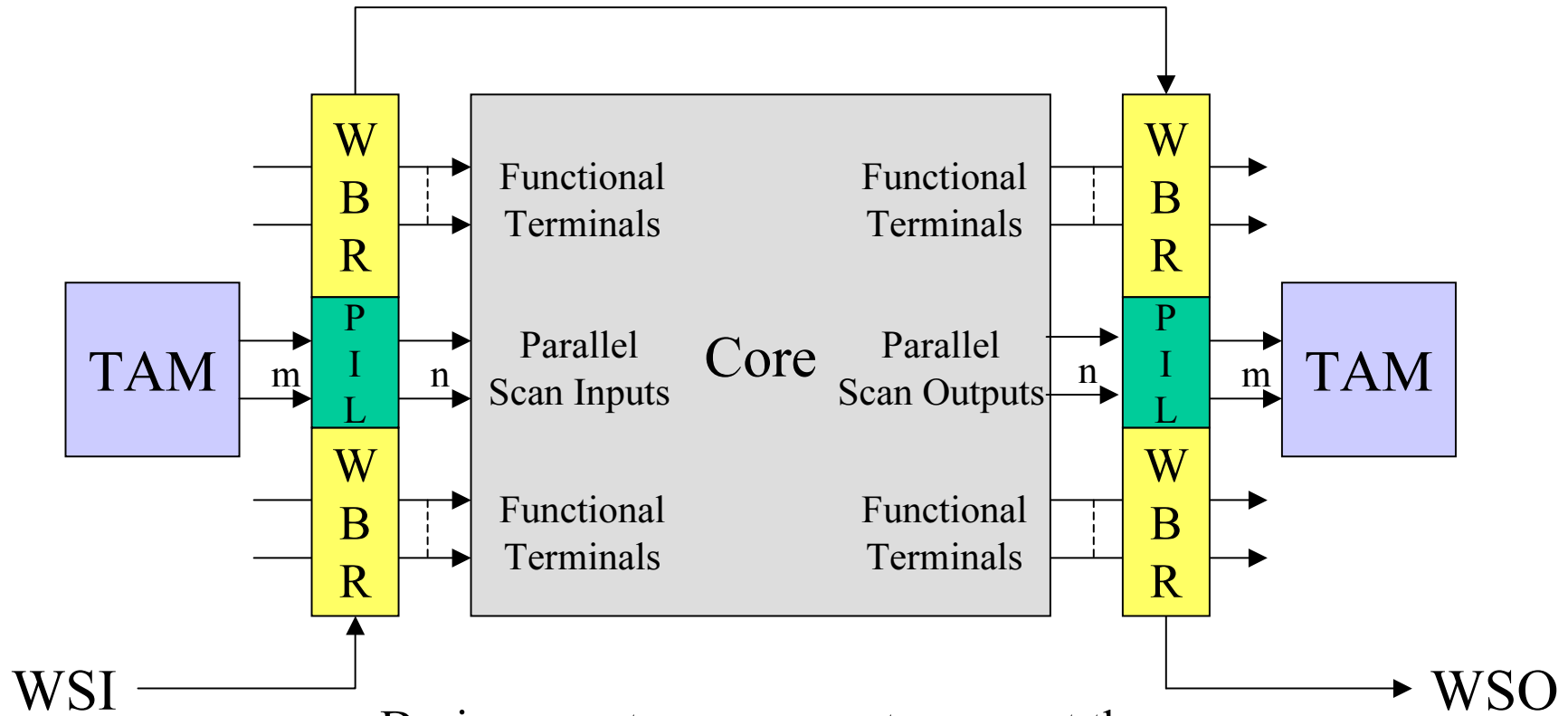
Wrapper adapts available TAM to Core Test Requirements



Given a TAM with an m input and output channels and a core which is designed to be tested with n scan chains...

Wrapper Model # 2

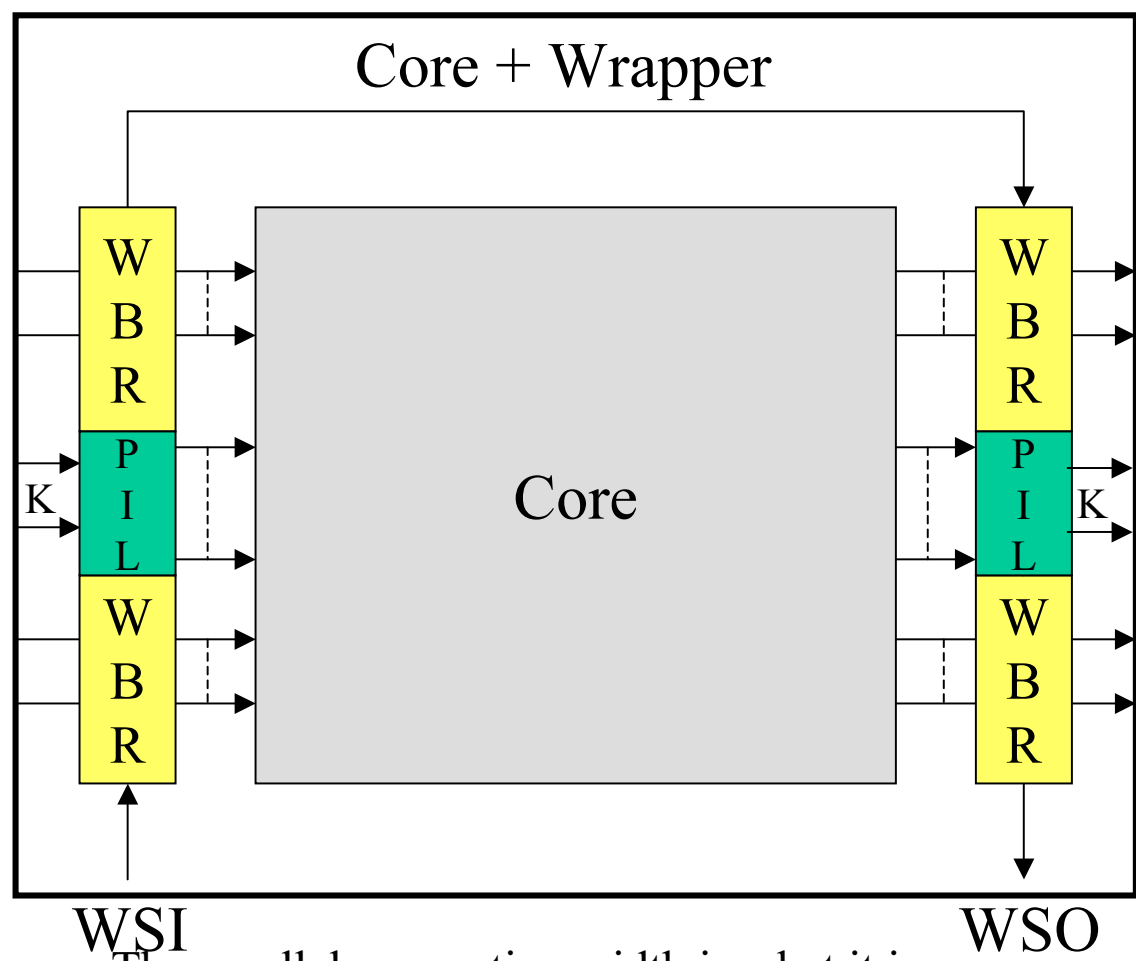
Wrapper adapts available TAM to
Core Test Requirements



Design a custom wrapper to convert the
disparate channel widths.

Wrapper Model #3

Wrapped Core with Predetermined Parallel Connection Width



The parallel connection width is what it is.
(Width adaptation, if necessary, is part of SoC integration.)