

# CALL FOR PARTICIPATION

## 3rd IEEE International Workshop on Testing Embedded Core-based System-Chips

Marriott Laguna Cliffs Resort, Dana Point, California  
April 28-29, 1999

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After two successful workshops held in conjunction with ITC/Test Week in 1997 and 1998, TECS 99 moves out of Washington to colocate with VTS and be closer to the IC industry in silicon valley.

As the use of embedded cores is growing in today's ICs, the complexity of creating an effective test for the individual cores and system-on-chip keeps increasing. TECS is the only workshop to dedicate its entire program to the emerging trends and methods in testing embedded core-based system-chips. TECS brings together the IP core creators, core users, EDA providers, system-on-chip manufacturers and ATE providers.

TECS 99 starts with a keynote address by Rene Segers, Philips Semiconductors, entitled "Philips' experiences in core-based design and test of system ICs". The five paper sessions at TECS 99 are comprised of presentations followed by discussion panels to conclude the sessions with roadmaps and recommendations. The papers cover actual experiences, innovative solutions, and common efforts such as the report from the VSIA Manufacturing Test DWG. TECS 99 will also have a full length panel session (co-organized with IEEE Design & Test of Computers) on "CAD Tools for Core Testing" followed by tool demos. The panelists represent EDA tool vendors, users, and internal tool providers.

The TECS 99 audience is composed of test engineers, designers, managers and researchers who are interested in learning about the state-of-the-art in testing complex system-on-chips. TECS 99 will start at 4:00pm on April 28 and end at 4:30pm on April 29th, 1999.

All TECS 99 participants require registration which includes workshop technical sessions, workshop informal proceedings, the evening reception, break refreshments, breakfast and lunch. Register using either the VTS 99 registration form or the registration form below.

TECS 99 is sponsored by the IEEE Computer Society Test Technology Technical Council and is in cooperation with VSIA (Virtual Socket Interface Alliance).

For more information see "<http://grouper.ieee.org/groups/1500/tecs/99/>" or contact the General Chair.

Early Registration Discount Form (available until April 12th)

Mail or fax form to: IEEE TECS 99, 1474 Freeman Dr., Amissville, VA 20106, USA

Tel: +1-540-937-8280, Fax: +1-540-937-3739, Email: [ttc@computer.org](mailto:ttc@computer.org)

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IEEE/Computer Society Member No\*: \_\_\_\_\_

Discount rate (check one):

- IEEE/CS Member\*: \$210 (normal rate is \$260)  
 Student Member\*: \$100 (normal rate is \$110)  
 Non-member: \$260 (normal rate is \$320)

\* IEEE/CS member rates granted only if current valid membership number filled in above.  
Student member rate granted only if photocopy of IEEE Student Member card enclosed.

**Include full payment in \$US with this form.**

Use a check or one of the credit cards listed at the right. Make checks payable to "TECS 99".

**Checks must be drawn on a US bank.**

Advance registration fees will be refunded only on written request, mailed or faxed, and received by April 12th. A \$50 processing fee will be charged for each refund.

Complete this section for credit card payment. Card type:  Visa  Mastercard  
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