

## Status Report

# P1500 Documentation Task Force

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VTS-2000 - Montreal, Canada  
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P1500 Documentation Task Force

# Presentation Outline

1. Task Force Organization
2. IEEE P1500/D0.1
3. Recent Achievements
  - Review Results
  - Revision Table of Contents
  - Release of IEEE P1500/D0.2
4. DocTF Continues...

# 1. Task Force Organization

- **Mission**  
Produce first full draft document of P1500 standard for non-merged digital logic and memory cores
- **Target**  
Document progress of technical Task Forces with at most one WG meeting delay
- **Members**
  - Erik Jan Marinissen\* (Philips Research) – Tony Taylor (Synopsys)
  - Nilanjan Mukherjee (Mentor Graphics) – Jon Udell (Palmchip)
  - Mike Ricchetti (Intellitech) – Alex Zamfirescu (ASC)
- **Meetings**
  - Biweekly teleconference meetings of 1.5 hours  
Thanks to Mentor Graphics (Nilanjan) for providing facilities!
  - Password-protected web site at IEEE computer

## P1500 Documentation Task Force

# 2. IEEE P1500/D0.1

- Released in P1500 WG meeting of January 31, 2000
- Captures status of P1500 in January 2000
  - Based on contributions from technical Task Forces
  - As much as possible in line with IEEE Style Manual
  - Used IEEE Draft Standard template for MS-Word97
- Distributed as PDF file per e-mail in February 2000 to active P1500 members who enlisted as reviewer
  - All text is preliminary and subject to change
  - Cannot be well understood without proper introduction
  - Hence, no release to general public yet!

## P1500 Documentation Task Force

# Table of Contents P1500/D0.1

Title, Abstract, Key Words

Introduction, Participants,  
Acknowledgements, Table of Contents

1. Overview, Scope, Purpose

2. References

3. Definitions, Acronyms and  
Abbreviations

4. Structure of This Standard

5. Dual Compliance Levels

6. Scalable Architecture Hardware

6.1 Overview

6.2 Wrapper Instruction Register (WIR)

6.3 WIR Instructions

6.4 Wrapper Cells

7. Core Test Language

7.1 Introduction

7.2 Process/Data Flow

7.3 The STIL Data Model

7.4 STIL Extensions

7.5 Environment

7.6 CTL Environment Statements

7.7 Examples

7.8 Use of Basic STIL Language for Core  
Applications

7.9 STIL-A Extensions of Interest to Cores

8. IEEE 1500 Compliance Level 1

9. IEEE 1500 Compliance Level 2

Annex A: Glossary

Annex B: Application Examples

## 3. Recent Achievements

- A. Sent our P1500/D0.1 to enlisted reviewers  
Received back reviewers comments  
Handled reviewers comments
  
- B. Established improved Table of Contents
  
- C. Relative little new technical material
  - Decided to postpone release of P1500/D0.2 from March 2000 (DATE) to May 2000 (VTS)

## 3A. Review Results

- Several reviewers sent in valid and useful comments about D0.1 - Thanks for that!
- DocTF members did reviews themselves as well
- Substantial improvements in
  - 6.3 : WIR Instructions
  - 7 : Core Test Language
  - Annex A: Glossary

# 3B. Revision Table of Contents

- Document divided into four major ‘parts’
  1. Introduction
  2. Core Test Language
  3. Scalable Architecture Hardware
  4. Compliancy Definitions
- Every part can have dedicated introductory texts
- Section numbering and page numbering restart in every part
- Benefits
  - Better organization
  - Easier for reader to find his way in the document
  - Easier to maintain for future revisions of standard

# P1500 Documentation Task Force

# Revised Table of Contents

Title, Abstract, Key Words

Introduction, Participants, Acknowledgements,  
Table of Contents

## Part 1: Introduction

1. Overview, Scope, Purpose
2. References
3. Definitions, Acronyms and Abbreviations
4. Structure of This Standard
5. Dual Compliance Levels

## Part 2: Core Test Language

1. Introduction
2. Process/Data Flow
3. Tutorial on CTL Capabilities
4. Tutorial on Test Pattern Reuse, Using STIL
5. Example of Test Data Mapping
6. The CTL Data Model

7. Environment

8. CTL Statements

9. CTL Code Examples

10. Example of Complete CTL Program

11. Use of Basic STIL Language for Core Applications

## Part 3: Scalable Architecture Hardware

1. Overview
2. Wrapper Instruction Register
3. WIR Instructions
4. Wrapper Boundary Register

## Part 4: Compliancy Definitions

1. IEEE 1500 Compliance Level 1
2. IEEE 1500 Compliance Level 2

Appendices

A: Glossary

B: Application Examples

## 3C. Release P1500/D0.2

- Originally, release of D0.2 planned for DATE-2000
- Release of D0.2 delayed until VTS-2000
  - Insufficient new content from technical Task Forces
  - Insufficient time of DocTF members
  - Difficulties with MS-Word97
- Rather a substantial improvement at VTS than a release at DATE ‘just because it was promised’

# IEEE P1500/D0.2 Disclaimers

- This is an unapproved draft of a proposed IEEE standard
- All text is preliminary and subject to change
- Certain sections are still empty
- This document cannot be well understood without proper introduction

In order not to create misunderstanding for those previously not involved in IEEE P1500 or for those unaware of the current status of IEEE P1500, we do not yet release this document to the general public!

# How to Obtain/Review D0.2?

## Obtain

- A PDF version of D0.2 will be placed on the password-protected Task Force web sites

## Review

- If you have comments about D0.2, send them by e-mail to [Erik.Jan.Marinissen@philips.com](mailto:Erik.Jan.Marinissen@philips.com)

## 4. DocTF Continues...

- Continue biweekly teleconference meetings
- 'To Do' list:
  - Writing of (currently missing) introductory texts
  - Acquisition of missing technical texts from technical Task Forces
  - Taking care of reviewer comments
  - Traditional editing work: typos, rewording, figures, etc.
- Release of P1500/D0.3 at an upcoming Working Group meeting: June (DAC) or August 2000

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