

CTAG Update

Status Since DATE 2000

❑ Wrapper Serial Interface Layer (SIL) has been stable

● Some on-going discussions:

- ✓ General WIP timing and WIP Plug-n-Play w/WBR
- ✓ Better WART definition (e.g., auxiliary clocks/signals required for shared cell P-n-P)
- ✓ SAFESTATE vs. WCLAMP/WPRELOAD
- ✓ IDDQ Instruction

❑ Wrapper Parallel Interface Layer (PIL) being specified

- PIL addresses various Wrapper TAM interface types
- Reviewing earlier TAM discussions and working toward a specification

❑ Key focus is to complete CTAG doc sections and review

- Tiger teams have updated and revised their sections for D0.4 Draft
 - ✓ WIR Tiger team, sections 3, 4 and 7
 - ✓ Instruction Tiger Team, section 5
 - ✓ WBR Tiger Team, section 6