

# **P1687 Working Group Activity**

# Overview

## □ Problem

- Diverse set of DFT structures in a SoC
- Possibly from multiple vendors
- Limited portability of off-the-shelf DFT IP

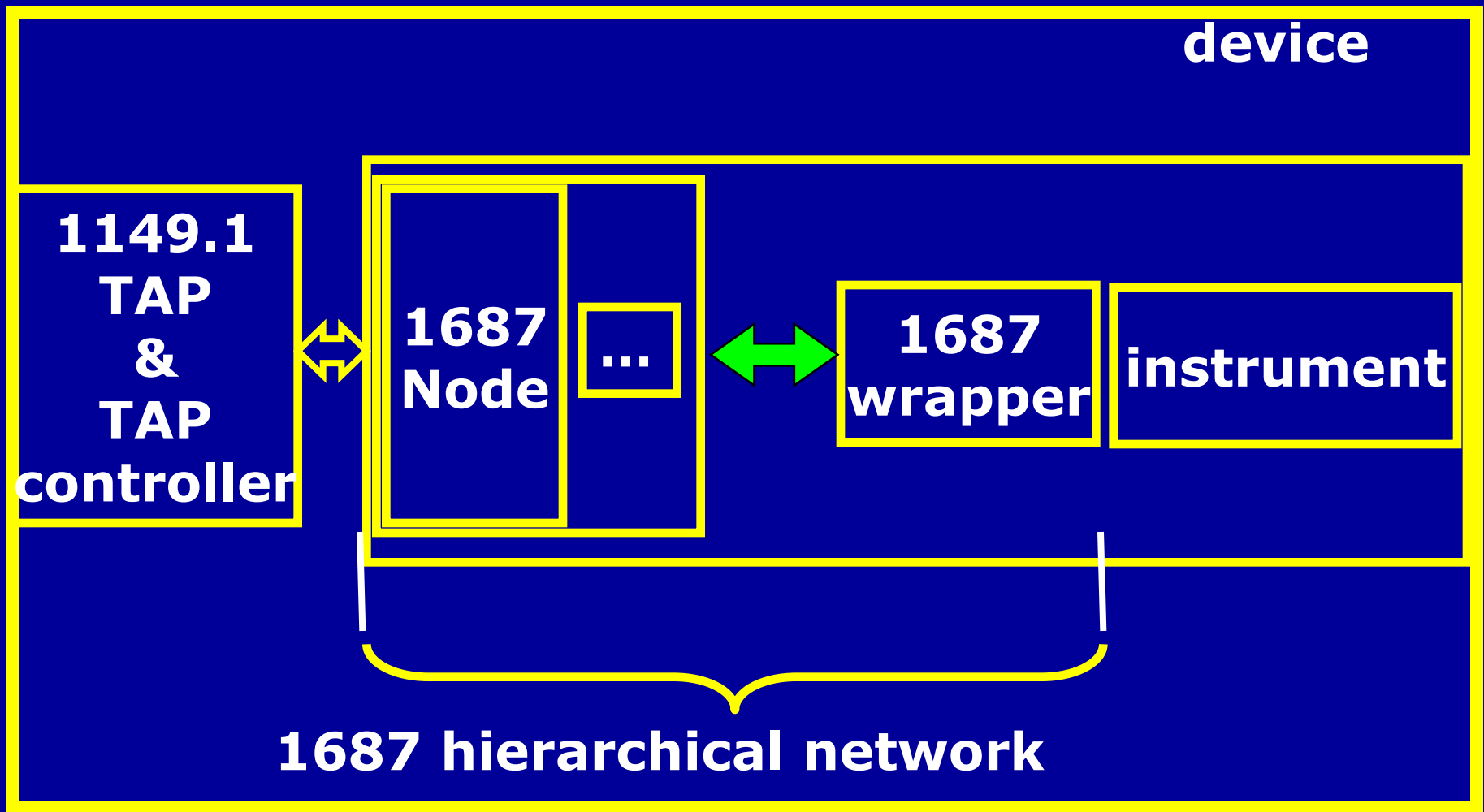
## □ Solution-P1687

- Provides a method to describe access mechanism for instruments
- Standard incorporates hardware and software description of interface
- Instrument details hidden from the user

## □ Enables greater portability of instruments across IP vendors, semiconductor companies, and EDA vendors

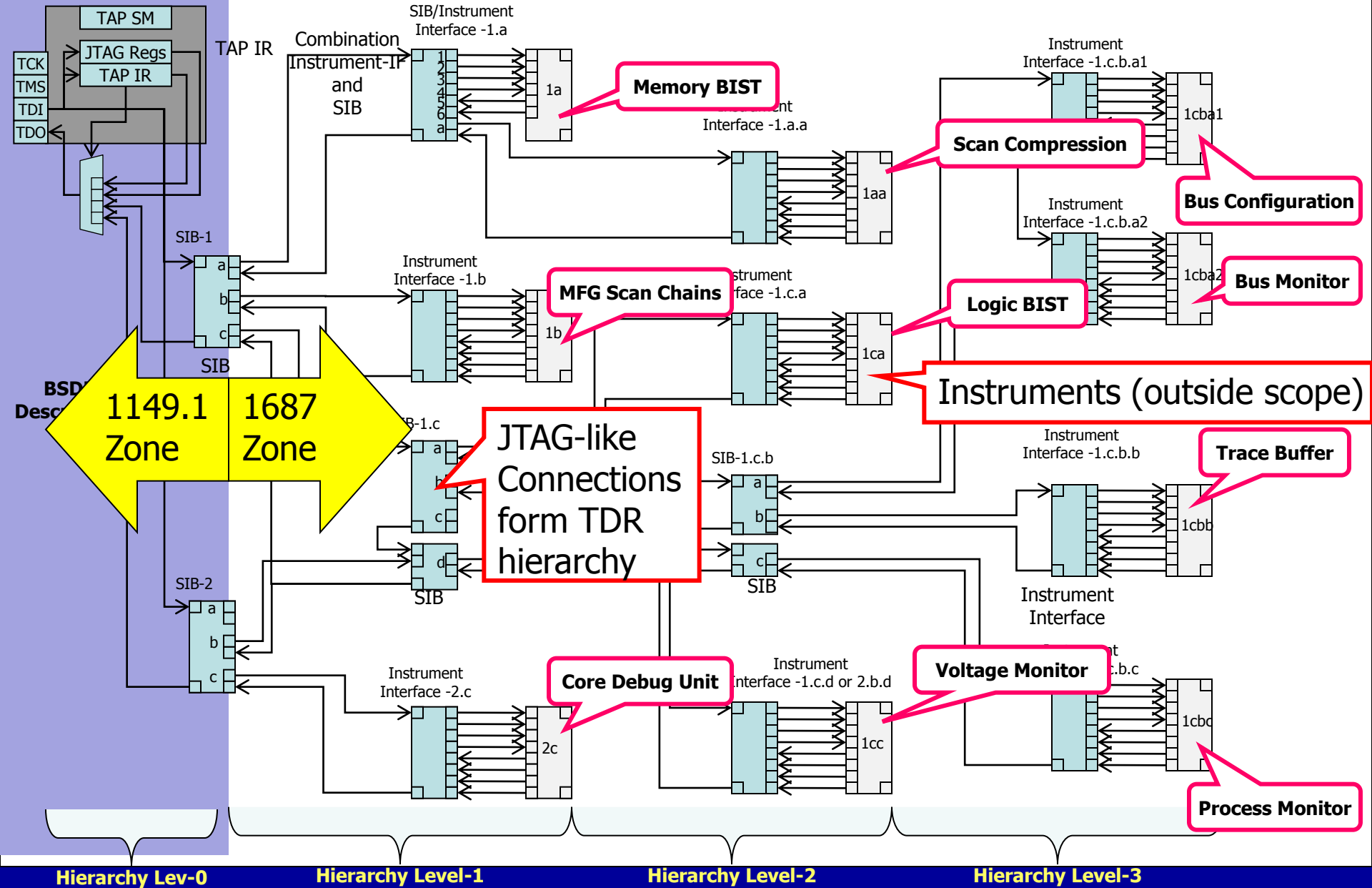
- Portability achieved because of standardized languages to describe operation of instruments

# P1687 Context



HDL describes the 1687 network  
PDL describes the procedures

# Example 1687 Network



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