



# IEEE 1722.1 Corrigendum Feb 2014

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## 2 Normative Reference

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- The normative reference for IIDC2 isn't in the list (and the one in the bibliography is wrong [1.31])

## 6.5.2.3 Advertise Interface State Machine

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- Advertising Entity State Machine and Advertising Interface State Machine both update the `available_index`.
  - Only the Advertising Entity State Machine should be doing it
  - Remove the `entityInfo.available_index += 1` in the `WAITING` state
- `doAdvertise` needs to be set to `false` in both `INITIALIZE` and `ADVERTISE`
- remove setting of `rcvdDiscover = FALSE` in `WAITING`

## 7.2.15 INTERNAL\_PORT\_\* Descriptors

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- Table 7.26 (the INTERNAL\_PORT\_INPUT and INTERNAL\_PORT\_OUTPUT Descriptor) has incorrect description for **block\_latency** field
  - EXTERNAL\_PORT\_\* should be INTERNAL\_PORT\_\*

## 7.2.10 Memory Object

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- Table 7.18 needs to state memory object lengths (maximum\_length and length) are in octets.

- Thanks Andy @ XMOS

## 7.2.17.1 Video Cluster

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- Need a value for IIDC (missing)
- Need to update to changes in IEEE P1722a subtype and format definitions

## 7.3.1 Sampling Rates

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- IIDC Frame Rates can't be represented
  - Add Pull 5, multiply base frequency by  $1/8$  (or divide by 8)

## 7.3.2.1 Stream Formats

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- IIDC needs to be changed to work with IIDC2
  - Define an enum for formats (eg VGA\_Mono8, VGA\_RGB8Packed)
- Need to update to changes in IEEE P1722a subtype definitions



## 7.3.10 Video Cluster Color Space

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- Need to add color spaces
  - 32 bit ARGB (8 bit Alpha, Red, Green and Blue)
  - 32 bit BGRA (8 bit Blue, Green, Red and Alpha)
  - 4:2:0 video range and full range

# 7.4.42 GET\_COUNTERS

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- 7.4.42.1 and 7.4.42.2
  - “**descriptor\_type** is set to either ENTITY, AVB\_INTERFACE, CLOCK\_SOURCE or STREAM\_INPUT.”
    - This is incorrect as there are no counters defined for CLOCK\_SOURCE, CLOCK\_SOURCE should be CLOCK\_DOMAIN
  - “**descriptor\_type** is set to either ENTITY, AVB\_INTERFACE, CLOCK\_DOMAIN or STREAM\_INPUT.”

## 7.4.15 SET\_STREAM\_INFO

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- Table 7.129 is out of sync with Table 8.3
  - bit 27 should be SUPPORTS\_ENCRYPTED
  - bit 26 should be ENCRYPTED\_PDU
  - bit 25 should be TALKER\_FAILED

## 7.4.27 & 7.4.28 INCREMENT/DECREMENT

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- For CONTROL\_SELECTOR\_\* types the number\_of\_values is actually the number of options. Two possible fixes:
  - Change INCREMENT\_CONTROL and DECREMENT\_CONTROL command text to specify that index\_count is number\_of\_values field value for CONTROL\_LINEAR\_\* and CONTROL\_ARRAY\_\* or it is 1 for all other types
  - Deprecate CONTROL\_SELECTOR\_\* type and replace with CONTROL\_NEW\_SELECTOR\_\* types which include a number\_of\_options field and then specify number\_of\_values correctly

## 7.4.42.2 GET\_COUNTERS

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- Table 7.137 and Table 7.140 have ENTITY\_SPECIFIC\_N in the wrong order
  - Symbols are in reversed order compared to other counter groups.

## 8.2.1.17 flags field

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- Add a note or text which clarifies that the `FAST_CONNECT` flag is set by the Listener in the `CONNECT_TX` command to indicate that it is sending the `CONNECT_TX` command because it is restoring from saved state (Fast Connect mode) and not because it is acting in response to receiving a `CONNECT_RX` command

# D Memory Object Uploads

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- Memory Object Upload Protocol needs to be broken into segments for devices which can't fit whole image into RAM
  - MEMORY\_OBJECT descriptor will have another field `maximum_segment_length`
  - UPLOAD operation will have 2 values, the length being written and the segment size being used
  - Annex D state machines will be tweaked to work in segments while attempting to maintain compatibility with a single segment download working in the same way as it does now

# Your Issues Here

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- Do you have any more issues?
- Please email them to Ashley and Jeff or the mailing list and I will add them