5.8 Time-aware station possibilities

NOTE—Recent 802.1AS meetings helped clarify the functionality of 802.1AS stations. DVJ has attempted to summarize these findings, in the remainder of this subclause, in response to the 2007-09-18 request. This is highly preliminary text and subject to change.

5.8.1 Time-aware end-station possibilities

A time-aware end station can have combinations of ClockSource and ClockTarget components, as illustrated in Figure 5.14. A GPS receiver could have access to an application-level ClockSource resource, as illustrated in Figure 5.14a. An inexpensive audio speaker could have access to an application-level ClockTarget resource, from which the D/A converter clock is derived, as illustrated in Figure 5.14b. A television or stereo receiver could provide both of application-level ClockSource and ClockTarget components, as illustrated in Figure 5.14c and Figure 5.14d.

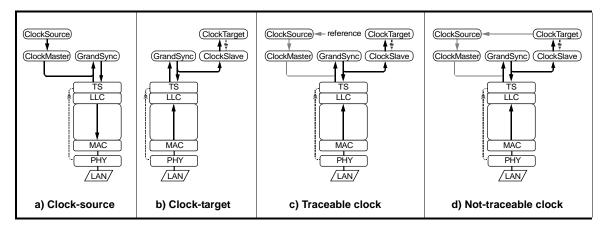


Figure 5.14—Time-aware end station possibilities

A traceable ClockSource/ClockTarget station uses an external reference to drive its ClockSource entity, as illustrated in Figure 5.14c. A VCR that derives its time from the sideband PBS signal is one such example. Although its ClockSource is unaffected by the observed network time, its application clock is expected to be derived from the network-supplied clock, so as to minimize timing drifts and errors between stations when another station has been selected to become the grandmaster.

When not the grandmaster, a non-traceable ClockSource/ClockTarget station sets it ClockSource clock reference based on the time observed at its ClockTarget, as illustrated in Figure 5.14d. A clock radio is one such example. When (and if) this station becomes the grandmaster, its now free-running ClockSource entity provides network time.

5.8.2 Time-aware bridge possibilities

Within the consumer environment, wiring constraints could be simplified by allowing devices to be cascaded. Consumer devices could thus include bridge-relay *and* end-station capabilities, as illustrated in Figure 5.15. Any of the ClockSource, ClockTarget, and ClockSource/ClockTarget combinations are possible, as illustrated in Figure 5.15a, Figure 5.15b, and Figure 5.15c respectively. A time-aware bridge with neither ClockSource or ClockTarget capabilities (not illustrated) is also possible.

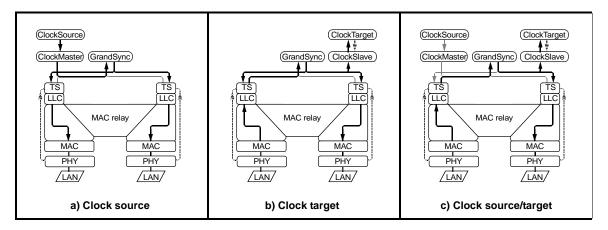


Figure 5.15—Clock-attached time-aware bridge possibilities

5.8.3 Design option possibilities

A variety of design options are associated with a time-aware station, as listed below. The P1588 draft currently restricts the subset of possibilities, with only certain allowed combinations of properties, as visible in Table 5.1.

- a) Ports. Either of the following are possible:
 - 1) One. An end-station has only one network port.
 - 2) More. A bridge station has two or more network ports.
- b) Reports. Either of the following link-delay mechanisms are possible:
 - 1) Peer-to-peer. Each master port calibrates delays to its directly attached slave port.
 - 2) End-to-end. Each master port calibrates delays to each of its indirectly attaches slave ports.
- c) Syncs. The transmission times of sync frames could depend on either of the following:
 - 1) Triggered. The clock-slave port's receipt of a sync frame triggers the transmissions.
 - 2) Periodic. Sync frame transmissions occurs periodically, independent of sync-frame receptions.
- d) Announce. Either of the following Announce-forwarding mechanism are possible:
 - 1) Filtered. Announce packets are checked; only the best candidate is forwarded to others.
 - 2) Periodic. Announce packets are flooded everywhere, regardless of need.
- e) Source. Either of the following grandmaster-capable clock-source presence selections are possible:
 - 1) Present. A ClockSource entity is present.
 - 2) Absent. No ClockSource entity is present.
- f) Target. Either of the following clock-target presence selections are possible:
 - 1) Present. A ClockTarget entity is present.
 - 2) Absent. No ClockTarget entity is present.

Table 5.1—Possible time-aware station alternatives

	Relay			Clock interfaces			
Ports	Slaves reports	Sync timing	Announce routing	Clock- Source	Clock- Target	Row	1588 name
1	n/a	n/a	n/a	present	present	1	Ordinary clock
				absent	present	2	_
				present	absent	3	_
				absent	absent	4	(uninteresting)
>1	1	periodic	filtered	present	present	5	Boundary clock
				absent	present	6	_
				present	absent	7	_
				absent	absent	8	_
			flooded	present	present	9	_
				absent	present	10	_
				present	absent	11	_
				absent	absent	12	_
	1	triggered	filtered	present	present	13	_
				absent	present	14	_
				present	absent	15	_
				absent	absent	16	_
			flooded	present	present	17	_
				absent	present	18	_
				present	absent	19	_
				absent	absent	20	Peer-to-peer transparent
	>1	triggered	filtered	present	present	21	_
				absent	present	22	_
				present	absent	23	_
				absent	absent	24	_
			flooded	present	present	25	_
				absent	present	26	_
				present	absent	27	_
				absent	absent	28	End-to-end transparent

Notes:

n/a is an abbreviation for not applicable

Note that the IEEE P1588 design approach appears to constrain particular combinations of features. A preferred 802.1AS design approach removes these application restrictions by eliminating impractical or undesirable options, as discussed below.

The number of viable 802.1AS options can be quickly reduced by elimination of the uninteresting end-station and non-scalable bridge rows, leaving fewer entries in Table 5.2.

Table 5.2—Viable time-aware station alternatives

	Relay		Clock interfaces			
Ports	Sync timing	Announce routing	Clock- Source	Clock- Target	Row	1588 name
1	n/a	n/a	present	present	1	Ordinary clock
	n/a	n/a	absent	present	2	_
	n/a	n/a	present	absent	3	_
>1	periodic	filtered	present	present	4	Boundary clock
			absent	present	5	_
			present	absent	6	_
			absent	absent	7	_
		flooded	present	present	8	_
			absent	present	9	_
			present	absent	10	_
			absent	absent	11	_
	triggered	filtered	present	present	12	_
			absent	present	13	_
			present	absent	14	_
			absent	absent	15	_
		flooded	present	present	16	_
			absent	present	17	_
			present	absent	18	_
			absent	absent	19	Peer-to-peer transparent

Notes:

n/a is an abbreviation for not applicable

If the presence/absence of ClockSource and ClockTarget interfaces are recognized as an independent design issues, the number of distinct 802.1AS options is further reduced by elimination of many rows, leaving fewer entries in Table 5.3..

Table 5.3—Distinct time-aware station alternatives

Donto	Relay			
Ports	Sync timing	Announce routing	Row	802.1AS name
1	n/a	n/a	1	End-station clock
>1	periodic	filtered	2	Boundary clock
		flooded	3	_
	triggered	filtered	4	_
		flooded	5	Transparent clock

Notes:

n/a is an abbreviation for not applicable

We recognize that the optimal sync-timing rate is media dependent and may differ for different technologies (802.3 full duplex, 802.3 EPON, or wireless) or for different operating modes of the same technology (normal vs. power-saving). Thus, a boundary-clock like design is necessary.

If this boundary-clock like design model meets our response-time and cumulative jitter requirements (as initial simulations appear to indicate), then the number of sufficient design alternatives is reduced, leaving fewer entries in Table 5.4..

Table 5.4—Sufficient time-aware station alternatives

Donto	Relay			
Ports	Sync timing Announce routing		Row	802.1AS name
1	n/a	n/a	1	End-station clock
>1	periodic	filtered	2	Boundary clock
		flooded	3	_

Notes:

n/a is an abbreviation for not applicable

The desire for quick response time, while avoiding broadcast storms in large configurations, appears to favor the use of filtered Announce-packet routing protocols. If the incremental costs filtering Announce packets are confirmed to be insignificant within time-aware bridges, the number of desired 802.1AS design alternatives is further reduced to an easily justifiable set, as illustrated in Table 5.5..

Table 5.5—Necessary time-aware station alternatives

Ports	Relay			
rorts	Sync timing	Announce routing	Row	802.1AS name
1	n/a	n/a	1	End-station clock.
>1	periodic	filtered	2	Bridge clock.

Notes:

n/a is an abbreviation for not applicable