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Re:	Sponsor re-circulation Ballot
Abstract	H-ARQ support corrections, for OFDMA PHY mode
Purpose	Adoption of proposed changes into P802.16-REVd/D4-2004
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In page 529, line 47, correct section 8.4.9.2.3.1 as shown below:

8.4.9.2.3.1 CTC encoder

The Convolutional Turbo Code defined in this section is designed to enable support of hybrid ARQ (H-ARQ). H-ARQ implementation is optional. The Convolutional Turbo Code encoder, including its constituent encoder, is depicted in Figure 240. It uses a double binary Circular Recursive Systematic Convolutional code. The bits of the data to be encoded are alternately fed to A and B, starting with the MSB of the first byte being fed to A. The encoder is fed by blocks of k bits or k couples (k = 2*N bits). For all the frame sizes k is a multiple of 8 and k is a multiple of 4. Further k shall be limited to: $k \le N/4 \le 1024$.

The polynomials defining the connections are described in octal and symbol notations as follows:

- For the feedback branch: 0xB, equivalently 1+D+D³ (in symbolic notation)
- For the Y parity bit: 0xD, equivalently $1+D^2+D^3$
- For the W parity bit: 0x9, equivalently $1+D^3$

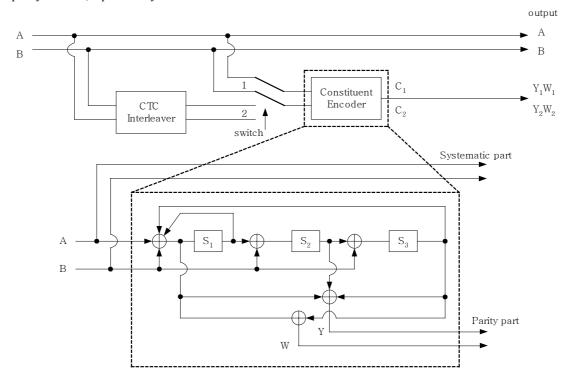


Figure 240—CTC encoder

First, the encoder (after initialization by the circulation state Sc 1, see 8.4.9.2.3.3) is fed the sequence in the natural order (position 1) with the incremental address i = 0 .. N–1. This first encoding is called S_{CI} encoding. Then the encoder (after initialization by the circulation state Sc2, see 8.4.9.2.3.3) is fed by the interleaved sequence (switch in position 2) with incremental address j = 0, ... N–1. This second encoding is called C_2 encoding.

The order in which the encoded bit shall be fed into the interleaver (8.4.9.3) subpacket generation block (8.4.9.2.3.4) is:

$$\frac{A, B, Y_{1}, W_{1}, Y_{2}, W_{2} =}{A, A_{2}, ..., A_{N}, B_{1}, B_{2}, ..., B_{N}, Y_{11}, Y_{12}, ..., Y_{1N}, W_{11}, W_{12}, ..., W_{1N}, Y_{21}, Y_{22}, ..., Y_{2N}, W_{21}, W_{22}, ..., W_{2N}}$$

$$A,B,Y_1,Y_2,W_1,W_2=A_{0,}B_0,...,A_{N-1},B_{N-1},Y_{1,0},Y_{1,1},...,Y_{1,N-1},Y_{2,0},Y_{2,1},...,Y_{2,N-1},W_{1,0},W_{1,1},...,W_{1,N-1},W_{2,0},W_{2,1},...,W_{2,N-1},W_{2$$

where *M* is the number of parity bits.

Note that the interleaver (8.4.9.3) shall not be used when using CTC

Table 284 gives the block sizes, code rates, channel efficiency, and code parameters for the different modulation and coding schemes. As 64-QAM is optional, the codes for this modulation shall only be implemented if the modulation is implemented.

The encoding block size shall depend on the number of subchannels allocated and the modulation specified for the current transmission. Concatenation of a number of subchannels shall be performed in order to make larger blocks of coding where it is possible, with the limitation of not passing the largest block under the same coding rate (the block defined by 64-QAM modulation). Table ccc specifies the concatenation of subchannels for different allocations and modulations. The concatenation rule shall not be used when using H-ARQ.

For any modulation and FEC rate, given an allocation of n subchannels, we define the following parameters: j = parameter dependent on the modulation and FEC rate n = number of allocated subchannels k = floor(n / j) m = n modulo j (aaa)

Table bbb shows the rules used for subchannel concatenation,

Table bbb—Subchannel concatenation rule for CTC

Number of subchannels	Subchannels concatenated
<u>n <= j</u>	1 block of n subchannels
<u>n≠7</u>	
<u>n=7</u>	1 block of 4 subchannels
	1 block of 3 subchannels
$\underline{n > j}$	(k-1) blocks of j subcahnnels
	1 block of L _{b1} subchannels
	1 block of L _{b2} subchannels
	Where:
	$\underline{L_{b1}} = \text{ceil}((m+j)/2)$
	$\underline{L}_{b2} = floor((m+j)/2)$
	<u>If $(L_{b1} == 7)$ or $(L_{b2} == 7)$</u>
	$L_{b1} = L_{b1} + 1$; $L_{b2} = L_{b2} - 1$;

Table ccc—Encoding Subchannel concatenation for different allocations and modulations in CTC

Modulation and rate	i
<u>QPSK 1/2</u>	<u>j = 10</u>
QPSK 3/4	<u>j = 6</u>
QAM16 1/2	<u>j = 5</u>
QAM16 3/4	<u>j = 3</u>
QAM64 1/2	<u>j = 3</u>
QAM64 2/3	<u>j = 2</u>
QAM64 3/4	<u>j = 2</u>
QAM64 5/6	<u>j = 2</u>

Table 284 gives the block sizes, code rates, channel efficiency, and code parameters for the different modulation and coding schemes. As 64-QAM is optional, the codes for this modulation shall only be implemented if the modulation is implemented. <u>Table hhh shows</u> code parameters for HARQ.

Table 256—Optimal CTC channel coding per modulation

Modulation	Data block size (bytes)	Encoded data block size (bytes)	Code rate	N	P0	P1	P2	Р3
OPSK	6	12	1/2	24	5	0	0	0
QPSK	12	24	1/2	48	13	24	0	24
OPSK	18	36	1/2	72	11	6	0	6
QPSK	24	48	1/2	96	7	48	24	72
QPSK	30	60	1/2	120	13	60	0	60
QPSK	36	72	1/2	144	17	74	72	2
QPSK	48	96	1/2	192	11	96	48	144
QPSK	54	108	1/2	216	13	108	0	108
<u>QPSK</u>	<u>60</u>	<u>120</u>	<u>1/2</u>	<u>240</u>	<u>13</u>	<u>120</u>	<u>60</u>	<u>180</u>
QPSK	9	12	3/4	36	11	18	0	18
QPSK	18	24	3/4	72	11	6	0	6
QPSK	27	36	3/4	108	11	54	56	2
QPSK	36	48	3/4	144	17	74	72	2
<u>QPSK</u>	<u>45</u>	<u>60</u>	<u>3/4</u>	<u>180</u>	<u>11</u>	<u>90</u>	<u>0</u>	<u>90</u>
<u>QPSK</u>	<u>54</u>	<u>72</u>	<u>3/4</u>	<u>216</u>	<u>13</u>	<u>108</u>	0	<u>108</u>
QAM16	12	24	1/2	48	13	24	0	24
QAM16	24	48	1/2	96	7	48	24	72
QAM16	36	72	1/2	144	17	74	72	2
<u>QAM16</u>	<u>48</u>	<u>96</u>	<u>1/2</u>	<u>192</u>	<u>11</u>	<u>96</u>	<u>48</u>	<u>144</u>
<u>QAM16</u>	<u>60</u>	<u>120</u>	<u>1/2</u>	<u>240</u>	<u>13</u>	<u>120</u>	<u>60</u>	<u>180</u>
QAM16	18	24	3/4	72	11	6	0	6
QAM16	36	48	3/4	144	17	74	72	2
<u>QAM16</u>	<u>54</u>	<u>108</u>	<u>3/4</u>	<u>216</u>	<u>13</u>	<u>108</u>	<u>0</u>	<u>108</u>
QAM64	18	36	1/2	72	11	6	0	6
QAM64	36	72	1/2	144	17	74	72	2
<u>QAM64</u>	<u>54</u>	<u>108</u>	<u>1/2</u>	<u>216</u>	<u>13</u>	<u>108</u>	<u>0</u>	<u>108</u>
QAM64	24	36	2/3	96	7	48	24	72
QAM64	48	<u>72</u>	2/3	<u>192</u>	<u>11</u>	<u>96</u>	<u>48</u>	<u>144</u>
QAM64	27	36	3/4	108	11	54	56	2
<u>QAM64</u>	<u>54</u>	<u>72</u>	<u>3/4</u>	<u>216</u>	<u>13</u>	<u>108</u>	<u>0</u>	<u>108</u>
QAM64	<u>30</u>	<u>36</u>	<u>5/6</u>	<u>120</u>	<u>13</u>	<u>60</u>	0	<u>60</u>
<u>QAM64</u>	<u>60</u>	<u>72</u>	<u>5/6</u>	<u>240</u>	<u>13</u>	<u>120</u>	<u>60</u>	<u>180</u>

<u>Data</u>	N	<u>P0</u>	<u>P1</u>	<u>P2</u>	<u>P3</u>
block size					
(bytes)					
<u>6</u>	<u>24</u>	<u>5</u>	<u>0</u>	<u>0</u>	<u>0</u>
<u>12</u>	<u>48</u> <u>72</u>	<u>13</u>	<u>24</u>	0	<u>24</u>
<u>18</u>	<u>72</u>	<u>11</u>	<u>6</u>	<u>0</u>	<u>6</u>
<u>24</u>	<u>96</u>	<u>7</u>	<u>48</u>	<u>24</u>	<u>6</u> <u>72</u> <u>2</u>
12 18 24 36 48	<u>144</u>	<u>17</u>	<u>48</u> <u>74</u> <u>96</u>	24 72 48	<u>2</u>
<u>48</u>	<u>192</u>	<u>11</u>	<u>96</u>	<u>48</u>	144
<u>60</u>	<u>240</u>	<u>13</u>	<u>120</u>	<u>60</u>	<u>180</u>
<u>120</u>	<u>480</u>	<u>13</u>	<u>240</u>	<u>120</u>	<u>360</u>
<u>240</u>	<u>960</u>	<u>13</u>	<u>480</u>	<u>240</u>	<u>720</u>
<u>360</u>	1440	<u>17</u>	<u>720</u>	<u>360</u>	<u>540</u>
<u>480</u>	<u>1920</u>	<u>17</u>	<u>960</u>	<u>480</u>	1440
600	2400	17	1200	600	1800

Table hhh—Optimal CTC channel coding per modulation when supporting H-ARQ

In page 532, line 40, correct the text as shown below:

8.4.9.2.3.4 Subpacket generation

Proposed FEC structure punctures the mother codeword to generate subpacket with various coding rates. The subpacket is also used as H-ARQ packet transmission. Figure bbb244 shows block diagram of subpacket generation. 1/3 CTC encoded codeword goes through interleaving block and the puncturing is performed. Figure 245 shows block diagram of the interleaving block. The puncturing is performed to select the consecutive interleaved bit sequence that starts at any point of whole codeword. For the first transmission, the subpacket is generated to select the consecutive interleaved bit sequence that starts from the first bit of the systematic part of the mother codeword. The length of the subpacket is chosen according to the needed coding rate reflecting the channel condition. The first subpacket can also be used as a codeword with the needed coding rate for a burst where H-ARQ is not applied.

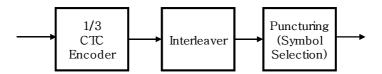


Figure 244— Block diagram of subpacket generation

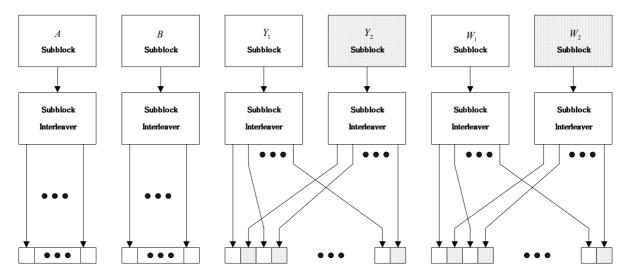


Figure 245— Block diagram of the interleaving scheme

8.4.9.2.3.4.1 Symbol Separation

All of the encoded symbols shall be demultiplexed into 6 subblocks denoted A, B, Y_1, Y_2, W_1 and W_2 . The encoder output symbols shall be sequentially distributed into 6 subblocks with the first encoder output symbols going to the A subblock, the second encoder output going to the B subblock, the third to the Y_1 subblock, the fourth to the Y_2 subblock, the fifth to the W_1 subblock, the sixth to the W_2 subblock, etc.

8.4.9.2.3.4.2 Subblock Interleaving

The six subblocks shall be interleaved separately. The interleaving is performed by the unit of symbol. The sequence of interleaver output symbols for each subblock shall be generated by the procedure described below. The entire subblock of symbols to be interleaved is written into an array at addresses from 0 to the number of the symbols minus one (N-1), and the interleaved symbols are read out in a permuted order with the i-th symbol being read from an address, AD_i (i = 0 to N - 1), as follows:

- 1. Determine the subblock interleaver parameters, $m_{\underline{a}}$ and $J_{\underline{b}}$. Table ddd gives these parameters.
- 2. Initialize *i* and *k* to 0.
- 3. Form a tentative output address T_k according to the formula

$$T_k = 2^m (k \mod J) + BRO_m(\lfloor k/J \rfloor)$$

where $BRO_m(y)$ indicates the bit-reversed m-bit value of y (i.e., BRO3(6) = 3).

- 4. If T_k is less than N_i , $AD_i = T_k$ and increment i and k by 1. Otherwise, discard T_k and increment k only.
- 5. Repeat steps 3 and 4 until all N interleaver output addresses are obtained.

The parameters for the subblock interleavers are specified in Table ddd.

Table ddd - The parameters for the subblock interleavers

Data block size (bits)	N	Subblock Interlea	ver Parameters
$\underline{\mathbf{N}}_{\mathbf{EP}}$	<u>N</u>	<u>m</u>	<u>J</u>
<u>48</u>	<u>24</u>	<u>3</u>	<u>3</u>
<u>72</u>	<u>36</u>	<u>4</u>	<u>3</u>
<u>96</u>	<u>48</u>	<u>4</u>	<u>3</u>
<u>144</u>	<u>72</u>	<u>5</u>	<u>3</u>
<u>192</u>	<u>96</u>	<u>5</u>	<u>3</u>
<u>216</u>	<u>108</u>	<u>6</u>	<u>3</u>
<u>240</u>	<u>120</u>	<u>6</u>	<u>2</u>
<u>288</u>	<u>144</u>	<u>6</u>	<u>3</u>
<u>384</u>	<u>192</u>	<u>6</u>	<u>3</u>
<u>432</u>	<u>216</u>	<u>6</u>	<u>4</u>
<u>480</u>	<u>240</u>	<u>7</u>	<u>2</u>

Table eee - The parameters for the subblock interleavers when supporting H-ARQ

Data block size (bits)	N	Subblock Interleaver Parameters			
$\underline{\mathbf{N}}_{\mathbf{EP}}$	1	<u>m</u>	<u>J</u>		
<u>48</u>	<u>24</u>	<u>3</u>	<u>3</u>		
<u>96</u>	<u>48</u>	<u>4</u>	<u>3</u>		

<u>144</u>	<u>72</u>	<u>5</u>	<u>3</u>
<u>192</u>	<u>96</u>	<u>5</u>	<u>3</u>
<u>288</u>	<u>144</u>	<u>6</u>	<u>3</u>
<u>384</u>	<u>192</u>	<u>6</u>	<u>3</u>
<u>480</u>	<u>240</u>	<u>7</u>	<u>2</u>
<u>960</u>	<u>480</u>	<u>8</u>	<u>2</u>
<u>1920</u>	<u>960</u>	<u>9</u>	<u>2</u>
<u>2880</u>	<u>1440</u>	<u>9</u>	<u>3</u>
<u>3840</u>	<u>1920</u>	<u>10</u>	<u>2</u>
4800	2400	10	3

8.4.9.2.3.54.3 Interleaving block

The puncturing process is very common to generate various coding rates with Turbo code families. However, the puncturing should guarantee the complementary characteristics of the punctured codeword. In other words, the parity bits of the punctured codeword should be chosen uniformly from the parity bits of a constituent encoder. The parity bits of the punctured codeword should have even number of parities from the two constituent encoders. Because the puncturing is just a simple process to select the subpacket, the proposed FEC structure rely such complementary property on the interleaving block.

Figure cee245 shows block diagram of the interleaving scheme of the proposed FEC structure. At first, the CTC encoder output is separated into a sublock. Then the interleaving is applied for the bit sequence within the sublock. It guarantees the uniformity of the interleaved codeword. Next, Symbol grouping is performed such that the parity bits from the two constituent encoders are interlaced bit by bit. The systematic part of the 1/3 CTC encoder is located at the head of the interleaved codeword. In this way, the proposed FEC structure ensures the quasi complementary characteristics of the interleaved codeword and thus, complementary characteristics of the subpacket. We just say "quasi complementary" for the case of breaking the complementariness of few bits after puncturing.

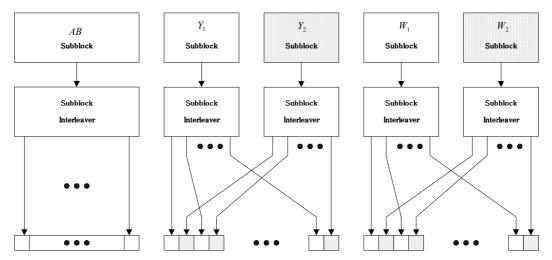


Figure 245 - Block diagram of the interleaving scheme

8.4.9.2.3.4.3 Symbol grouping

The channel interleaver output sequence shall consist of the interleaved A and B subblock sequence followed by a symbol-by-symbol multiplexed sequence of the interleaved Y_1 and Y_2 subblock sequences followed by a symbol-by-symbol multiplexed sequence of the interleaved W_1 and W_2 subblock sequences. The symbol-by-symbol multiplexed sequence of interleaved Y_1 and Y_2 subblock sequences shall consist of the first output bit from the Y_1 subblock interleaver, the first output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the symbol-by-symbol multiplexed sequence of interleaver, the second output bit from the Y_2 subblock interleaver, the first output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver, the second output bit from the Y_2 subblock interleaver.

interleaver, the second output bit from the W_2 subblock interleaver, etc. Figure 245 shows the interleaving scheme.

8.4.9.2.3.6.4.4 Symbol selection

Lastly, symbol selection is performed to generate the subpacket. We call the puncturing block as the symbol selection in the viewpoint of subpacket generation.

Mother code is transmitted with one of subpackets. The symbols in a subpacket are formed by selecting specific sequences of symbols from the interleaved CTC encoder output sequence. The resulting subpacket sequence is a binary sequence of symbols for the modulator.

Let

k be the subpacket index when H-ARQ is enabled. k=0 for the first transmission and increases by one

for the next subpacket. k = 0 when H-ARQ is not used.

 N_{EP} be the number of bits in the encoder packet (before encoding) N_{SCHk} be the number of subchannel(s) allocated for the k-th subpacket

 m_k be the modulation order for the k th subpacket ($m_k = 2$ for QPSK, 4 for 16QAM, and 6 for 64-

QAM); and

 $SPID_k$ be the subpacket ID for the k-th subpacket, (for the first subpacket, $SPID_{k=0} = 0$).

Also, let the scrambled and selected symbols be numbered from zero with the 0-th symbol being the first symbol in the sequence. Then, the index of the i-th symbol for the k-th subpacket shall be

$$S_{k,i} = (F_k + i) \mod(3 * N_{FP})$$

where i = 0 to $L_K - 1$,

$$L_k = 48 * N_{SCHk} * m_k \text{ and}$$

$$F_k = (SPID_k * L_k) \operatorname{mod}(3 * N_{EP}).$$

The N_{EP} , N_{SCHk} , \underline{m}_k and SPID values are determined by the BS and can be inferred by the SS through the allocation size in the DL-MAP and UL-MAP. The m_k parameter is determined in the next subsection. The above symbol selection makes the followings possible.

- 1. The first transmission includes the systematic part of the mother code. Thus, it can be used as the codeword for a burst where the H-ARQ is not applied.
- 2. The location of the subpacket can be determined by the SPID itself without the knowledge of previous subpacket. It is very important property for H-ARQ retransmission.

In page 535, line 17, Add a new section as shown below:

8.4.9.2.3. 6-5 Optional H-ARQ Support

H-ARQ implementation is optional. The randomization block in 8.4.9.1, the concatenation scheme in 8.4.9.2.3.1 and the interleaving in 8.4.9.3 shall not be applied for the encoding described in this section.

8.4.9.2.3. 6.5.1 Padding

MAC PDU (or concatenated MAC PDUs) is a basic unit processed in this channel coding and modulation blocks. When the size of MAC PDU (or concatenated MAC PDUs) is not the element in the allowed set for H-ARQ, '1's are padded at the end of MAC PDU (or concatenated MAC PDUs). The amount of the padding is the same as the difference between the size of the PDU (or concatenated MAC PDUs) and the smallest element in the allowed set that is not less than the size of the PDU (or concatenated MAC PDUs). The padded packet is input into the Randomization block.

The allowed set is {32, 80, 128, 176, 272, 368, 464, 944, 1904, 2864, 3824, 4784, 9584, 14384, 19184, 23984} bits.

8.4.9.2.3. 6.5.2 Randomization

The randomization is performed on each allocation (burst), which means that for each allocation of a data block the randomizer shall be used independently.

The Pseudo Random Binary Sequence (PRBS) generator shall be $1 + X^{14} + X^{15}$ as shown in Figure eee. Each data byte to be transmitted shall enter sequentially into the randomizer, MSB first. The seed value shall be used to calculate the randomization bits, which are combined in an XOR operation with the serialized bit stream of each burst. The randomizer sequence is applied to the output from the padding block. The bit issued from the randomizer shall be applied to the CRC encoder.

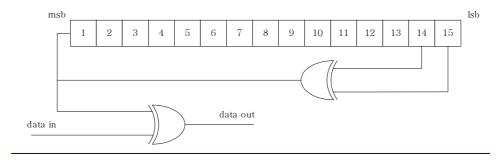
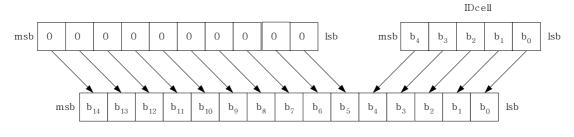


Figure eee - PRBS of the randomizer



OFDMA scrambler initalization vector

Figure fff - Initialization construction for the PRBS of the randomizer

The scrambler is initialized with the vector created as shown in Figure fff. The lowest 5 bits are ID_{cell} or UL_ID_{cell} and the other bits are set '0'.

8.4.9.2.3. 6.5.3 CRC encoding

When H-ARQ is applied to a packet, error detection is provided on the padded packet through a Cyclic Redundancy Check (CRC). The size of the CRC is 16 bits. CRC16-CCITT, as defined in ITU-T Recommendation X.25, shall be included at the end of the padded and randomized packet. The CRC covers both the padded bits and the information part of the padded and randomized packet. After the CRC operation, The packet size shall belong to set {48, 96, 144, 192, 288, 384, 480, 960, 1920, 2880, 3840, 4800, 9600, 14400, 19200, 24000}.

8.4.9.2.3. 6.5.4 Fragmentation

When the size after the padding and CRC encoding is n*4800 bits they are separately encoded by the block of 4800 bits and concatenated as the same order of the separation before modulation. No operation is performed for the packet whose size after the padding and CRC encoding is not more than 4800 bits.

The bits output from the fragmentation block are denoted by r_1 , r_2 , r_3 , \cdots , $r_{N_{EP}}$, and this sequence is defined as encoder packet. NEP is the number of the bits in an encoder packet and defined as encoder packet size. The values of N_{EP} are 48, 96, 144, 192, 288, 384, 480, 960, 1920, 2880, 3840, 4800.

8.4.9.2.3. 6.5.5 CTC encoding and subpacket generation

The CTC encoding and subpacket generation is same as the operation described in 8.4.9.2.3.1~8.4.9.2.3.4.

8.4.9.2.3.6.4.6.5.6 Modulation order of DL traffic burst

For DL, the modulation order (2 for QPSK, 4 for 16-QAM, and 6 for 64-QAM) shall be set for all the allowed transmission formats as shown in table kkk. The transmission format is given by the N_{EP} (Encoding Packet Size) and the N_{SCH} (number of allotted subchannels). N_{EP} per an encoding packet is {144, 192, 288, 384, 480, 960, 1920, 2880, 3840, 4800}. The N_{SCH} per an encoding packet is {1~480}. In the table, the numbers in the first row are N_{EP} 's and the numbers in the remaining rows are N_{SCH} 's and related parameters.

The supportable modulation schemes are QPSK, 16QAM, and 64QAM. When the N_{EP} and the N_{SCH} are given, the modulation order is determined by the value of MPR (Modulation order Product code Rate). The MPR means the effective number of the information bit transmitted per a subcarrier and is defined as follows.

$$MPR = \frac{N_{EP}}{48 \cdot N_{SCH}}$$

Then, the modulation order is specified by the following rule:

- i) If 0 < MPR < 1.5, then a QPSK (modulation order 2) is used
- ii) If $1.5 \le MPR < 3.0$, then a 16QAM (modulation order 4) is used
- iii) If $\overline{3.0 \le MPR < 5.4}$, then a 64QAM (modulation order 6) is used

The effective code rate is equal to MPR divided by the modulation order (i.e. 2 for QPSK).

The information of Nep and Nsch shall be signaled in DL, UL MAP. Instead of the actual values of Nep and Nsch, the encoded value of Nep (Nep code) and Nsch (Nsch code) shall be used for the signaling. They are encoded by 4 bits, respectively. The encoding of Nep (Nep code) is shown in Table III. The encoding of Nsch (Nsch code) is performed per Nep value. For each Nep, there are less than 16 kinds of Nsch values and they are encoded from '0' (the smallest number of subchannels) to '15' in increasing order. When the kinds of Nsch for a Nep is smaller than 16 and it is z, the smallest z codes are used. When the fragmentation is applied and the number of the subpackets for an allocation is n, n*Nep and Nsch (the number of subchannels allocated for a subpacket) should be signaled. The encoding for n*Nep (Nep code) is also shown in Table III. The encoded value of Nsch (Nsch code) should be interpreted as Nsch for a subpacket, and n*Nsch for the whole allocation.

Table kkk - Transmission format and modulation level for DL

Nep	144	192	288	384	480	960	1920	2880	3840	4800
Sch	1.00	1.00								
MPR	3.00	4.00								
MOD	6.00	6.00								
Rate	1/2	2/3								
<u>Rate</u>	0.50	0.67								
Sch	2.00	2.00	2.00	2.00	2.00					
<u>MPR</u>	1.50	2.00	3.00	4.00	5.00					
MOD	4.00	4.00	<u>6.00</u>	<u>6.00</u>	6.00					
Rate	<u>3/8</u>	1/2	1/2	2/3	<u>5/6</u>					
<u>Rate</u>	0.38	<u>0.50</u>	<u>0.50</u>	<u>0.67</u>	0.83					
<u>Sch</u>	<u>3.00</u>	<u>3.00</u>	<u>3.00</u>	<u>3.00</u>	3.00					
<u>MPR</u>	<u>1.00</u>	<u>1.33</u>	2.00	2.67	<u>3.33</u>					
MOD	2.00	2.00	<u>4.00</u>	<u>4.00</u>	<u>6.00</u>					
Rate	1/2	<u>2/3</u>	1/2	<u>2/3</u>	5/9					
Rate	<u>0.50</u>	0.67	<u>0.50</u>	0.67	<u>0.56</u>					
Sch		<u>4.00</u>	4.00	<u>4.00</u>	<u>4.00</u>	<u>4.00</u>				
<u>MPR</u>		<u>1.00</u>	<u>1.50</u>	2.00	<u>2.50</u>	<u>5.00</u>				
MOD		2.00	4.00	<u>4.00</u>	4.00	6.00				
<u>Rate</u>		1/2	<u>3/8</u>	<u>1/2</u>	<u>5/8</u>	<u>5/6</u>				
Rate		<u>0.50</u>	0.38	0.50	0.63	0.83				
Sch	5.00		<u>5.00</u>	<u>5.00</u>	<u>5.00</u>	<u>5.00</u>				
<u>MPR</u>	0.60		<u>1.20</u>	<u>1.60</u>	<u>2.00</u>	<u>4.00</u>				
MOD	2.00		2.00	4.00	4.00	<u>6.00</u>				
<u>Rate</u>	3/10		<u>3/5</u>	<u>2/5</u>	1/2	<u>2/3</u>				
Rate	0.30		<u>0.60</u>	0.40	0.50	0.67				
<u>Sch</u>	<u>6.00</u>	<u>6.00</u>	<u>6.00</u>	6.00	<u>6.00</u>	<u>6.00</u>				
MPR	0.50	0.67	1.00	1.33	1.67	3.33				
MOD	2.00	2.00	2.00	2.00	4.00	6.00				
<u>Rate</u>	1/4	<u>1/3</u>	1/2	<u>2/3</u>	5/12	<u>5/9</u>				
Rate	<u>0.25</u>	0.33	<u>0.50</u>	0.67	0.42	<u>0.56</u>	0.00			
Sch MDD		<u>8.00</u>		<u>8.00</u>	8.00	<u>8.00</u>	<u>8.00</u>			
MPR MOD		<u>0.50</u> 2.00		1.00 2.00	1.25 2.00	2.50 4.00	<u>5.00</u> 6.00			
Rate		<u>2.00</u> <u>1/4</u>		<u>2.00</u> <u>1/2</u>	<u>2.00</u> <u>5/8</u>	<u>4.00</u> <u>5/8</u>	<u>6.00</u> <u>5/6</u>			
Rate Rate		$\frac{-1/4}{0.25}$		0.50	0.63	<u></u>	0.83			
Sch	9.00	0.20	9.00	0.00	0.00	0.00	9.00			
MPR	0.33		<u>9.00</u> <u>0.67</u>				<u>9.00</u> <u>4.44</u>			
MOD MOD	2.00		2.00				6.00			
Rate	1/6		1/3				20/27			
Rate	0.17		0.33				0.74			
Sch					10.00	10.00	10.00			
MPR					1.00	2.00	4.00			
MOD					2.00	4.00	6.00			
Rate					1/2	1/2	2/3			
Rate					0.50	0.50	0.67			
Sch	12.00	12.00	12.00	12.00				12.00		
<u> </u>	12.00	12.00	12.00	12.00]]		12.00		

MPR	0.25	0.33	0.50	0.67		ĺ	ĺ	5.00		Ī
MOD	2.00	2.00	2.00	2.00				6.00		
Rate	1/8	1/6	1/4	1/3				5/6		
Rate	0.13	0.17	0.25	0.33				0.83		
Sch						13.00	13.00	13.00		
MPR						1.54	3.08	4.62		
MOD						4.00	6.00	6.00		
Rate						5/13	20/39	10/13		
Rate						0.38	0.51	0.77		
Sch					15.00	15.00	15.00	15.00		
MPR					0.67	1.33	2.67	4.00		
MOD					2.00	2.00	4.00	6.00		
Rate					1/3	2/3	2/3	2/3		
Rate					0.33	0.67	0.67	0.67		
Sch		16.00		16.00					16.00	
<u>MPR</u>		0.25		0.50					5.00	
MOD		2.00		2.00					<u>6.00</u>	
<u>Rate</u>		1/8		1/4					<u>5/6</u>	
<u>Rate</u>		0.13		0.25					0.83	
Sch	18.00		18.00						18.00	
<u>MPR</u>	0.17		0.33						4.44	
MOD	2.00		2.00						<u>6.00</u>	
<u>Rate</u>	1/12		1/6						20/27	
Rate	0.08		0.17						0.74	
<u>Sch</u>					20.00	20.00	20.00	20.00	<u>20.00</u>	20.00
<u>MPR</u>					0.50	1.00	<u>2.00</u>	3.00	<u>4.00</u>	<u>5.00</u>
MOD					2.00	2.00	<u>4.00</u>	<u>6.00</u>	<u>6.00</u>	<u>6.00</u>
Rate					1/4	1/2	1/2	1/2	<u>2/3</u>	<u>5/6</u>
Rate					0.25	<u>0.50</u>	<u>0.50</u>	<u>0.50</u>	0.67	0.83
Sch								22.00		22.00
<u>MPR</u>								2.73		<u>4.55</u>
MOD								<u>4.00</u>		<u>6.00</u>
<u>Rate</u>								<u>15/22</u>		<u>25/33</u>
<u>Rate</u>								0.68		<u>0.76</u>
<u>Sch</u>		24.00	24.00	24.00						
<u>MPR</u>		0.17	0.25	0.33						
MOD		2.00	2.00	2.00						
		1/10	1/8	1/6						
<u>Rate</u>		<u>1/12</u>							ļ ļ	
<u>Rate</u>		<u> 1/12</u> <u> 0.08</u>	0.13	0.17						
Rate Sch							26.00		<u>26.00</u>	<u>26.00</u>
Rate Sch MPR							<u>1.54</u>		<u>3.08</u>	<u>3.85</u>
Rate Sch MPR MOD							1.54 4.00		3.08 6.00	3.85 6.00
Rate Sch MPR MOD Rate							1.54 4.00 5/13		3.08 6.00 20/39	3.85 6.00 25/39
Rate Sch MPR MOD Rate Rate							1.54 4.00 5/13 0.38		3.08 6.00 20/39 0.51	3.85 6.00
Rate Sch MPR MOD Rate Rate Sch					30.00	30.00	1.54 4.00 5/13 0.38 30.00	30.00	3.08 6.00 20/39 0.51 30.00	3.85 6.00 25/39
Rate Sch MPR MOD Rate Rate Sch MPR					0.33	<u>0.67</u>	1.54 4.00 5/13 0.38 30.00 1.33	<u>2.00</u>	3.08 6.00 20/39 0.51 30.00 2.67	3.85 6.00 25/39
Rate Sch MPR MOD Rate Rate Sch							1.54 4.00 5/13 0.38 30.00		3.08 6.00 20/39 0.51 30.00	3.85 6.00 25/39

<u>Rate</u>				0.17	0.33	0.67	<u>0.50</u>	0.67	
Sch			32.00						32.00
MPR			0.25						3.13
MOD			2.00						6.00
<u>Rate</u>			1/8						25/48
<u>Rate</u>			0.13						0.52
Sch		36.00							
MPR		0.17							
MOD		2.00							
Rate		1/12							
Rate		0.08							
Sch									38.00
<u>MPR</u>									2.63
MOD									4.00
<u>Rate</u>									25/38
<u>Rate</u>									0.66
Sch				<u>40.00</u>	40.00	40.00	40.00	40.00	
<u>MPR</u>				0.25	0.50	1.00	<u>1.50</u>	2.00	
MOD				<u>2.00</u>	2.00	2.00	<u>4.00</u>	<u>4.00</u>	
Rate				1/8	1/4	1/2	3/8	1/2	
Rate				0.13	0.25	0.50	0.38	<u>0.50</u>	
Sch							44.00		
<u>MPR</u>							1.36		
MOD							2.00		
<u>Rate</u>							_15/22		
<u>Rate</u>							0.68		
Sch			48.00						
<u>MPR</u>			0.17						
MOD			2.00						
Rate			1/12						
<u>Rate</u>			0.08						
Sch									<u>50.00</u>
<u>MPR</u>									2.00
MOD									<u>4.00</u>
<u>Rate</u>									1/2
<u>Rate</u>									<u>0.50</u>
Sch								<u>52.00</u>	
<u>MPR</u>								<u>1.54</u>	
MOD								<u>4.00</u>	
<u>Rate</u>								<u>5/13</u>	
Rate								0.38	
Sch				<u>60.00</u>	60.00	<u>60.00</u>	<u>60.00</u>	<u>60.00</u>	
<u>MPR</u>				0.17	0.33	0.67	1.00	<u>1.33</u>	
$\underline{\text{MOD}}$				2.00	2.00	2.00	2.00	2.00	
<u>Rate</u>				1/12	<u>1/6</u>	1/3	1/2	<u>2/3</u>	
<u>Rate</u>				<u>0.08</u>	0.17	<u>0.33</u>	<u>0.50</u>	<u>0.67</u>	
Sch									<u>64.00</u>
<u>MPR</u>									<u>1.56</u>

I	ı	ı	1	I	I	1	1	1		
MOD										4.00
Rate										25/64
<u>Rate</u>										0.39
Sch										<u>76.00</u>
<u>MPR</u>										1.32
$\underline{\text{MOD}}$										2.00
<u>Rate</u>										25/38
Rate										0.66
Sch						80.00	80.00		80.00	
<u>MPR</u>						0.25	0.50		1.00	
MOD						2.00	2.00		2.00	
Rate						1/8	1/4		1/2	
Rate						0.13	0.25		0.50	
Sch								90.00		
MPR								0.67		
MOD								2.00		
Rate								1/3		
Rate								0.33		
Sch								0.00		100.00
										1.00
MPR MOD										<u>2.00</u>
										1/2
Rate										
Rate	-									<u>0.50</u>
Sch						120.00	120.00	120.00	120.00	
<u>MPR</u>						0.17	0.33	0.50	0.67	
MOD						2.00	2.00	2.00	2.00	
<u>Rate</u>						1/12	1/6	1/4	1/3	
<u>Rate</u>						0.08	0.17	0.25	0.33	
<u>Sch</u>										<u>150.00</u>
<u>MPR</u>										0.67
MOD										2.00
<u>Rate</u>										<u>1/3</u>
<u>Rate</u>										<u>0.33</u>
Sch							160.00		<u>160.00</u>	
<u>MPR</u>							0.25		0.50	
MOD							2.00		2.00	
Rate							1/8		1/4	
Rate							0.13		0.25	
Sch								180.00		
<u>MPR</u>								0.33		
MOD								2.00		
Rate								1/6		
Rate								0.17		
Sch										200.00
MPR										0.50
MOD MOD										2.00
Rate										1/4
Rate										0.25
Mate	<u> </u>	<u> </u>								0.40

Sch				240.00	240.00	240.00	
<u>MPR</u>				0.17	0.25	0.33	
MOD				2.00	2.00	2.00	
<u>Rate</u>				1/12	1/8	<u>1/6</u>	
Rate				<u>0.08</u>	0.13	<u>0.17</u>	
Sch							300.00
<u>MPR</u>							0.33
MOD							2.00
Rate							1/6
<u>Rate</u>							0.17
Sch						<u>320.00</u>	
<u>MPR</u>						0.25	
MOD						<u>2.00</u>	
<u>Rate</u>						1/8	
Rate						<u>0.13</u>	
Sch					<u>360.00</u>		
<u>MPR</u>					0.17		
MOD					2.00		
<u>Rate</u>					1/12		
Rate					0.08		
<u>Sch</u>							400.00
<u>MPR</u>							0.25
MOD							2.00
Rate							1/8
Rate							<u>0.13</u>
Sch						480.00	
MPR						0.17	
MOD						2.00	
Rate						<u>1/12</u>	
<u>Rate</u>						<u>0.08</u>	

Table III - N_{EP} Encoding

N _{EP}	<u>48</u>	<u>96</u>	<u>144</u>	<u>192</u>	<u>288</u>	<u>384</u>	<u>480</u>	<u>960</u>	<u>1920</u>	<u>2880</u>	<u>3840</u>	<u>4800</u>	<u>9600</u>	<u>14400</u>	<u>19200</u>	<u>24000</u>
Encoding	<u>0</u>	1	2	<u>3</u>	4	<u>5</u>	<u>6</u>	7	<u>8</u>	9	<u>10</u>	<u>11</u>	<u>12</u>	<u>13</u>	<u>14</u>	<u>15</u>

8.4.9.2.3.6.5.5.7 Modulation order of UL traffic burst

For UL, the modulation order (2 for QPSK and 4 for 16-QAM) shall be set for all the allowed transmission formats as shown in Table mmm. The transmission format is given by the N_{EP} (Encoding Packet Size) and the N_{SCH} (number of allotted subchannels). N_{EP} per an encoding packet is {48, 96, 144, 192, 288, 384, 480, 960, 1920, 2880, 3840, 4800}. The N_{SCH} per an encoding packet is {1~288}. In the table, the numbers in the first row are N_{EP} 's and the numbers in the remaining rows are N_{SCH} 's and related parameters.

The supportable modulation schemes are QPSK and 16QAM. When the N_{EP} and the N_{SCH} are given, the modulation order is determined by the value of MPR (Modulation order Product code Rate). The MPR means the effective number of the information bit transmitted per subcarrier and is defined as follows.

$$MPR = \frac{N_{EP}}{48 \cdot N_{SCH}}$$

Then, the modulation order is specified by the following rule:

- i) If 0 < MPR < 1.5, then a QPSK (modulation order 2) is used
- ii) If $1.5 \le MPR < 3.4$, then a 16QAM (modulation order 4) is used

The effective code rate is equal to MPR divided by the modulation order (i.e. 2 for QPSK).

The information of Nep and Nsch shall be signaled in UL MAP. Instead of the actual values of Nep and Nsch, the encoded value of Nep (Nep code) and Nsch (Nsch code) shall be used for the signaling. They are encoded by 4 bits, respectively. The encoding of Nep (Nep code) is shown in Table III. The encoding of Nsch (Nsch code) is performed per Nep value. For each Nep, there are less than 16 kinds of Nsch values and they are encoded from '0'(the smallest number of subchannels) to '15' in increasing order. When the kinds of Nsch for a Nep is smaller than 16 and it is z, the smallest z codes are used. When the fragmentation is applied and the number of the subpackets for an allocation is n, n*Nep and Nsch (the number of subchannels allocated for a subpacket) should be signaled. The encoding for n*Nep (Nep code) is also shown in Table III. The encoded value of Nsch (Nsch code) should be interpreted as Nsch for a subpacket, and n*Nsch for the whole allocation.

Table mmm - Transmission format and modulation level for UL

Nep	48	<u>96</u>	144	<u>192</u>	<u>288</u>	384	<u>480</u>	<u>960</u>	<u>1920</u>	2880	<u>3840</u>	<u>4800</u>
<u>Sch</u>	<u>1.00</u>	1.00	<u>1.00</u>									
<u>MPR</u>	<u>1.00</u>	<u>2.00</u>	<u>3.00</u>									
$\underline{\text{MOD}}$	<u>2.00</u>	<u>4.00</u>	<u>4.00</u>									
Rate	<u>1/2</u>	<u>1/2</u>	3/4									
Rate	<u>0.50</u>	<u>0.50</u>	0.75									
<u>Sch</u>	2.00	2.00	<u>2.00</u>	2.00	2.00							
<u>MPR</u>	<u>0.50</u>	<u>1.00</u>	<u>1.50</u>	<u>2.00</u>	3.00							
$\underline{\text{MOD}}$	<u>2.00</u>	2.00	<u>4.00</u>	<u>4.00</u>	<u>4.00</u>							
<u>Rate</u>	1/4	1/2	3/8	1/2	3/4							
Rate	0.25	<u>0.50</u>	0.38	<u>0.50</u>	0.75							
<u>Sch</u>	3.00	3.00	<u>3.00</u>	<u>3.00</u>	3.00	3.00	3.00					
$\underline{\text{MPR}}$	0.33	0.67	1.00	1.33	2.00	2.67	3.33					
$\underline{\text{MOD}}$	2.00	2.00	2.00	2.00	4.00	<u>4.00</u>	4.00					
<u>Rate</u>	1/6	1/3	1/2	<u>2/3</u>	1/2	2/3	_5/6					
<u>Rate</u>	0.17	<u>0.33</u>	<u>0.50</u>	0.67	<u>0.50</u>	<u>0.67</u>	<u>0.83</u>					
Sch	4.00	4.00		4.00	4.00	4.00	4.00					
<u>MPR</u>	0.25	<u>0.50</u>		1.00	1.50	<u>2.00</u>	<u>2.50</u>					
$\underline{\text{MOD}}$	2.00	2.00		2.00	4.00	<u>4.00</u>	4.00					
Rate	1/8	1/4		1/2	3/8	1/2	_5/8					
Rate	0.13	0.25		<u>0.50</u>	0.38	<u>0.50</u>	0.63					
<u>Sch</u>			<u>5.00</u>		<u>5.00</u>	<u>5.00</u>	<u>5.00</u>					

MPR	1 1	I	0.60	ı	1.20	1.60	2.00	ĺ		Ī]
MOD MOD			2.00		2.00	4.00	<u>4.00</u>				
Rate					3/5	2/5	1/2				
Rate			3/10 0.30		0.60	0.40	0.50				
Sch	6.00	6.00	6.00	6.00		6.00		6.00			
MPR	0.00	0.33	0.50	6.00 0.67	6.00 1.00	1.33	6.00 1.67	3.33			
MOD	2.00	2.00	2.00	2.00	2.00	2.00	4.00	4.00			
Rate		1/6	1/4	1/3	1/2	2/3		5/6			
Rate	1/12 0.08	0.17	0.25	0.33	0.50	0.67	<u>5/12</u> 0.42	0.83			
Sch	0.00	0.11	0.20	0.00	0.00	0.01	0.12	7.00			
MPR								2.86			
MOD								4.00			
Rate								<u>5/7</u>			
<u>Rate</u>								<u>0.714</u>			
<u>Sch</u>		<u>8.00</u>		<u>8.00</u>		<u>8.00</u>	<u>8.00</u>	<u>8.00</u>			
MPR		0.25		0.50		<u>1.00</u>	<u>1.25</u>	<u>2.50</u>			
MOD		2.00		2.00		2.00	2.00	4.00			
Rate		<u>1/8</u> <u>0.13</u>		<u>1/4</u> <u>0.25</u>		<u>1/2</u> <u>0.50</u>	<u>5/8</u> 0.625	<u>5/8</u> <u>0.625</u>			
Rate Sch		0.15	9.00	0.20	9.00	0.50	0.020	0.020			
MPR			0.33		0.67						
MOD			2.00		2.00						
Rate			1/6		1/3						
Rate			<u>0.17</u>		<u>0.33</u>						
<u>Sch</u>							<u>10.00</u>	<u>10.00</u>			
MPR							<u>1.00</u>	<u>2.00</u>			
MOD							2.00	4.00			
Rate Pate							<u>1/2</u> 0.50	1/2 0.50			
Rate Sch		12.00	12.00	12.00	12.00	12.00	0.50	0.50	12.00		
MPR		0.17	0.25	0.33	0.50	0.67			3.33		
MOD		2.00	2.00	2.00	2.00	2.00			4.00		
Rate			1/8	1/6	1/4	1/3			5/6		
Rate		<u>1/12</u> <u>0.08</u>	0.13	0.17	0.25	0.33			0.83		
Sch Sch		0.00	0.10	<u>0.11</u>	0.20	0.00			13.00		
MPR									3.08		
MOD									4.00		
Rate									10/19		
Rate									10/13 0.77		
Sch							15.00	15.00	15.00		
MPR							0.67	1.33	2.67		
MOD							2.00	2.00	4.00		
<u>Rate</u>							1/3	<u>2/3</u>	2/3		
<u>Rate</u>							0.33	<u>0.67</u>	0.67		
Sch MDD				16.00		<u>16.00</u>					
MPR MOD				0.25		0.50					
MOD Rate				2.00 1/8		2.00 1/4					
<u>Rate</u>										I	

Rate	Ī		0.13		0.25						-
Sch		18.00		18.00					18.00		
MPR		0.17		0.33					3.33		
$\underline{\text{MOD}}$		2.00		2.00					4.00		
Rate		1/12		<u>1/6</u>					<u>5/6</u>		
Rate		0.08		0.17					0.83		
Sch						20.00	20.00	20.00	20.00		
<u>MPR</u>						0.50	1.00	2.00	3.00		
MOD						2.00	2.00	4.00	4.00		
Rate						1/4	1/2	1/2	3/4		
Rate						<u>0.25</u>	<u>0.50</u>	<u>0.50</u>	<u>0.75</u>		
<u>Sch</u>			24.00	24.00	24.00				24.00	24.00	
MPR			0.17	0.25	0.33				<u>2.50</u>	3.33	
MOD			2.00	<u>2.00</u>	<u>2.00</u>				<u>4.00</u>	<u>4.00</u>	
Rate			<u>1/12</u>	1/8	1/6				<u>5/8</u>	<u>5/6</u>	
<u>Rate</u>			0.08	<u>0.13</u>	0.17				<u>0.63</u>	<u>0.83</u>	
Sch								26.00		<u>26.00</u>	
<u>MPR</u>								<u>1.54</u>		3.08	
MOD								<u>4.00</u>		<u>4.00</u>	
<u>Rate</u>								<u>5/13</u>		10/13	
<u>Rate</u>								0.385		<u>0.77</u>	
<u>Sch</u>						<u>30.00</u>	<u>30.00</u>	<u>30.00</u>	30.00	<u>30.00</u>	30.00
MPR						0.33	<u>0.67</u>	1.33	2.00	2.67	3.33
MOD						2.00	2.00	2.00	4.00	4.00	4.00
Rate						<u>1/6</u> 0.17	1/3	2/3	1/2	<u>2/3</u> <u>0.67</u>	<u>5/6</u> 0.83
Rate					22.00	<u>0.17</u>	<u>0.33</u>	<u>0.67</u>	<u>0.50</u>	<u>0.67</u>	0.83
Sch MPR					32.00 0.25						
MOD					2.00						
Rate					1/8						
Rate					0.13						
Sch											34.00
MPR											2.94
MOD											4.00
Rate											25/34
Rate											0.74
Sch				<u>36.00</u>							
\underline{MPR}				0.17							
MOD				<u>2.00</u>							
<u>Rate</u>				1/12							
Rate				0.08							
Sch											38.00
MPR											2.63
MOD											4.00
Rate											25/38
<u>Rate</u>											0.66
<u>Sch</u>						<u>40.00</u>	<u>40.00</u>	<u>40.00</u>	<u>40.00</u>	<u>40.00</u>	

MFR MOD		ı	ı	ı	ı	ı i		1	1	1	1	1
Rate 1/8 1/4 1/2 38 1/2 Rate 0.13 0.25 0.50 0.38 0.50 MPR 0.13 0.25 0.50 0.38 0.50 MDD 0.07 0.07 0.07 0.07 MPR 0.17 0.08 0.07 0.07 MDD 2.00 0.08 0.07 0.07 Rate 0.08 0.08 0.00 0.00 0.00 Sch 0.08 0.08 0.00												
Rate 6 0												
Sch MPR												
MPR							0.15	0.23	0.50		0.50	
MOD Rate R												
Rate Rate 8 48.00 48.00 5.00												
Rate 48.00 0.67 <td< td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></td<>												
Sch MPR Sch MPR MPR												
MPR MOD						48.00				0.01		
MOD Rate												
Rate 1/12 0,08												
Rate 0.08 0.08 0.08 0.08 0.00 50.00 50.00 20.00 20.00 4.00 20.00 4.00 20.00 4.00 1.00 1.00 1.00 1.00 1.00 1.50 0.00 60.00						=100						
Sch MPR MOD												
MPR						0.08						
MOD Rate R												
Rate												
Rate 6.50 Sch MPR 6.50 5200 MPR MOD 6.00 <th< td=""><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td><td></td></th<>												
Sch MPR MOD Rate Mode Mod												
MPR MOD Rate 60.00 1.33 60.00 1.33 60.00 1.33 60.00 1.33 1.22 2.23 2.00 <td></td> <td>0.50</td>												0.50
MOD Rate Rate 60.00 1.33 1.00 1.33 1.00 1.33 1.00 1.33 1.00 1.33 1.00 1.33 1.00 1.33 1.00 1.33 1.00 1.33 1.00 1.33 1.00 2.00												
Rate 60.00 2.00												
Rate 60.00 2.00 <												
Sch MPR 60.00 60.00 60.00 60.00 60.00 60.00 60.00 60.00 60.00 60.00 60.00 60.00 1.33 MOD 1.33 0.67 1.00 1.33 1.02 2.00												
MPR MOD Brate <							20.00	20.00	20.00	20.00		
MOD Rate 2.00												
Rate 1/12 1/6 1/3 1/2 2/3 Sch 0.08 0.17 0.33 0.50 0.67 MPR 0.08 0.08 0.08 0.08 0.08 0.08 0.08 0.08 0.08 0.08 0.08 0.08 0.08 0.08 0.08 0.08 0.08 0.08 0.00 0.08 0.00 0.08 0.00												
Rate 0.08 0.17 0.33 0.50 0.67 Sch MPR 1.52 4.00 1.52 MOD MO							<u>2.00</u>					
Sch MPR 1.52 MOD 25/66 Rate 25/66 Rate 0.38 Sch 0.38 MPR 1.32 MOD 2.00 Rate 25/38 Rate 0.66 Sch 80.00 80.00 MPR 0.25 0.50 1.00 MOD 2.00 2.00 2.00 Rate 1/8 1/4 1/2 Rate 0.13 0.25 0.50 Sch 0.67 0.67 MOD 2.00 2.00	Rate							1/6				
MPR MOD 1.52 MOD 25/66 Rate 25/66 Rate 0.38 Sch 0.38 MPR 0.20 MOD 0.20 Rate 0.66 Sch 0.66 Sch 0.00 MPR 0.00 MOD 0.00 Sch 0.00 MOD 0.00 Rate 0.25 0.50 1.00 MOD 2.00 Rate 0.13 0.25 0.50 Sch 0.67 MPR 0.67 MOD 0.67 MOD 0.67 MOD 0.67 MOD 0.67 MOD 0.67	Rate						0.08	0.17	0.33	<u>0.50</u>	0.67	
MOD Rate 25/66 Rate 25/66 Rate 0.38 Sch 0.38 MPR 1.32 MOD 2.00 Rate 25/38 Rate 0.25 MOD 2.00 MPR 0.25 MOD 2.00 Rate 0.13 0.25 0.50 Sch 0.13 MOD 0.67 MPR 0.67 MOD 0.67 MOD 0.67 MOD 0.67	<u>Sch</u>											<u>66.00</u>
Rate 25/66 Rate 76.00 MPR 76.00 MOD 2.00 Rate 25/38 Rate 80.00 80.00 MPR 90.00 MOD 2.00 Rate 1/8 1/4 1/2 Rate 0.13 0.25 0.50 Sch 90.00 90.00 MPR 0.67 90.00 MPR 0.67 2.00 MOD 2.00 2.00												
Rate 0.38 Sch 76.00 MPR 1.32 MOD 2.00 Rate 25/38 Rate 80.00 80.00 Sch 0.25 0.50 1.00 MOD 2.00 2.00 2.00 Rate 0.13 0.25 0.50 Sch 0.13 0.25 0.50	MOD											4.00
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MOD Rate 2.00 Rate 25/38 Rate 80.00 80.00 80.00 MPR 0.25 0.50 1.00 MOD 2.00 2.00 2.00 Rate 1/8 1/4 1/2 Rate 0.13 0.25 0.50 Sch 90.00 90.00 MPR 0.67 2.00 MOD 2.00 2.00												
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Sch 80.00 80.00 80.00 MPR 0.25 0.50 1.00 MOD 2.00 2.00 2.00 Rate 1/8 1/4 1/2 Rate 0.13 0.25 0.50 Sch 90.00 0.67 MPR 0.67 2.00												
MPR MOD 0.25 0.50 1.00 Rate 1/8 1/4 1/2 Rate 0.13 0.25 0.50 Sch 90.00 0.67 MPR 0.67 2.00	Rate											0.66
MOD Rate 2.00	Sch							80.00	80.00		80.00	
Rate Rate	<u>MPR</u>							0.25	0.50		<u>1.00</u>	
Rate 0.13 0.25 0.50 Sch 90.00 90.00 MPR 0.67 2.00	MOD							<u>2.00</u>	<u>2.00</u>		2.00	
Sch 90.00 MPR 0.67 MOD 2.00	<u>Rate</u>											
MPR 0.67 MOD 2.00	<u>Rate</u>							<u>0.13</u>	0.25		<u>0.50</u>	
<u>MOD</u> <u>2.00</u>	Sch									90.00		
	<u>MPR</u>									0.67		
<u>Rate</u>												
	Rate	1								1/3		

Rate	ĺ						0.33		
Sch									100.0
MPR									<u>0</u> <u>1.00</u>
MOD									2.00
Rate									1/2
Rate									<u>0.50</u>
Sch					120.0 0	120.0 <u>0</u>	120.0 0	120.0 <u>0</u>	
MPR					<u>0.17</u>	<u>0.33</u>	<u>0.50</u>	<u>0.67</u>	
MOD					2.00	2.00	2.00	2.00	
Rate					1/12	1/6	1/4	1/3	
Rate					0.08	0.17	0.25	0.33	
Sch									<u>150.0</u>
MPR									<u>0</u> <u>0.67</u>
MOD MOD									<u>0.67</u> <u>2.00</u>
Rate									1/3
Rate									0.33
Sch						<u>160.0</u>		<u>160.0</u>	
MPR						<u>0</u> <u>0.25</u>		<u>0</u> 0.50	
MOD						2.00		2.00	
Rate						1/8		1/4	
Rate						<u>0.13</u>		<u>0.25</u>	
Sch							180.0		
MPR							<u>0</u> 0.33		
MOD							2.00		
Rate							<u>1/6</u>		
<u>Rate</u>							<u>0.17</u>		
Sch MPR									<u>200.0</u> <u>0.50</u>
MOD MOD									2.00
Rate									1/4
Rate									0.25
Sch						240.0	240.0	240.0	
<u>MPR</u>						<u>0.17</u>	<u>0.25</u>	<u>0.33</u>	
MOD						<u>2.00</u>	<u>2.00</u>	<u>2.00</u>	
Rate						1/12	1/8	1/6	
Rate						<u>0.08</u>	0.13	0.17	

In page 488 line 43, add a new paragraph:

8.4.5.4.9 UL ACK channel

The uplink ACK (Acknowledgement) provides feedback for Downlink Hybrid ARQ. This channel shall only be supported by SS supporting H-ARQ. The SS transmits ACK or NAK feedback for Downlink packet data. One ACK channel occupies half subchannel (3 pieces of 3x3 uplink tile) of the PUSC optional permutation.

The ACK channel is orthogonally modulated. The acknowledgement bit B_n^{ACK} of the n-th ACK channel shall be '0' (ACK) if the corresponding downlink packet has been successfully received; otherwise, it shall be a '1' (NAK). The k-th orthogonal modulation symbol of the n-th ACK channel, $M_{n,k}^{ACK}$ (k=0,1,...,8 and n=0,1,..., N_{ACK} -1) is made as shown in Table ppp.

Table ppp- Orthogonal Modulation for ACK channel

B_n^{ACK}	$M_{n,k}^{ACK}$
<u>0</u>	11111
	$\frac{1 + 1 + 1}{1 - \exp\left(j\frac{2\pi}{3}\right) - \exp\left(j\frac{4\pi}{3}\right) - \exp\left(j\frac{2\pi}{3}\right) - \exp\left(j\frac{4\pi}{3}\right)}$
1	$\frac{1}{2} \exp\left(j\frac{4\pi}{3}\right) = \exp\left(j\frac{2\pi}{3}\right)$

Then the modulated symbols are mapped to the subcarriers allocated to the *n*-th ACK channel, as follows.

$$c_{n,k}^{ACK} = \begin{cases} M_{n,k}^{ACK} & \text{if } k = 0,1,\dots,8 \\ \exp\left(j\frac{2\pi}{3}\right)M_{n,k-9}^{ACK} & \text{if } k = 9,10,\dots,17 \\ \exp\left(j\frac{4\pi}{3}\right)M_{n,k-18}^{ACK} & \text{if } k = 18,19,\dots,26 \end{cases}$$

where

 $c_{n,k}^{ACK}$ = mapping symbol of the *k*-th ACK subcarrier in the *n*-th ACK channel

 $M_{n,k}^{ACK}$ = modulation symbol index of the k-th modulation symbol made from the n-th ACK bit as shown in Table ppp

 $\underline{n} = ACK$ channel index from the set $[0 \sim N_{ACK} - 1]$

k = ACK subcarrier index of an ACK channel from the set $[0 \sim 26]$

In page 243, line 41, modify the text to read:

6.4.167 MAC support for H-ARQ

Hybrid automatic repeat request (H-ARQ) scheme is an optional part of the MAC and can be enabled on a per-terminal basis. <u>H-ARQ</u> <u>may be supported only for the OFDMA PHY</u>. The per-terminal H-ARQ and associated parameters shall be specified and negotiated during initialization procedure. A <u>terminal</u> burst cannot have a mixture of H-ARQ and non-H-ARQ traffic.

One or more MAC PDUs can be concatenated and an H-ARQ packet formed by adding a CRC to the PHY burst. Figure 125 shows how the H-ARQ encoder packet is constructed.

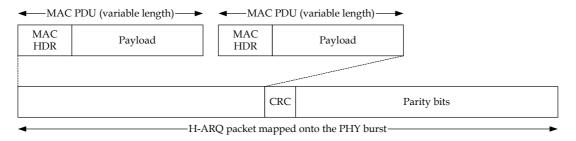


Figure 125—Construction of H-ARQ encoder packet

Each encoder packet is encoded according to the PHY specification, and four subpackets are generated from the encoded result. A subpacket identifier (SPID) is used to distinguish the four subpackets. In case of downlink communication, a BS can send one of the subpackets in a burst transmission. Because of the redundancy among the subpackets, SS can correctly decode the original encoder packet even before it receives all four subpackets. Whenever receiving the first subpacket, the SS attempts to decode the original encoder packet from it. If it succeeds, the SS sends an ACK to the BS, so that the BS stops sending additional subpackets of the encoder packet. Otherwise, the SS sends a NAK, which causes the BS to transmit one subpacket selected from the four. These procedures go on until the SS successfully decodes the encoder packet. When the SS receives more than one subpacket, it tries to decode the encoder packet from ever-received subpackets.

The rule of subpacket transmission is as follows,

- 1. At the first transmission, BS shall send the subpacket labeled '00'.
- 2. BS may send one among subpackets labeled '00', '01', '10', or '11' in any order, as long as the total number of transmitted subpackets does not exceed the maximum number of H ARQ retransmission specified in CD message
- 3. BS can send more than one copy of any subpacket, and can omit any subpacket except the subpacket labeled '00'.

In order to specify the start of a new transmission, one-bit H-ARQ identifier sequence number (AI_SN) is toggled on every successful transmission of an encoder packet on the same H-ARQ channel. If the AI_SN changes, the receiver treats the corresponding subpacket as a subpacket belongs to a new encoder packet, and discards ever-received subpackets with the same ARQ identifier.

The H-ARQ scheme is basically a stop-and-wait protocol. The ACK is sent by the SS after a fixed delay (synchronous ACK) defined by H-ARQ_ACK_DELAY_H-ARQ_DL_ACK_delay offset which is specified in DCD message. Timing of retransmission is, however, flexible and corresponds to the asynchronous part of the H-ARQ. The ACK/NAK is a differential binary PSK modulated signal sent by the SS_sent by the BS using the H-ARQ_Bitmap IE, and sent by a SS using the fast feedback UL subchannel.

The H-ARQ scheme supports multiple H-ARQ channels per a connection, each of which may have an encoder packet transaction pending. The number of H-ARQ channels in use is determined by BS. These ARQ channels are distinguished by an H-ARQ channel identifier (ACID). The ACID for any subpackets can be uniquely identified by the control information carried in the MAPs.

H-ARQ (Hybrid Automatic Repeat reQuest) can be used to mitigate the effect of channel and interference fluctuation. H-ARQ renders performance improvement due to SNR gain and time diversity achieved by combining previously erroneously decoded packet and retransmitted packet, and due to additional coding gain by IR (Incremental Redundancy).

6.4.17.1 Subpacket generation

H-ARQ operates at the FEC block level. The FEC encoder is responsible for generating the H-ARQ subpackets, as defined in the relevant PHY section. The subpackets are combined by the receiver FEC decoder as part of the decoding process.

6.4.17.42 DL/UL ACK/NAK signaling

For DL/UL H-ARQ, fast ACK/NAK signaling is necessary. For the fast ACK/NAK signaling of DL H-ARQ channel, a dedicated PHY layer ACK/NAK channel is designed in UL. For the fast ACK/NAK signaling of UL H-ARQ channel fast feedback, H-ARQ ACK message is designed.

6.4.17.3 H-ARQ parameter signaling

The parameters for each subpacket should be signaled independent of the subpacket burst itself. The parameters for each subpacket include SPID (Subpacket Identifier. The BS shall set this field to the subpacket identifier for the subpacket transmission.), ACID (ARQ Channel Identifier. The BS shall set this field to the ARQ channel identifier for the subpacket transmission.), and AI_SN (ARQ identifier sequence number. This toggles between '0' and '1' on successfully transmitting each encoder packet with the same ARQ channel.). For the signaling of those parameters, H-ARQ Allocation IE is defined and the IE is to be placed in a DL-MAP for a burst where H-ARQ is used.

6.4.17.4 CQICH Operations

This section describes the operation scenarios and requirements of CQICH, which is designed for H-ARQ enabled SS.

After an SS turns on its power, the only appropriate subchannels that can be allocated to the MSS are normal subchannels. To determine the M/C level of normal subchannels, the average CINR measurement is enough for the BS to determine the M/C levels of uplink and downlink. As soon as the BS and the MSS know the capabilities of both entities' modulation and coding, the BS may allocate a CQICH subchannel using a CQICH Control IE (see 8.4.5.x.x.2 CQICH Control IE). Then, the MSS reports the average CINR of the BS' preamble. From then on, the BS is able to determine the M/C level. A CINR measurement is quantized into 32 levels and encoded into 5 information bits.

At any time, the BS may de-allocate the SS' CQICH by putting another CQICH Control IE with Duration d = 0000. Before the CQICH life timer which is set at the receipt of the CQICH Control IE expires, sending another CQICH Control IE overwrites all the information related to the CQICH such as Allocation Index, Period, Frame offset, and Duration. Hence, unless the BS refreshes the timer, the SS should stop reporting as soon as the timer expires. However, in case of sending the MAP IE for re-allocation or deallocation, the BS should make sure if the previous CQICH is released before it is re-allocated to another SS.

The SS sends the REP-RSP message in an unsolicited fashion to BS to trigger Band AMC operation. The triggering conditions are given by TLV encodings in UCD messages. The REP-RSP (see 11.12 for the TLV encodings) includes the CINR measurements of five best bands. Only when an SS reports its BS the CINR measurements of Band AMC channels, its logical definition is differently made as follows. If the number of bands is less than or equal to 12, it is the same as the original one. However, if it is 24 (1024 FFT in 10 MHz), two contiguous bands are paired and 12 logical bands are newly defined. Hence, band 2n and band (2n+1) are paired and the paired band is the n-th band. If the number of bands is 48 (2048 FFT in 20 MHz), the two contiguous bands are paired and renumbered the same as a 24 band system. Then, if the LSB of an SS' MAC address is 1, it only uses the odd-numbered bands. If not, it only uses the even-numbered bands. Hence, for example, the LSB of an SS' MAC address is 1, (4m+2, 4m+3) bands are paired and the paired band is the m-th band of the SS. Similarly, for an even-numbered SS, (4m, 4m+1) bands are paired and the paired band is the m-th band of the SS.

The BS acknowledges the trigger by allocating Band AMC subchannels. From the next frame when the SS sent the REP-RSP, the SS starts reporting the differential of CINR five selected bands (increment: 1 and decrement: 0 with a step of 1 dB) on its CQICH. If the BS does not allocate the Band AMC subchannels within the specified delay (CQICH Band AMC Transition Delay) in the UCD message, the SS reports the updated average CINR of the preamble for normal subchannel allocations.

When the BS wants to trigger the transition to Band AMC mode or update the CINR reports, it sends the REP-REQ message (see 11.11 for the TLV encodings). When the SS receives the message, it replies with REP-RSP. When the BS receives the REP-RSP, it should synchronize the selection of bands reported and their CINR. Unless the BS allocates normal subchannels, the SS reports the differential increment compared to the most up-to-date report from the next CQI reporting frame.

In page 480, line 50, add the following text:

8.4.5.3.8 H-ARQ MAP Pointer IE

This IE shall only be used by a BS supporting H-ARQ, for SS supporting H-ARQ.

Table 255—H-ARQ MAP Pointer IE

<u>Syntax</u>	Size	<u>Notes</u>
H-ARQ_Pointer_DL_IE () {	_	_
Extended DIUC	4 bits	$\underline{\text{H-ARQ MAP pointer}} = 0\text{x}05$
Length	<u>4 bit</u>	$\underline{\text{Length}} = 0 \times 02$
AMC DIUC	4 bits	Indicates the AMC level of the burst containing a H-ARQ MAP message.
No. Slots	8 bits	The number of slots allocated for the burst containing a H-ARQ MAP message.
Reserved	4 bits	
}		

<u>AMC DIUC</u> <u>Indicates the burst profile used for the H-ARQ MAP message.</u>

The number of OFDMA slots allocated for the burst containing a H-ARQ MAP message. The H-ARQ MAP message shall directly follows the DL MAP, the number of the slots allocated for the H-ARQ MAP message.

In page 104, line 21, add the following text:

6.3.2.3.44 H-ARQ MAP message

This section describes the H-ARQ MAP message, which is designed for H-ARQ enabled SS. This IE shall only be used by a BS supporting H-ARQ, for SS supporting H-ARQ.

6.3.2.3.44.1 H-ARQ MAP message format

The H-ARQ MAP message format is presented in Table 1. This message includes Compact DL/UL-MAP_IE and defines the access information for the downlink and uplink burst of H-ARQ enabled SS. This message shall be sent without a generic MAC header. BS may broadcast multiple H-ARQ MAP messages using multiple burst after the MAP message. Each H-ARQ MAP message should have a different modulation and coding rate. If the frame contains DCD or UCD message following the MAP message, the H-ARQ MAP should follow DCD or UCD message.

The DL-MAP_IEs in the MAP message describe the location and coding and modulation schemes of the bursts. The order of DL-MAP_IEs in the MAP message and the bursts for H-ARQ MAP messages is determined by the coding and modulation scheme of the burst. The burst for H-ARQ MAP message with lower rate coding and modulation should be placed before other bursts for H-ARQ MAP message.

The presence of the H-ARQ MAP message format is indicated by the contents of the three most significant bits of the first data byte of a burst. These bytes overlay the HT and EC bits of a generic MAC header. When these bits are both set to 1 (an invalid combination for a standard header) and followed by 1 bits of 1, the Compact DL-MAP format is present.

Table 1 Format of H-ARQ MAP

Syntax	Size	Notes
H-ARQ MAP Message Format () {		
H-ARQ MAP Indicator = 111	3 bits	Set to binary 111
H-ARQ_UL-MAP appended	<u> 1 bit</u>	
CRC appended	<u> 1 bit</u>	
Map message length	9 bits	Length of H-ARQ MAP in bytes
DL IE count	6 bits	Number of DL IE in the burst
for (i=0; $i \le DL$ IE count; $i++$){	_	
Compact_DL-MAP_IE()	<u>variable</u>	
}	-	
<pre>If (Compact_UL-MAP appended ==1){</pre>		
while (map data remains) {		
Compact_UL-MAP_IE()	<u>variable</u>	
_}		
_}		
if!(byte boundary) {		
Padding nibble	4 bits	
}	_	
<u> </u>		

H-ARQ MAP Indicator

The value of binary 111 means this message is a H-ARQ MAP Message

Compact UL-MAP appended

A value of 1 indicates a compact UL-MAP is appended to the current compact DL-MAP data structure

CRC appended

A value of one indicates a CRC-32 value is appended to the end of the H-ARQ MAP data. The CRC is computed across all bytes of the H-ARQ MAP starting with the byte containing the H-ARQ MAP indicator through the last byte of the map as specified by the Map message length field. The CRC calculation is the same as that used for standard MAC messages. A value of zero indicates that no CRC is appended.

MAP message length

This value specifies the length of the H-ARQ MAP message beginning with the byte containing the H-ARQ MAP

indicator and ending with the last byte of the H-ARQ MAP message. The length includes the computed 32-bit CRC value if the CRC appended indicator is on.

DL IE count

This field holds the number of IE entries in the following list of DL-MAP IEs.

Table 2 and Table 3 represent the types of compact DL/UL MAP.

Table 2 Compact DL-MAP IE Types

Compact DL-MAP Type	Description
<u>0</u>	Normal subchannel
<u>1</u>	Band AMC
<u>2</u>	Safety
<u>3</u>	DIUC
4	Format Configuration IE
<u>5</u>	H-ARQ ACK BITMAP IE
<u>6</u>	Reserved
7	<u>Extension</u>

Table 3 Compact UL-MAP IE Types

Compact_UL-MAP Type	Description
<u>0</u>	Normal subchannel
<u>1</u>	Band AMC
<u>2</u>	Safety
<u>3</u>	<u>UIUC</u>
<u>4</u>	H-ARQ Region IE
<u>5</u>	CQI Region IE
<u>6</u>	Reserved
<u>7</u>	Extension

6.3.2.3.44.2 Format Configuration

Table 4 represents the format of Format Configuration IE that configures CID type, safety pattern, maximum logical bands and frame structure. The format should be set to default value at the start of each frame.

Table 4 Format Configuration IE

Syntax	Size	<u>Notes</u>
Compact_DL-MAP_IE() {		
DL-MAP Type = 4	3 bits	Format Configuration IE
New Format Indication	1 bits	0 = Use the format configured by the latest Format_Configuration_IE 1 = New format
if (New Format Indication == 1) {		
CID Type	2 bits	00 = Normal CID 01 = RCID11 (default) 10 = RCID7 11 = RCID3
Safety Pattern	<u>10 bits</u>	
Subchannel Type for Band AMC	2 bit	See Band AMC specification. 00 = Default type (default) 01 = 1x6 type

		10 = 2x3 type $11 = 3x2 type$
Max Logical Bands	2 bits	0 = 3 bands, 1 = 6 bands, 2 = 12 bands (default) 3 = 24 bands
No. Symbols for Broadcast	4 bits	No. Symbol, $(default = 0)$
No. Symbols for DL Band AMC	4 bits	No. Symbol, $(default = 0)$
No. Symbols for UL Band AMC	4 bits	No. Symbol, $(default = 0)$
}		
}		

New Format Indication

If this value set to 0, the format should be configured by the latest Format Configuration IE in the previous frames. Otherwise, whole parameters in Format Configuration IE should be configured. The configured parameters are valid for the following Compact DL/UL MAP IE.

At the start of each frame all parameters are set to default values.

CID Type

This value specifies CID type used in the Compact_DL/UL_MAP_IE.

Safety Pattern

If this value is less than 16, the number of safety bins is 12 and the indices of allocated bins for safety are 16m+x, where x is the value of Safety Pattern and $m=0\sim11$. If this value is not less than 16, the number of safety bins is 24 and the indices of allocated bins for safety are 16m+x' and 16m+(x'+8), where x'=x-16 and $m=0\sim11$.

Subchannel Type for Band AMC

This value specifies the subchannel type for Band AMC subchannel. See related PHY specification.

No. Symbols for Broadcast

This specifies the number of symbols allocated for Broadcast subchannel.

No. Symbols for DL Band AMC

This specifies the number of symbols allocated for DL Band AMC subchannel.

The other DL symbols excluding the symbols for Broadcast and DL Band are allocated for the DL Normal subchannel.

No. Symbols for UL Band AMC

This specifies the number of symbols allocated for UL Band AMC subchannel.

The other UL symbols excluding the symbols for UL Band are allocated for the UL Normal subchannel.

Max Logical Bands

This value specifies the maximum number of logical bands for Band AMC. The size of 3 fields (No. Selected Bands, Band BITMAP and Band Index) in the DL/UL-MAP_IE for Bands AMC depends on this value. Table 5 represents the fields in the DL/UL-MAP_IE and specific values.

Table 5 Field length for Band AMC MAP IE

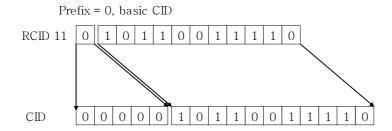
Logical Bands	24 bands	12 bands	6 bands	3 bands
Max Logical Bands	<u>11</u>	<u>10</u>	<u>01</u>	<u>00</u>
Nb-Band (# of bits for No. Selected Bands)	4 bits	4 bits	4 bits	0 bits
Nb-BITMAP (# of bits for Band BITMAP)	<u> 24 bits</u>	<u>12 bits</u>	8 bits	4 bits
Nb-Index (# of bits for Band Index)	8 bits	4 bits	4 bits	0 bits

6.3.2.3.44.3 Reduced CID

Table 6 presents the format of reduced CID. BS may use reduced CID instead of basic CID or multicast CID to reduce the size of H-ARQ MAP message. The type of reduced CID is determined by BS considering the range of basic CIDs of SS connected with the BS and specified by the RCID Type field of the Format Configuration IE.

The reduced CID is composed of 1 bit of prefix and n-bits of LSB of CID of SS. The prefix is set to 1 for the broadcast CID or multicast CID and set to 0 for basic CID. The reduced CID can not be used instead of transport CID, primary management CID or secondary management CID.

Figure 1 shows the decoding of reduced CID when the RCID_Type is set to 3.



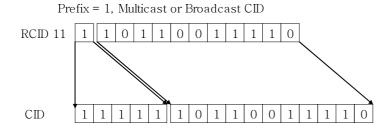


Figure 1 Reduced CID Decoding

Table 6 RCID_IE format

<u>Syntax</u>	Size	<u>Notes</u>
RCID_IE(){	_	Support of Reduced CID
if $(RCID_Type == 0)$ {		RCID Type is specified in Format Configuration IE
CID	<u>16</u>	normal CID
}else{		
<u>Prefix</u>	<u>1</u>	for multicast, AAS, Padding & broadcast burst temporary disable RCID
$if (Prefix == 1) \{$		
RCID 11	<u>11</u>	11 LSB of multicast, AAS or broadcast CID
}else{		
$if (RCID_Type == 1){}$	_	
RCID 11	<u>11</u>	11 LSB of basic CID
} else if (RCID_Type == 2){	_	
RCID 7	<u>7</u>	7 LSB of basic CID
} else if (RCID_Type == 3){		
RCID 3	3	3 LSB of basic CID
}		
_}	_	
}		
}		

Normal 16 bits CID

Prefix

A value of one indicates that 11 bits RCID for broadcast and multicast follows the prefix. Otherwise, the n-bits RCID for basic CID follows the prefix. The value of n is determined by the RCID_Type field in Format_Configuration_IE.

RCID_n

n-bits LSB of CID

6.3.2.3.44.4 H-ARQ Control IE

The format of H-ARQ_Control_IE, which includes encoding/decoding information for H-ARQ enabled DL/UL bursts, is presented in Table 7. This IE shall be located in the compact DL/UL MAP IE.

Table 7 H-ARQ Control IE format

Syntax	Size	Notes
H-ARQ_Control_IE() {	_	In DL/UL-MAP
Prefix	<u>1 bit</u>	0 = Temporary disable H-ARQ 1 = enable H-ARQ
$if (Prefix == 1){}$		
AI_SN	1 bits	H-ARQ ID Seq. No
SPID	2 bits	Subpacket ID
ACID	4 bits	H-ARQ CH ID
} else{		
Reserved	3 bit	
}		
}		-

Prefix

Indicates whether H-ARQ is enabled or not.

AI SN

<u>Defines ARQ Identifier Sequence Number. This is toggled</u> between '0' and '1' on successfully transmitting each encoder packet with the same ARQ channel.

SPID

Defines SubPacket ID, which is used to identify the four subpackets generated from an encoder packet.

ACID

<u>Defines H-ARQ Channel ID</u>, which is used to identify H-ARQ channels. Each connection can have multiple H-ARQ channels, each of which may have an encoder packet transaction pending.

6.3.2.3.44.5 CQICH Control IE

Table 8 Format of CQICH Control IE

Syntax	Size	<u>Notes</u>
CQICH_Control_IE() {		
CQICH indicator	<u>1 bit</u>	If the indicator is set to 1, the CQICH Control IE follows.
if CQICH indicator == 1 {		

Allocation Index	6 bits	Index to the channel in a frame the CQI report should be transmitted by the SS.
Period (=p)	2 bits	A CQI feedback is transmitted on the CQI channels indexed by the (CQI Channel Index) by the SS in every 2^p frames.
Frame offset	3 bits	The MSS starts reporting at the frame of which the number has the same 3 LSB as the specified frame offset. If the current frame is specified, the MSS should start reporting in 8 frames
Duration (=d)	4 bits	A CQI feedback is transmitted on the CQI channels indexed by the (CQI Channel Index) by the SS for 2^(d-1) frames. If d is 0000, the CQICH is deallocated. If d is 1111, the MSS should report until the BS command for the MSS to stop.
<u>} else {</u>		
Reserved	3 bits	
<u>}</u>		
}		

Each field of the IE represents the following parameters:

Allocation Index

It indicates its position from the start of the CQICH region

Period

It informs the SS of the period of CQI reports

Frame offset

It informs the SS of when to start. The SS starts reporting at the frame of which the number has the same 3 LSB as the specified frame offset. If the current frame is specified, the SS should start reporting in 8 frames

Duration

It indicates when the SS should stop reporting unless the CQICH allocation is refreshed beforehand. If duration d = 0000, the BS is intended to de-allocate the CQICH. If d == 1111, the CQICH is allocated indefinitely and the SS should report until the BS commands the SS to stop, which happens it receives another MAP_IE with d =0000.

6.3.2.3.44.6 Compact DL-MAP IE

6.3.2.3.44.6.1 Compact DL-MAP IE for Normal Subchannel

Table 9 Compact_DL-MAP_IE for normal subchannel

Syntax	Size	Notes
Compact_DL-MAP_IE() {		
DL-MAP Type =0	3 bits	
UL-MAP append	1 bits	
RCID_IE	variable	See Reduced CID section
Nep code	4 bits	Code of encoder packet bits (see FEC & CTC section)
Nsch code	4 bits	Code of allocated subchannels (see FEC & CTC section)
H-ARQ_Control_IE	variable	See H-ARQ section
CQICH_Control_IE	variable	See CQICH section

if(UL-MAP append){		
Nep code for UL	4 bits	# of encoder packet bits
Nsch code for UL	4 bits	
H-ARQ_Control_IE for UL	variable	
}		
}		

DL-MAP Type

This value specifies the type of the compact DL-MAP IE. A value of 0 indicates the Normal Subchannel.

UL-MAP append

A value of 1 indicates the uplink access information is appended to the end of the DL-MAP IE.

RCID_IE

Represent the assignment of the IE.

Nep code, Nsch code

The combination of Nep code and Nsch code indicates the number of allocated subchannels and scheme of coding and modulation for the DL burst.

Nep code for UL, Nsch code for UL

The combination of Nep code and Nsch code indicates the number of allocated subchannels and scheme of coding and modulation for the UL burst.

6.3.2.3.44.6.2 Compact DL-MAP IE for Band AMC Subchannel

Table 10 Compact DL-MAP IE for band AMC

<u>Syntax</u>	<u>Size</u>	<u>Notes</u>
Compact_DL-MAP_IE () {		
DL-MAP Type =1	3 bits	
Reserved	1 bit	
RCID_IE	<u>variable</u>	
Nep code	4 bits	Code of encoder packet bits (see FEC & CTC section)
Nsch code	4 bits	Code of allocated subchannels (see FEC & CTC section)
Nband	Nb-Band bits	Number of bands, 0 = use BITMAP instead
if(Nband == 0){		
Band BITMAP	Nb-BITMAP bits	n-th LSB is 1 if n-th band is selected
}else {		
for (i=0;i< Nband; i++)		
Band Index	Nb-Index bits	Band selection.
}		
Allocation Mode	<u>2 bit</u>	Indicates the subchannel allocation mode. 00 = same number of subchannels for the selected bands 01 = different number of subchannels for the selected bands 10 = total number of subchannels for the selected bands determined by Nsch code and Nep code 11 = reserved
Reserved	2 bits	
if(Allocation Mode == 00){		
No. Subchannels	8 bits	
} else if(Allocation Mode == 01){		
for (i=0;i< band count ;i++){	-	If Nband is 0, band count is the number of '1' in Band BITMAP. Otherwise band count is Nband.

No. Subchannels	8 bits	Number of subchannels per band
_}	_	_
}		
H-ARQ Control IE	<u>variable</u>	
CQICH Control IE	<u>variable</u>	
<u>}</u>	_	_

DL-MAP Type

This value specifies the type of the compact DL-MAP IE. A value of 1 indicates the Band AMC Subchannel.

RCID IE

Represent the assignment of the IE.

Nep code, Nsch code

The combination of Nep code and Nsch code indicates the number of allocated subchannels and scheme of coding and modulation for the DL burst.

Nband

Indicates the number of bands selected for the burst. If this value is set to 0, the Band BITMAP is used to indicate the number and the position of selected bands instead. The number of the maximum logical bands determines the length of this field.

Band BITMAP

This BITMAP is valid when Nband is 0. The n-th LSB of the Band BITMAP is set to 1 when the n-th logical band is selected for the burst. If the number of the maximum logical bands is 12 then the length of the Band BITMAP is 12 bits. The band count is set to the number of '1's in the Band BITMAP. The number of the maximum logical bands determines the length of this field.

Band Index

This value indexes the selected band offset and is valid when Nband is larger than 0. The number of the maximum logical bands determines the length of this field.

Allocation Mode

This value indicates the subchannel allocation mode in the selected bands.

The value is set to binary 00 when the same numbers of subchannels are allocated in the selected bands by the following field 'No. Subchannels'.

The value is set to 01 when different numbers of subchannels are allocated in each selected bands by the following fields "No. Subchannels".

The value is set to 10 when the total number of subchannels allocated in the selected bands is defined by Nsch code and Nep code. The subchannels fill from the bands with lowest index.

The allocation mode variant is shown in Figure 2.

No. Subchannels

This value indicates the number of subchannels allocated for this burst.

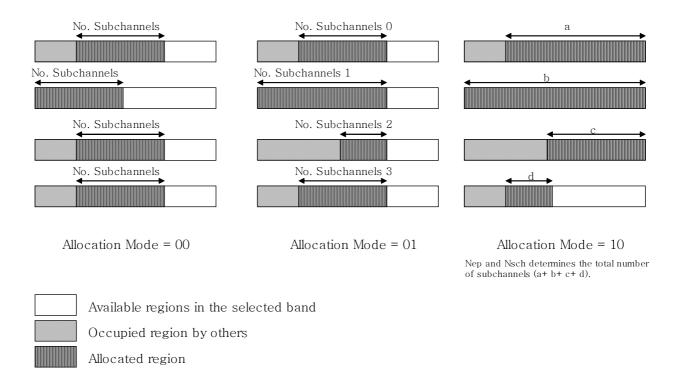


Figure 2 Subchannel allocation modes of Compact DL-MAP_IE for Band AMC

6.3.2.3.44.6.3 Compact DL-MAP IE for Safety Subchannel

Table 11 Compact DL-MAP IE for safety

Syntax	<u>Size</u>	<u>Notes</u>
Compact_DL-MAP_IE() {		
DL-MAP Type =2	3 bits	
UL-MAP append	1 bit	
RCID_IE	<u>variable</u>	
Nep code	4 bits	Code of encoder packet bits (see FEC & CTC section)
Nsch code	4 bits	Code of allocated subchannels (see FEC & CTC section)
BIN Offset	8 bits	_
H-ARQ_Control_IE	<u>variable</u>	
CQICH_Control_IE	<u>variable</u>	
if (UL-MAP append) {		
Nep code for UL	4 bits	Code of encoder packet bits (see FEC & CTC section)
Nsch code for UL	4 bits	Code of allocated subchannels (see FEC & CTC section)
BIN Offset for UL	8 bits	-

H-ARQ_Control_IE for UL	<u>variable</u>	
}		
}		

DL-MAP Type

This value specifies the type of the compact DL-MAP IE.

A value of 2 indicates the Safety Subchannel.

UL-MAP append

A value of 1 indicates the uplink access information is appended to the end of the DL-MAP IE.

RCID IE

Represent the assignment of the IE.

Nep code, Nsch code

The combination of Nep code and Nsch code indicates the number of allocated subchannels and scheme of coding and modulation for the burst.

BIN Offset

The offset of the BIN allocated for this DL burst. See appropriate specification.

Nep code for UL, Nsch code for UL

The combination of Nep code and Nsch code indicates the number of allocated subchannels and scheme of coding and modulation for the UL burst.

BIN Offset for UL

The offset of the BIN allocated for this UL burst. See appropriate specification.

6.3.2.3.44.6.4 Compact DL-MAP IE for DIUC Subchannel

Table 12 Compact DL-MAP IE for DIUC

<u>Syntax</u>	<u>Size</u>	<u>Notes</u>
Compact_DL-MAP_IE () {		
DL-MAP Type = 3	3 bits	DIUC type
Reserved	1 bits	
DIUC	4 bits	See DIUC section
RCID_IE	<u>variable</u>	-
No. Subchannels	8 bits	The number of subchannels allocated by the IE
}		-

DL-MAP Type

This value specifies the type of the compact DL-MAP IE. A value of 3 indicates the DIUC type.

DIUC

This value indicates the usage of this burst.

RCID IE

Represent the assignment of the IE.

No. Subchannels

This value indicates the number of subchannels allocated by the IE.

6.3.2.3.44.6.5 Compact DL-MAP IE for H-ARQ ACK BITMAP

The H-ARQ ACK_Bitmap information for the H-ARQ enabled UL bursts is delivered through the Compact_DL-MAP_IE as shown in Table 13. The bit position in the bitmap is determined by the order of the H-ARQ enabled UL bursts in the UL-MAP. The frame offset between the UL burst and the H-ARQ-ACK-BITMAP is specified by "H-ARQ_ACK_Delay_for UL Burst" field in the DCD message.

For example, when a SS transmits a H-ARQ enabled burst at i-th frame and the burst is j-th H-ARQ enabled burst in the MAP, the SS should receive H-ARQ ACK at j-th bit of the BITMAP which is sent by the BS at i+(frame offset)-th frame.

Table 13 Compact DL-MAP IE for H-ARQ ACK BITMAP

<u>Syntax</u>	Size	<u>Notes</u>
Compact_DL-MAP_IE () {		
DL-MAP Type = 5	3 bits	H-ARQ ACK BITMAP IE
Reserved	1 bit	
BITMAP Length	4 bits	Length in Bytes
BITMAP	<u>variable</u>	-
}		

DL-MAP Type

Defines the type of Compact DL-MAP. If the type value is 5, the Compact DL-MAP is for H-ARQ-ACK-BITMAP.

BITMAP Length

Specifies the length of the following BITMAP field.

BITMAP

Includes H-ARQ ACK information for H-ARQ enabled UL bursts. The size of BITMAP should be equal or larger than the number of H-ARQ enabled UL-bursts.

6.3.2.3.44.6.6 Compact DL-MAP IE for Extension

Table 14 Compact DL-MAP IE for extension

<u>Syntax</u>	Size	<u>Notes</u>
Compact_DL-MAP_IE() {		
DL-MAP Type = 7	3 bits	Extension type
DL-MAP Sub-Type	5 bits	Extension sub type
Length	4 bits	Length of the IE in Bytes
Payload	variable	Sub-type dependent payload
<u>}</u>	-	-
<u>Total</u>		-

DL-MAP Type

This value specifies the type of the compact DL-MAP IE. A value of 7 indicates the extension type.

DL-MAP Sub-Type

This value specifies the sub-type of the compact DL-MAP IE.

Length

This indicates the length of this IE in Bytes. If a SS can't recognize the DL-MAP Sub-Type, it skips the IE.

Payload

The payload depends on the value of DL-MAP Sub-Type. The length of payload is Length -1 Bytes.

6.3.2.3.44.7 UL-MAP_IE

6.3.2.3.44.7.1 Compact UL-MAP IE for Normal Subchannel

Table 15 Compact UL-MAP IE for normal subchannel

Syntax	Size	<u>Notes</u>
--------	------	--------------

Compact_UL-MAP_IE () {		
UL-MAP Type = 0	3 bits	
Reserved	<u>1 bit</u>	
RCID_IE	<u>variable</u>	-
Nep code	4 bits	
Nsch code.	4 bits	-
H-ARQ_Control_IE		
}		

<u>UL-MAP Type</u>

This value specifies the type of the compact UL-MAP IE. A value of 0 indicates the Normal Subchannel.

RCID IE

Represent the assignment of the IE.

Nep code, Nsch code

The combination of Nep code and Nsch code indicates the number of allocated subchannels and scheme of coding and modulation for the DL burst.

6.3.2.3.44.7.2 Compact UL-MAP IE for Band AMC Subchannel

Table 16 Compact UL-MAP IE for band AMC

<u>Syntax</u>	Size	<u>Notes</u>
Compact_UL-MAP_IE() {		
UL-MAP Type = band	3 bits	
Reserved	1 bit	
RCID_IE	<u>variable</u>	
Nep code	4 bits	Number of encapsulate packet
Nsch code	4 bits	
Nband	Nb-Band bits	Indicates the number of selected bands. 0 = BITMAP indicates the number and offset of selected bands
<u>if(Nband == 0)</u> {		
Band BITMAP	Nb-BITMAP bits	n-th LSB is 1 if n-th band is selected
}else {	_	
for (i=0;i <nband;i++)< td=""><td></td><td></td></nband;i++)<>		
Band Index	Nb-Index bits	Band selection.
}		
Allocation Mode	2 bits	Indicates the subchannel allocation mode. 00 = same No. subchannel for the selected bands 01 = different No. subchannel for the selected bands 10 = total No. subchannels for the selected bands determined by Nsch code
Reserved	2 bits	
if(Allocation Mode == 0){		
No. Subchannels	8 bits	
} else if(Allocation Mode == 1){		
for (i=0;i< band count ;i++){	-	If Nband is 0, band count is the number of '1' in Band BITMAP. Otherwise band count is Nband.
No. Subchannels	8 bits	Number of subchannels per band
_}		
}		
H-ARQ_Control_IE	<u>variable</u>	

UL-MAP Type

This value specifies the type of the compact UL-MAP IE. A value of 1 indicates the Band AMC Subchannel.

RCID IE

Represent the assignment of the IE.

Nep code, Nsch code

The combination of Nep code and Nsch code indicates the number of allocated subchannels and scheme of coding and modulation for the UL burst.

Nband

Indicates the number of bands selected for the burst. If this value is set to 0, the Band BITMAP is used to indicate the number and the position of selected bands instead. The number of the maximum logical bands determines the length of this field.

Band BITMAP

This BITMAP is valid when Nband is 0. The n-th LSB of the Band BITMAP is set to 1 when the n-th logical band is selected for the burst. If the number of the maximum logical bands is 12 then the length of the Band BITMAP is 12 bits. The band count is set to the number of '1's in the Band BITMAP. The number of the maximum logical bands determines the length of this field.

Band Index

This value indexes the selected band offset and is valid when Nband is larger than 0. The number of the maximum logical bands determines the length of this field.

Allocation Mode

This value indicates the subchannel allocation mode in the selected bands.

The value is set to binary 00 when the same numbers of subchannels are allocated in the selected bands by the following field 'No. Subchannels'.

The value is set to 01 when different numbers of subchannels are allocated in each selected bands by the following fields "No. Subchannels".

The value is set to 10 when the total number of subchannels allocated in the selected bands is defined by Nsch code and Nep code. The subchannels fill from the bands with lowest index.

The allocation mode variant is shown in Figure 2.

No. Subchannels

This value indicates the number of subchannels allocated for this burst.

6.3.2.3.44.7.3 Compact UL-MAP IE for Safety Subchannel

Table 17 Compact UL-MAP IE for safety

Syntax	<u>Size</u>	<u>Notes</u>
Compact_UL-MAP_IE() {		
UL-MAP Type = 2	3 bits	
Reserved	<u>1 bit</u>	
RCID_IE	<u>variable</u>	
Nep code	4 bits	
Nsch code	4 bits	
BIN Offset	8 bits	-
H-ARQ Control IE	<u>variable</u>	
}		_

UL-MAP Type

This value specifies the type of the compact UL-MAP IE.

A value of 2 indicates the Safety Subchannel.

UL-MAP append

A value of 1 indicates the uplink access information is appended to the end of the UL-MAP IE.

RCID IE

Represent the assignment of the IE.

Nep code, Nsch code

The combination of Nep code and Nsch code indicates the number of allocated subchannels and scheme of coding and modulation for the burst.

BIN Offset

The offset of the BIN allocated for this UL burst. See appropriate specification.

6.3.2.3.44.7.4 Compact UL-MAP IE for UIUC Subchannel

Table 18. Compact UL-MAP IE for UIUC

Syntax	Size	<u>Notes</u>
Compact_UL-MAP_IE() {		
UL-MAP Type = 4	3 bits	
Reserved	1 bits	
UIUC	4 bits	

RCID_IE	<u>variable</u>	-
No. Subchannels	8 bits	
<u>}</u>		-

UL-MAP Type

This value specifies the type of the compact UL-MAP IE. A value of 3 indicates the UIUC type.

UIUC

This value indicates the usage of this burst.

RCID IE

Represent the assignment of the IE.

No. Subchannels

This value indicates the number of subchannels allocated by the IE.

6.3.2.3.44.7.5 Compact UL-MAP IE for H-ARQ Region Allocation

The H-ARQ ACK region information is delivered through the Compact_UL-MAP_IE as shown in Table 19. SS sends ACK information for H-ARQ enabled DL bursts in the H-ARQ region specified by the IE.

The subchannels in the H-ARQ region are divided into two half-subchannels. The first half-subchannel is composed of first, third and fifth tiles and the second half-subchannel is composed of second, fourth and sixth tiles. In the H-ARQ Region, the 2n-th half-subchannel is the first half-subchannel and the (2n+1)-th half-subchannel is the second half-subchannel of the n-th subchannel.

The H-ARQ enabled SS that receives H-ARQ DL burst at i-th frame should transmit ACK signal through the half-subchannel in the H-ARQ region at (i+j)-th frame. The frame offset 'j' is defined by the "H-ARQ ACK Delay for DL Burst" field in the UCD message. The half-subchannel offset in the H-ARQ Region is determined by the order of H-ARQ enabled DL burst in the H-ARQ MAP. For example, when a SS receives a H-ARQ enabled burst at i-th frame and the burst is n-th H-ARQ enabled burst in the H-ARQ MAP, the SS should transmit H-ARQ ACK at n-th half-subchannel in H-ARQ Region that is allocated by the BS at the (i+j)-th frame.

Table 19 Compact UL-MAP IE for H-ARQ Region

Syntax Syntax	<u>Size</u>	Notes
Compact_UL-MAP_IE () {		
UL-MAP Type = 4	3 bits	H-ARQ_Region_IE
H-ARQ Region Change Indication	<u>1 bit</u>	0: no region change 1: region changed
if(H-ARQ Region Change Indication == 1) {		
OFDMA Symbol offset	8 bits	
Subchannel offset	8 bits	
No. OFDMA Symbols	8 bits	
No. Subchannels	8 bits	
}		
}_		

H-ARQ Region Change Indication

Indicates whether the region for H-ARQ ACK is changed or not.

OFDMA Symbol offset

Subchannel offset

No. OFDMA Symbols

No. Subchannels

Specify the start symbol offset, the start subchannel offset, the number of allocated symbols and the number of subchannels for the H-ARQ acknowledgement region respectively.

6.3.2.3.44.7.6 Compact UL-MAP IE for CQICH Region Allocation

The CQI region information is delivered through the Compact_UL-MAP_IE as shown in Table 20. SS sends CQI report in CQI region.

Table 20 Compact UL-MAP IE for CQI Region

<u>Syntax</u>	Size	Notes
Compact_UL-MAP_IE() {		
UL-MAP Type = 5	3 bits	CQI_Region_IE
CQI Region Change Indication	1 bits	0: no region change 1: region changed
if(CQI Region Change Indication == 1) {		
OFDMA Symbol offset	8 bits	
Subchannel offset	8 bits	
No. OFDMA Symbols	8 bits	
No. Subchannels	8 bits	
3		
<u> </u>		

CQI Region Change Indication

Indicates whether the region for CQI is changed or not.

OFDMA Symbol offset

Subchannel offset

No. OFDMA Symbols

No. Subchannels

Specify the start symbol offset, the start subchannel offset, the number of allocated symbols and the number of subchannels for the CQI report region respectively.

6.3.2.3.44.7.6 Compact UL-MAP IE for Extension

Table 21 Compact UL-MAP IE for extension

Syntax	<u>Size</u>	<u>Notes</u>
Compact_UL-MAP_IE() {		
UL-MAP Type = 7	3 bits	
UL-MAP Sub-Type	5 bits	
Length	4 bits	Length of this IE in Bytes
Payload		Sub-type dependent payload
}	-	-

UL-MAP Type

This value specifies the type of the compact UL-MAP IE. A value of 7 indicates the extension type.

UL-MAP Sub-Type

This value specifies the sub-type of the compact UL-MAP IE.

Length

This indicates the length of this IE in Bytes. If a SS can't recognize the UL-MAP Sub-Type, it skips the IE.

Payload

The payload depends on the value of UL-MAP Sub-Type. The length of payload is Length -1 Bytes.

Add the following rows to table 365:

Name	Type	Length	Value
Band AMC Allocation Threshold	<u>157</u>	1	dB unit
Band AMC Release Threshold	<u>158</u>	1	dB unit
Band AMC Allocation Timer	<u>159</u>	1	Frame unit
Band AMC Release Timer	<u>160</u>	1	Frame unit
Band Status Reporting MAX Period	<u>161</u>	1	Frame unit
Band AMC Retry Timer	<u>162</u>	<u>1</u>	Frame unit
Safety Channel Allocation Threshold	<u>163</u>	1	dB unit
Safety Channel Release Threshold	<u>164</u>	1	dB unit
Safety Channel Allocation Timer	<u>165</u>	1	Frame unit
Safety Channel Release Timer	<u>166</u>	1	Frame unit
Bin Status Reporting MAX Period	<u>167</u>	1	Frame unit
Safety Channel Retry <u>Timer</u>	<u>168</u>	1	Frame unit
H-ARQ ACK delay for UL burst	<u>169</u>	1	1 = 1 frame offset 2 = 2 frame offset 3 = 3 frame offset
CQICH Band AMC Transition Delay	<u>170</u>	1	Frame unit

Add the following rows to Table 312—DCD channel encoding:

Name	Type	Length	Value
H-ARQ ACK delay for DL burst	<u>17</u>	1	1 = 1 frame offset 2 = 2 frame offset 3 = 3 frame offset

[Change the table in section 11.8.3.7.2 and 11.8.3.7.3]

11.8.3.7.2 OFDMA SS demodulator

Type	Length	Value	Scope
5.12.2	1	bit #0: 64-QAM bit #1: BTC bit #2: CTC bit #3: STC bit #4: AAS bit #5-7: Reserved bit #5: H-ARQ bit #6-7: Reserved, shall be set to 0	SBC-REQ (see 6.4.2.3.23) SBC-RSP (see 6.4.2.3.24)

11.8.3.7.3 OFDMA SS modulator

Type	Length	Value	Scope
5.12.2	1	bit #0: 64-QAM bit #1: BTC bit #2: CTC	SBC-REQ (see 6.4.2.3.23) SBC-RSP
		bit #3-7: Reserved, set to 0 bit #3: H-ARQ bit #4-7: Reserved, shall be set to 0	(see 6.4.2.3.24)

11.8.3.7.4 OFDMA SS modulator

Type	Length	Value	Scope
5.12.3	1	The number of HARQ ACK Channel	SBC-REQ (see 6.4.2.3.23) SBC-RSP (see 6.4.2.3.24)

[Change the table in section 11.11 and 11.12]

11.11 TLV of REP-REQ

Add the following parameters:

Type	Length	Value
<u>2.1</u>	1	<u>00</u> = Normal subchannel,
		01 = Band AMC Channel,
		10 = Safety Channel,
		11 = Reserved for future

11.12 TLV of REP-RSP

Add the following parameters:

REP-REQ	<u>Name</u>	<u>Type</u>	<u>Length</u>	Value
---------	-------------	-------------	---------------	-------

Channel Type request in WirelessMAN- OFDMA PHY				
	Reported Channel Type	2.1	1	00 = Normal subchannel,
				01 = Band AMC Channel, 10 = Safety Channel, 11 = Reserved for future
Channel Type type in WirelessMAN-OFDMA PHY = 00	Normal subchannel Report	2.2	1	First 5 bits for the CINR measurement report and the rest for don't care
Channel Type type in WirelessMAN-OFDMA PHY = 01	Band AMC Report	2.3	4	First 12 bits for the band indicating bitmap and Next 25 bits for CINR reports (5 bits per each band)
Channel Type type in WirelessMAN-OFDMA PHY = 10	Safety Channel Report	2.3	<u>5</u>	The first 20 bits for the reported bin indices and the next 20 bits for CINR reports (5 bits for each bin)