

Cl 00 SC 0 P L # 48  
Ganga, Ilango Intel

Comment Type TR Comment Status A  
The references to 802.3 document is based on IEEE Std 802.3-2008. Should the document be aligned to the revised references as per the 802.3REV project that is already in sponsor ballot.

For example, see the following reference in 13.5 ifMauFECmode object, REFERENCE "IEEE Std 802.3. 30.5.1.1.14 aFECMode." However this reference has been changed to 30.5.1.1.16 in 802.3 Revision.

SuggestedRemedy  
Align this document references to 802.3REV project that is already in sponsor ballot.

Response Response Status U  
ACCEPT.  
See details in response to comment # 23.

Cl 13 SC 13.5 P239 L3 # 50  
Ganga, Ilango Intel

Comment Type TR Comment Status A  
Update dot3StatsSymbolErrors object description as per changes specified in 30.3.2.1.5 aSymbolErrorDuringCarrier(see 802.3ba).

SuggestedRemedy  
As per comment

Response Response Status W  
ACCEPT IN PRINCIPLE.  
Add the following text:  
For operation at 10 Gb/s, 40 Gb/s, and 100 Gb/s, it is a count of the number of times the receiving media is non-idle (the time between the Start of Packet Delimiter and the End of Packet Delimiter) for a period of time equal to or greater than minFrameSize, and during which there was at least one occurrence of an event that causes the PHY to indicate "Receive Error" on the XGMII, the XLGMII, or the CGMII.

Cl 13 SC 13.5 P370 L18 # 47  
Ganga, Ilango Intel

Comment Type TR Comment Status A  
Update ifMauFECMode object description as per changes specified in 30.5.1.1.14 (see 802.3ba).

For example change "10GBASE-R FEC control register" to "BASE-R FEC control register"

SuggestedRemedy  
As per comment

Response Response Status W  
ACCEPT IN PRINCIPLE.  
See response to comment # 21.

*Cl* 13    *SC* 13.5    *P*370    *L*23    # 49  
Ganga, Ilango    Intel

*Comment Type*    **TR**    *Comment Status*    **A**

Update ifMauFECCorrectedBlocks object description as per changes specified in 30.5.1.15 (see 802.3ba). This is an array of counters for 40 and 100 GbE.

Update ifMauFECUnCorrectableBlocks object description as per changes specified in 30.5.1.16 (see 802.3ba). This is an array of counters for 40 and 100 GbE.

*SuggestedRemedy*

As per comment

*Response*    *Response Status*    **W**

ACCEPT IN PRINCIPLE.

Update ifMauFECCorrectedBlocks:

Change object type to an array.

Change reference to 30.5.1.17 (already addressed in comment #23)

Change description to read:

For 1000BASE-PX, 10/40/100GBASE-R PHYs, an array of corrected FEC block counters. The counters will not increment for other PHY types. The indices of this array (0 to N-1) denote the PCS lane number where N is the number of PCS lanes in use. The number of PCS lanes in use is set to one for PHYs that do not use PCS lanes. Each element of this array contains a count of corrected FEC blocks for that PCS lane.

Update ifMauFECUnCorrectableBlocks:

Change object type to an array.

Change reference to 30.5.1.18 (already addressed in comment #23)

Change description to read:

For 1000BASE-PX PHYs or 10/40/100GBASE-R PHYs, an array of uncorrectable FEC block counters. The counters will not increment for other PHY types. The indices of this array (0 to N-1) denote the PCS lane number where N is the number of PCS lanes in use. The number of PCS lanes in use is set to one for PHYs that do not use PCS lanes. Each element of this array contains a count of uncorrectable FEC blocks for that PCS lane.

*Cl* 99    *SC*    *P*5    *L*1    # 46  
Ganga, Ilango    Intel

*Comment Type*    **ER**    *Comment Status*    **A**

Update the introduction to include list of additions to 802.3.1-2011 covered by this revision.

*SuggestedRemedy*

Update introduction to include list of additions covered by this revision as stated in the project objectives.

*Response*    *Response Status*    **W**

ACCEPT IN PRINCIPLE.

Add a paragraph that describes the amendments pulled in to this revision.