

Meeting Notes

June 15, 2012

Hosted by John D'Ambrosia, Dell

Meeting Notes prepared by John D'Ambrosia

Attendees

John D'Ambrosia, Dell

Wheling Cheng, Juniper

John Nolan

Kent Lusted, Intel

Adam Healey, LSI

Adee Ran, Intel

Alexander Umnov , Huawei

Andy Zambell, FCI

Beth Kochuparambil, Cico

Catalin Muntean, Meritec

Charles Moore, Avago

Chris Di Minico, 3M Communications

Dave Chalupsky, Intel

Galen Fromm, Cray

Guarav Malhotra, Xilinx

Jeff Slavick Avago

Jim Keeley, LSI

Joel Goergen, Cisco

John Ewen, IBM

Liav Ben-Artzi, Marvell

Luis Armenta, Ansys

Marc Dupuis, TEC / Madison Cable

Megha Shanbhag, TEC

Pete Anslow, Ciena

Piers Dawe, IP Tronics

Rich Mellitz, Intel

Scott Irwin, MoSys

Stephen Bates, PMC Sierra

Subramanian Lalguida, Ansys

Tom Palkert , Molex / Xilinx

Vasu Parthasarathy, Broadcom

Vivek Telang, Broadcom

Will Bliss, Broadcom

Mike Dudek, QLogic

Essaid Bensoudane, Huawei

Dan Dove, Applied Micro
Mark Bugg, Molex
Pavel Zivny, Tektronix
Patrick Satarzadeh, Xilinx
Andreas Lenkisch, Schroff
Bob Wagner, Panduit
Myles Kimmitt, Emulex

Reviewed dambrosia_12_0615.pdf

Discussed resolution of channel methodology, specifications, and interaction with schedule.

Discussed Test Points (TP0 / TP5 versus TP0 through TP5 with intermediate test points)

Some discussion regarding consensus around TP0/TP5

Not clear where the cap should go, and impact on TP5

Not sure what to do with intermediate test points

Methodology

Test points will be different between backplane specs and cabling specs