### 33.2.4.4 Variables

$\mathrm{I}_{\text {Inrush-2P }}$
Output current per pairset during POWER_UP (see Table 33-11 and Figure 33-13).
$\mathrm{I}_{\text {Port-2P }}$
Output current on a pairset (see 33.2.7.6)
$\mathrm{I}_{\text {Port-2P-other }}$
Output current on the other pairset, defined as $\mathrm{I}_{\text {Port-2P-other }}=\mathrm{I}_{\text {Port }}-\mathrm{I}_{\text {Port-2P }}$
$I_{\text {Port }}$
Total output current (see 33.2.7.6)

### 33.2.7.4 Continuous output current capability in the POWER_ON state

PSEs shall meet I con as specified in Table 3311. Type 3 and Type 4 PSEs when connected to a single-signature PD shall meet ICon-2P as speeified in Table 33-11 item-4a.
PSEs connected to a single-signature PD shall be able to source $\mathrm{I}_{\text {Con }}$ and $\mathrm{I}_{\text {Con-2P_unb }}$ as specified in Table 33-11. PSEs connected to a dual-signature PD shall be able to source $\mathrm{I}_{\text {Con }}$ on each pairset as specified in Table 33-11.
$\mathrm{I}_{\text {Con }}$ is the total current of both pairs with the same polarity that a PSE has to support. $\mathrm{I}_{\text {Con-2P_unb }}$ is the maximum current the PSE is required to support over one of the pairs of same polarity under E2EP2PRunb condition in the POWER_ON state.
When connected to single-signature PDs, $\mathrm{I}_{\text {Con }}$ is the total current of both pairs with the same polarity that a PSE must be able to source. $I_{\text {Con-2P_unb }}$ is the maximum current the PSE must be able to source over one of the pairs of same polarity at maximum current unbalance condition in the POWER_ON state.

### 33.2.7.7 Output current at short circuit condition

## Replace Figure 33-14 by:

Equation 33-6a, Equation 33-7a and Figure 33-14a apply to PSEs that operate in 2-pair mode, as well as to Type 3 and Type 4 PSEs connected to dual-signature PDs.
Equation 33-6b, Equation 33-7b and Figure 33-14b apply to Type 3 PSEs connected to single-signature PDs, operating in 4-pair mode.
Equation 33-6c, Equation 33-7c and Figure 33-14c apply to Type 4 PSEs connected to single-signature PDs, operating in 4-pair mode.

Figure 33-14a


Figure 33-14b


$$
I_{\text {PSEUT-2P }}=\left\{\begin{array}{ll}
50 & \text { for }\left(0 \leq t<10.0 \times 10^{-6}\right)  \tag{33-6b}\\
\sqrt{\frac{K}{t}} & \text { for }\left(10.0 \times 10^{-6} \leq t<8.20 \times 10^{-3}\right) \\
1.75 & \text { for }\left(8.20 \times 10^{-3} \leq t<T_{\text {CUT-2P }} \max \right) \\
0.8 & \text { for }\left(T_{\text {CUT-2P }} \max \leq t\right)
\end{array}\right\}
$$

$$
I_{\text {PSELT-2P }}=\left\{\begin{array}{ll}
I_{\text {LIM-2P }} \min & \text { for }\left(0 \leq t<T_{\text {LIM-2P }} \min \right)  \tag{33-7b}\\
I_{\text {Peak-2P }} & \text { for }\left(T_{\text {LIM-2P }} \min \leq t<T_{\text {CUT-2P }} \min \right) \\
\min \left(I_{\text {Con }}-I_{\text {Port-2P-other }}, I_{\text {Con-2P_unb }}\right) & \text { for }\left(T_{\text {CUT-2P }} \min \leq t\right)
\end{array}\right\}
$$

Figure 33-14c


