

**Comments 158, 160**

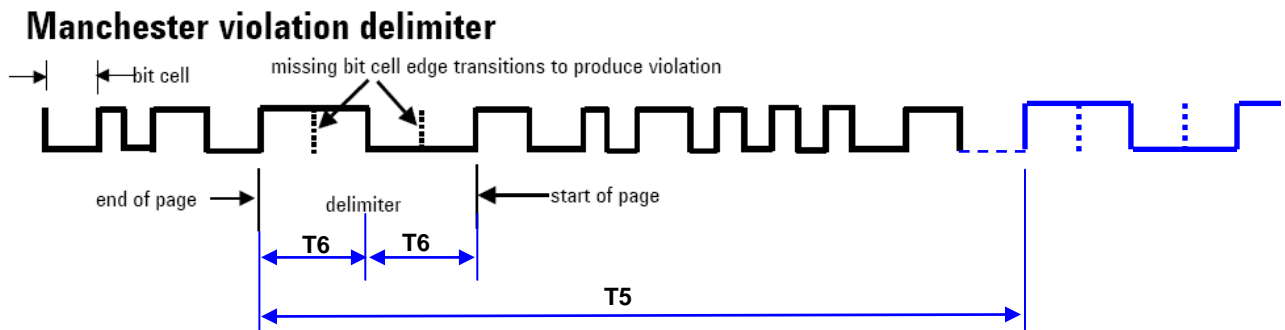
Modify the Table 73-2 in 802.3ap-D0.91 page 128 as shown below:

**Table 73-2 DME page timing summary**

#	Parameter	Min	Typ	Max	Units
T1	Transition position spacing (period)		3.2+/- 0.01%		ns
T2	Clock transition to Clock transition	6.2	6.4	6.6	ns
T3	Clock transition to Data transition (Data=1)	3.0	3.2	3.4	ns
T4	Transitions in a DME page	51		100	#
T5	DME page width	338.8	339.2	339.6	ns
T6	DME Manchester Violation delimiter width	12.6	12.8	13.0	ns

**Comment #168**

Add the following figure after Fig 73-3 on page 128 (modified from thaler\_01\_0105 page 13 to include T5, T6)

**Comment #159**

Modify figure 73-8 Receive state diagram as follows:

....Continued on Page 2

Add new Timer as shown below:

clock\_detect\_min\_timer = min=4.8ns, max=6.4ns-200ps=6.2ns

clock\_detect\_max\_timer = min=6.4ns+200ps=6.6ns, max=6.4ns+1.6ns=8.0ns

Modify figure 73-8 Receive state diagram as follows:

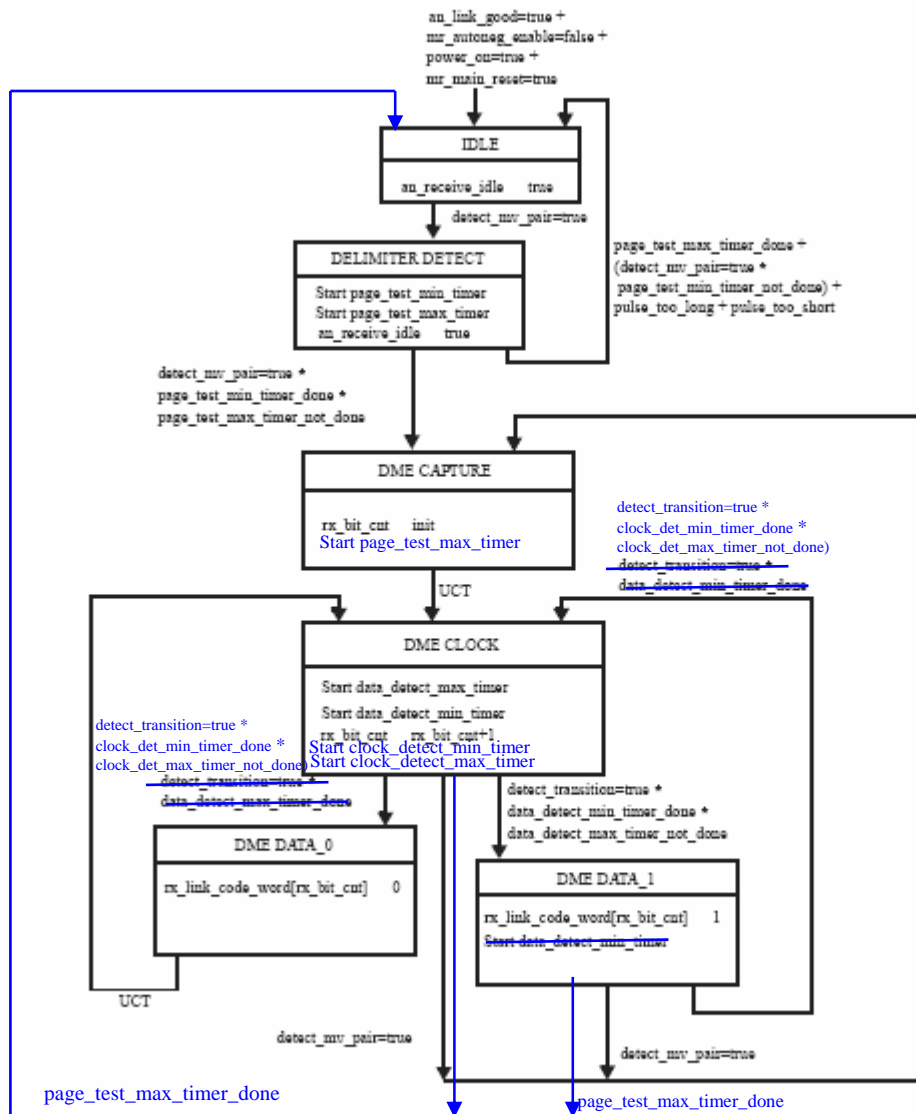


Figure 73-8—Receive state diagram