

# Status of 802.3ap Document

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# IEEE 802.3ap Draft 0.91 Doc

- 187 comments received against Draft 0.91
- From 13 individuals and 10 Companies
- Rearranged registers in Clause 45 to be compatible with 802.3an

# Comments on Dr.0.8

Comment	TR	T	E	Total
Clause 01-44	0	0	1	1
Clause 45	0	8	1	9
Clause 69	2	18	9	29
Clause 70	1	21	13	35
Clause 71	1	13	6	20
Clause 72	9	29	9	47
Clause 72A	0	2	1	3
Clause 73	1	27	15	43
Clause 73A	0	0	0	0
Total	15	118	55	187

# TR's and T's (!)

## General Concepts;

- AC coupling / on chip
- Need link block diagrams

## CI 45:

- Naming and use of coefficient update register

## CI 69:

- New informative channel concept (69.3) from Rich Mellitz
- Crosstalk equation vs. ICR
- Remove shall's. No PICS
- Move annex 72A to annex 69A

# TR's and T's (2)

## CI 70:

- **Remove synchronization process. It is the same as Clause 36**
- **Correct cut and past errors (multiple lanes -> single lanes)**
- **Differential output return loss minimum and remove equations**
- **CJPAT:**
  - Option A: Restructure the pattern such that it is the equivalent of a CJPAT sequence on a single lane only.
  - Option B: Reference the Jitter pattern defined by EFM in Clause 59.7.1.
  - Option C: Define a new jitter frame.
  - Option D: Keep the existing frame.
  - Option E: Reference the pattern specified in 36A.
- **add new sub clause to 70.7.2 stating:**
  - Compliant Receiver shall pass Interference tolerance test as defined in annex 69A with

# TR's and T's (3)

## CI 71:

### Add signaling speed

- add new sub clause to 71.6.2.1 stating:
  - Compliant Receiver shall pass Interference tolerance test as defined in annex 69A with ...

## CI 72:

- PMD MDIO function mapping is missing.
- The PMD Signal Detect function is awaiting proposed text
- Adopt definitions for PMD\_fault, PMD Transmit Fault, and PMD Receive Fault as written.
- Control channel length bit pattern
- Coefficient update operations
- Common mode output return loss is missing
- The test criteria does not define C0 values or ranges or resolution
- Max 1200 mV launch signal could be 1400mV with -8 dB return loss
- Add common-mode return loss add new sub clause to 71.6.2. stating:
  - Compliant Receiver shall pass Interference tolerance test as defined in annex 69A with

# TR's and T's (4)

## CI 72A:

- Delete “data like” test

## CI 73:

- DME tolerance is too tight
- Add Manchester violation delimiter
- Provide priority resolution if both clauses, 37 and 73, are present
- Make parallel detection happen before ability detection