

Unconfirmed Meeting Minutes
IEEE P802.3bq Channel Model Ad Hoc

April 8th, 2014

Prepared by Dave Chalupsky, Pete Cibula, and Brad Booth

Meeting Agenda:

- 1) Roll call - Record attendance, attendees' names and affiliations
- 2) Reminder of IEEE patent policy: www.ieee802.org/3/patent.html
- 3) Housekeeping:
 - a) Review & approve meeting agenda.
 - b) Approve minutes from Feb 18 & March 4 meetings
- 4) New business for the April 8th ad hoc meeting as follows:
 - a) Channel Modeling ad hoc sub-team updates with discussion – status of new/ongoing work
 - PCB transmission lines and noise for 10GBASE-T systems (B. Booth and P. Cibula)
 - MDI-to-MDI cabling channel (C. DiMinico and W. Larsen)
 - MDI and isolation path (G. Zimmerman)
 - b) New contributions with discussion
 - Supplemental Measurements of System Background Noise in 10GBASE-T Systems, Pete Cibula & Dave Chalupsky, Intel.
 - c) General discussion on system background noise and other channel impairments
- 5) General Discussion and meeting wrap-up
 - a) Review action items from this meeting
 - b) Next steps/future meetings.

The 16th meeting of the P802.3bq Channel Modeling Ad Hoc was called to order at 8:05 AM Pacific Standard Time.

- 1) Meeting chaired by Dave Chalupsky in Pete's absence
- 2) Voice roll call take and participants were asked to email meeting chair (reproduced in the attendance record at the bottom of these minutes).
- 3) Participants were reminded of the IEEE's patent policy. Those not familiar with it were directed to the URL above.
- 4) Houskeeping & general updates:
 - a) The agenda was reviewed with those in attendance. Attendees were asked to note any corrections or additions. None were noted.
 - c) Approve minutes from Feb 18 & March 4 meetings. Minutes from the last meeting are available on ad hoc web page. Dieter Schicketanz noted that Feb 18th meeting made a request for information on shielding, bonding & grounding and that information was provided in his presentation on March 4th. March 4th minutes updated to acknowledge that.

- 5) No specific updates were provided by ad hoc subteams.
- 6) The meeting was then opened to hear new business for the ad hoc meeting as follows:
 - Supplemental Measurements of System Background Noise in 10GBASE-T Systems, Pete Cibula & Dave Chalupsky, Intel. Updated presentation adds noise floor extensions to the measurement technique, and data from several additional product ports types.
 - d) Discussion: George has a few items of clarification for follow up with Pete...
 - e) *Action Item: George requests PHY implementers provide threshold values for pk-to-pk and broadband noise. What levels matter? When is this background noise measurement good enough? Discussion that measurement sensitivity ~-150dBm/Hz is sufficient. Need more thought on the peak to peak noise though. (George & Peter to discuss.)*
- 7) Closing discussion – Action item review and future meetings
 - a) Meeting wrap-up - The next meeting has been scheduled for April 22, 2014, 8am Pacific Time.

The P802.3bq Channel Modeling Ad Hoc meeting was adjourned at 9:10 PM Pacific Time.

Meeting Attendance 4/8/14

Name	Employer	Affiliation (if different)
David Chalupsky	Intel	
Jerry Chiang	Foxconn Interconnect Technology	
Dave Jeskey	Sentinel Connector Systems	
Rich Mellitz	Intel	
Bob Wagner	Panduit	
Wayne Larsen	Commscope	
Victor Renteria	BelFuse/TRP	
Martin Rossbach	Nexans	
Dieter Schicketanz	University of Science, Reutlingen	Leoni Kerpen
Tom Souvignier	Broadcom	
Peter Wu	Marvell	
George Zimmerman	CME Consulting	Aquantia, Commscope