Approved minutes
P802.3bs 200 Gb/s and 400 Gb/s Ethernet SMF Ad Hoc Teleconference 16 May 2017

Minutes taken by Pete Anslow, Ciena

The meeting started at 8:10 am Pacific chaired by Pete Anslow, the attendee list was taken from the Webex attendee list plus any e-mail notifications of attendance.

Documentation for the call can be found at the Ad Hoc web page:
http://www.ieee802.org/3/bs/public/adhoc/smf/index.shtml

Pete reminded everyone of the IEEE patent policy (http://www.ieee802.org/3/patent.html) and asked if anyone was unfamiliar with it. No one responded.

Pete also noted the updated IEEE 802 participation slide (http://www.ieee802.org/devdocs.shtml) and asked if anyone was unfamiliar with it. No one responded.

Pete asked if anyone had any objection or additions to the draft agenda. There was no response, so the agenda was approved by the Ad Hoc.

Pete asked if anyone had any corrections to the draft minutes from the 25 April 2017 call. No one responded, so these minutes were approved by the Ad Hoc.

Presentation #1
Title: P802.3bs optical reflection limits
Presenter: Pete Anslow, Ciena
See   anslow_01_0517_smf

Presentation #2
Title: Improved extinction ratio specifications
Presenter: Piers Dawe, Mellanox
See   dawe_01_0517_smf

In discussion of the alternative methods of accommodating the 0.1 dB increase in MPI penalty for FR and LR, there was a preference for increasing the OMAouter – TDECQ value by 0.1 dB for transmitters with ER lower than 4.5 dB.

Presentation #3
Title: 100G/λ TDECQ analysis 53.1 GBd MZM Tx
Presenter: Jonathan King, Finisar
See   king_01_0517_smf

Presentation #4

Title:	Proposed reference equalizer change in Clause 124 –updated results
Presenter:	Marco Mazzini, Cisco
See	mazzini_01_0517_smf

This presentation was an update of mazzini_01_0417_smf related to comment r01-21 against P802.3bs D3.1. Marco had updated page 4 of the presentation, so this was uploaded after the call as mazzini_01a_0517_smf. While there was still some consensus for increasing the length of the reference equalizer for 400GBASE-DR4, there were still differing views on how many taps should be specified.

The meeting closed at 10:15 am Pacific.

Attendee list (taken from Webex attendee list plus any e-mail notifications of attendance)

Anand Anandakumar, MaxLinear

Pete Anslow, Ciena

Derek Cassidy, BT (Affiliation ICRG)

Frank Chang, Inphi

Jaclyn Dang, Cisco

Piers Dawe, Mellanox

Stephen Didde, Keysight

Mike Dudek, Cavium

Ali Ghiasi, Ghiasi Quantum LLC, Huawei

Drew Guckenberger, Luxtera

Kenneth Jackson, Sumitomo

Mark Kimber, Semtech

Jonathan King, Finisar

Bill Kirkland, Semtech

Paul Kolesar, CommScope

Greg LeCheminant, Keysight

David Lewis, Lumentum

Hai-Feng Liu, Intel

Karen Liu, Kaiam

David Malicoat, Senko

Marco Mazzini, Cisco

Gary Nicholl, Cisco

Feng Pan, Luxshare-ICT

Thang Pham, Finisar

David Piehler, Dell EMC

Adee Ran, Intel

Peter Stassar, Huawei

Bharat Tailor, Semtech

Nathan Tracy, TE Connectivity

Stephen Trowbridge, Nokia

Winston Way, NeoPhotonics

Martin White, Cavium