Cl **45**   *SC* **45.2.1.186c.4**   *P* **42**   *L* **44**   # 337

Thompson, Geoff   GraCaSI S.A.

Comment Type   **TR**   *Comment Status*   **R**   *PMA*

The behavior coming out of sleep is not implementation specific, it is governed by what happens upon reset.

SuggestedRemedy

Fix text.

Response   *Response Status*   **U**

REJECT.

While often confused with sleep mode or EEE mode, low-power mode is neither. It is a standard low-power state where the PHY is only responsive to MDIO, and exit requires a reset (and therefore retraining, per the PHY control diagram). It is mirrored in the PMA control bit 1.0.11, the PMA/PMD control 1 register - common to most PHYs. The low-power mode functionality specified in 802.3cg is specified in other PHY clauses throughout 802.3, including clause 28, clause 36, clause 37 and clause 97 (1000BASE-T1), with identical or nearly identical specification of the implementation-specific nature of the function.

Commenter and Chair are encouraged to submit a maintenance request to deal with this confusion globally.

TYPE: TR/technical required ER/editorial required GR/general required T/technical E/editorial G/general
COMMENT STATUS: D/dispatched A/accepted R/rejected   RESPONSE STATUS: O/open W/written C/closed U/unsatisfied Z/withdrawn
SORT ORDER: Clause, Subclause, page, line

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