Comment Type: T  Comment Status: X  late

[PHY Capability Bits]: PHY Vendors need to communicate vendor specific information between the two link partners. Most previous BASE-T standards provided such capability, but currently 802.3ch does not provide it.

SuggestedRemedy

Replace paragraph on page 141, line 50 with the following:
The format of PHY capability bits is Oct10<0> = OAMen, Oct10<2:1> = InterleaverDepth, Oct10<4:3> = PrecodeSel, Oct10<5> = SlowWakeRequest, Oct10<6> = EEEen and Oct10<7> = VendorSpecificMessage. EEEen and OAMen indicate EEE and MultiGBASE-T1 OAM capability enable, respectively. The PHY shall indicate the support of these two optional capabilities by setting the corresponding capability bits. When the VendorSpecificMessage bit is set to 1 then the remaining 23 bits of the MSG24 field is vendor specific data. Otherwise when VendorSpecificMessage=0, the remaining bits shall be reserved and set to 0.

Proposed Response  Response Status: O
In test mode 3, the PCS generates continuous pattern of \( \{0,3\} \) symbols into the precoder. The precoder output is then mapped into PAM4. This paragraph should be rephrased to make it clear. The proposed change is based on discussions with George.

**Suggested Remedy**

Change this paragraph to:

"Test mode 3 is for testing the precoder operation. When test mode 3 is enabled, the PCS shall generate a continuous pattern of \( \{0, 3\} \) symbols to be input to the transmit precoder specified in 149.3.2.2.19, to be precoded according to the Transmit precoder settings as determined by the value set in register 1.2309:10:9, or equivalent functionality if MDIO is not implemented, and transmitted by the PMA timed from its local clock source."

**Comment Status** X

**Response Status** O

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The MDI return loss specification as shown in Equation 149-27 is unnecessarily restrictive.

**Suggested Remedy**

See the proposal on the last page of "vakilian_3ch_01_0719.pdf".

**Proposed Response**

**Response Status** O