

Options for 98B.3 Technology Ability Field bit assignments

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IEEE802.3ch

802.3cgD2p0/chD0p5

bit	Selector description
A0	100BASE-T1
A1	Reserved
A2	1000BASE-T1
A3	Reserved
A4	10BASE-T1L
A5	Reserved
A6	10BASE-T1S
A7	2.5GBASE-T1
A8	5GBASE-T1
A9	10GBASE-T1
A10 – A19	Reserved

Proposal rationale

bit	Selector description
A0	100BASE-T1
A1	Reserved 10BASE-T1S
A2	1000BASE-T1
A3	Reserved 2.5GBASE-T1
A4	10BASE-T1L 5GBASE-T1
A5	Reserved 10GBASE-T1
A6	10BASE-T1S Reserved (25GBASE-T1)
A7	2.5GBASE-T1 Reserved (50GBASE-T1)
A8	5GBASE-T1 Reserved (100GBASE-T1)
A9	10GBASE-T1 10BASE-T1L
A10	Reserved (100BASE-T1L)
A11	Reserved (1000BASE-T1L)
A12	Reserved (2.5GBASE-T1L)
A13	Reserved (5GBASE-T1L)
A14	Reserved (10GBASE-T1L)
A15	Reserved (25GBASE-T1L)
A16	Reserved (50GBASE-T1L)
A17	Reserved (100GBASE-T1L)
A18 – A19	Reserved

802.3cgD2p1/chD0p6 if
comments accepted

bit	Selector description
A0	100BASE-T1
A1	10BASE-T1S
A2	1000BASE-T1
A3	2.5GBASE-T1
A4	5GBASE-T1
A5	10GBASE-T1
A6 – A8	Reserved
A9	10BASE-T1L
A10 – A19	Reserved