```
2
                      REVISION REQUEST
3
4
    DATE: 5/10/2019
5
   NAME: James A. McIntosh
6
    COMPANY/AFFILIATION: Microhip
7
   E-MAIL: james.mcintosh@microchip.com
8
9
   REQUESTED REVISION:
10
     STANDARD: 802.3-2018
11
     CLAUSE NUMBER: 45.2.1.191.1
12
     CLAUSE TITLE: Test mode control (1.2308.15:13)
13
   PROPOSED REVISION TEXT:
14
15
    Change register number reference in text from "1.2307.15:13" to
16
    "1.2308.15:13".
17
18
    Also correct R/W type in Table 45-155 for bits 15:13.
19
    Change from "RO" to "R/W".
20
21
22
   RATIONALE FOR REVISION:
23
24
    The register number is simply wrong.
25
26
    The R/W type is also wrong for a control register and is assumed to be
27
    an oversight.
28
29
30
    IMPACT ON EXISTING NETWORKS:
31
32
    I assume no impact. Anyone implementing the control register would have
33
    had to implement it as R/W already.
34
35
36
    +----
37
    |Please attach supporting material, if any
    |Submit to:- David Law, Chair IEEE 802.3
38
39
    |and copy:- Adam Healey, Vice-Chair IEEE 802.3
40
41
            E-Mail: stds-802-3-maint-req@ieee.org
   |At:-
42
43
                +-----+
44
                | REV REQ NUMBER: 1345
                | DATE RECEIVED: May 10, 2019
45
                | EDITORIAL/TECHNICAL
46
47
               | ACCEPTED/DENIED
48
               | BALLOT REQ'D YES/NO
   | COMMENTS:
49
50
    +-----
51
   | For information about this Revision Request see -
52
   | http://www.ieee802.org/3/maint/requests/revision history.html#REQ1345 |
53
```