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2
                     REVISION REQUEST
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    +----
    DATE: May 9, 2023
5
    NAME: Andras de Koos
6
    COMPANY/AFFILIATION: Microchip Technology Inc.
7
   E-MAIL: andras.dekoos@microchip.com
8
9
   REQUESTED REVISION:
10
     STANDARD: 802.3-2022
11
     CLAUSE NUMBER: 119
12
     CLAUSE TITLE: Physical Coding Sublayer (PCS) for 64B/66B, type
13
   200GBASE-R and 400GBASE-R
   PROPOSED REVISION TEXT:
15
    In "Table 119-5-MDIO/PCS status variable mapping", the row for
    "Lane x aligned" refers to the PCS variable "am lock<x>".
16
17
    However, no such variable exists in Clause 119.
18
    It should map to the "amps lock<x>" variable in clause 119.
19
    "am lock<x>" exists for clauses 82/91.
20
21
22
    Note that the same typo exists in the MII-Extender Clause:
23
    Table 118-2-MDIO PHY XS to Clause 119 status variable mapping
24
    Table 118-4-MDIO DTE XS to Clause 119 status variable mapping
25
26
27
   RATIONALE FOR REVISION:
28
   Seems like a typo, and an obvious one at that. But doing a text search
29
    for amps lock<x>, one could mistakenly conclude that the clause 119
30
    amps lock < x > is not actually mapped to MDIO registers.
31
32
33
34
    IMPACT ON EXISTING NETWORKS:
35
    None.
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37
38
39
   |Please attach supporting material, if any
   |Submit to:- David Law, Chair IEEE 802.3
|and copy:- Adam Healey, Vice-Chair IEEE 802.3
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43
    |At:- E-Mail: stds-802-3-maint-req@ieee.org
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45
                +------ For official use -----+
46
                | REV REQ NUMBER: 1403
47
                | DATE RECEIVED: 9 May 2023
48
                | EDITORIAL/TECHNICAL
49
                | ACCEPTED/DENIED
                | BALLOT REQ'D YES/NO
| COMMENTS:
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51
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   | For information about this Revision Request see -
54 | http://www.ieee802.org/3/maint/requests/revision history.html#REQ1403 |
55 +------
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