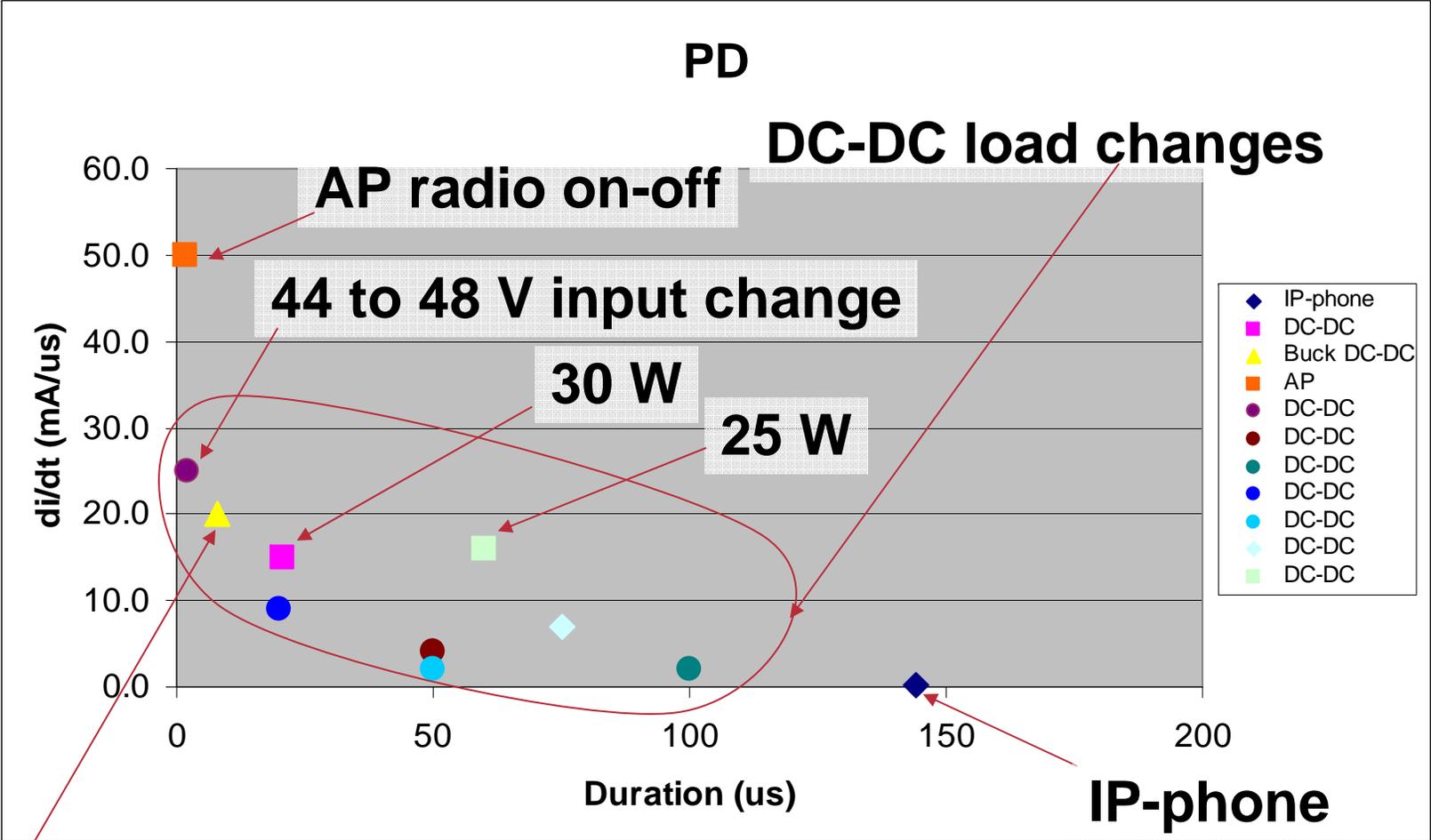
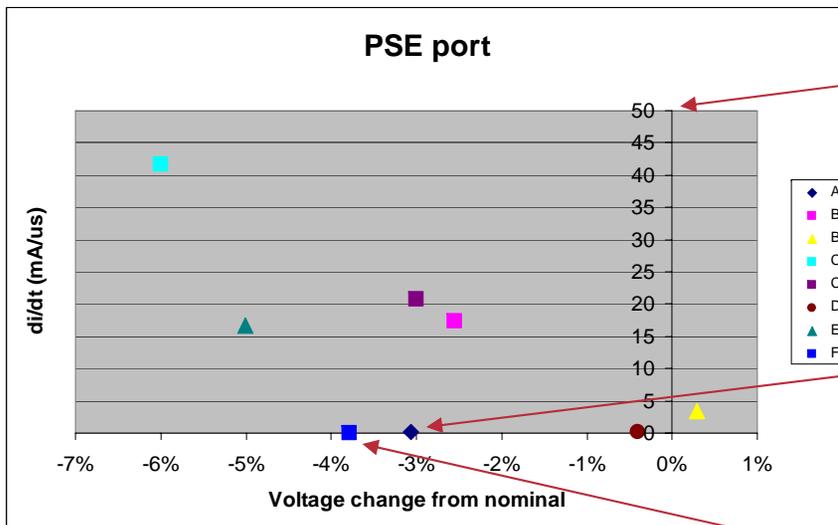


Sample PD di/dt values



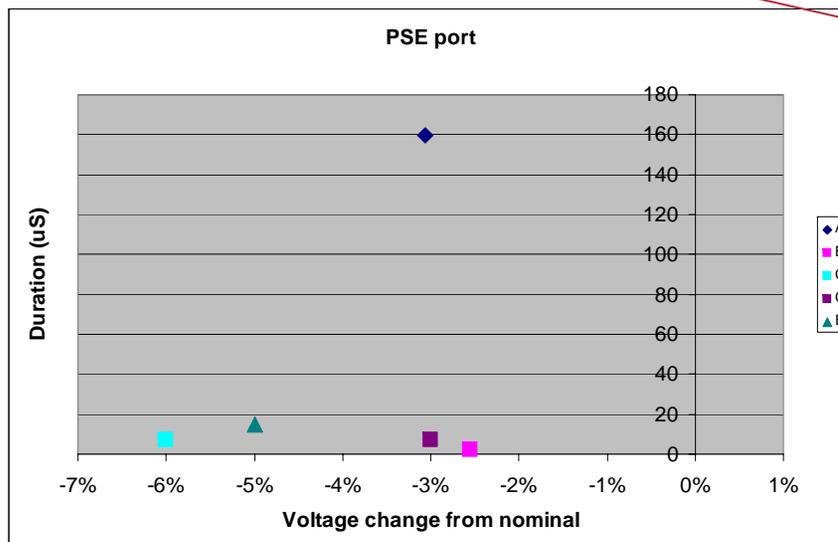
Output capacitor 1/4 of the recommend size

Sample PSE load change effects



Highest PD di/dt from sample set.

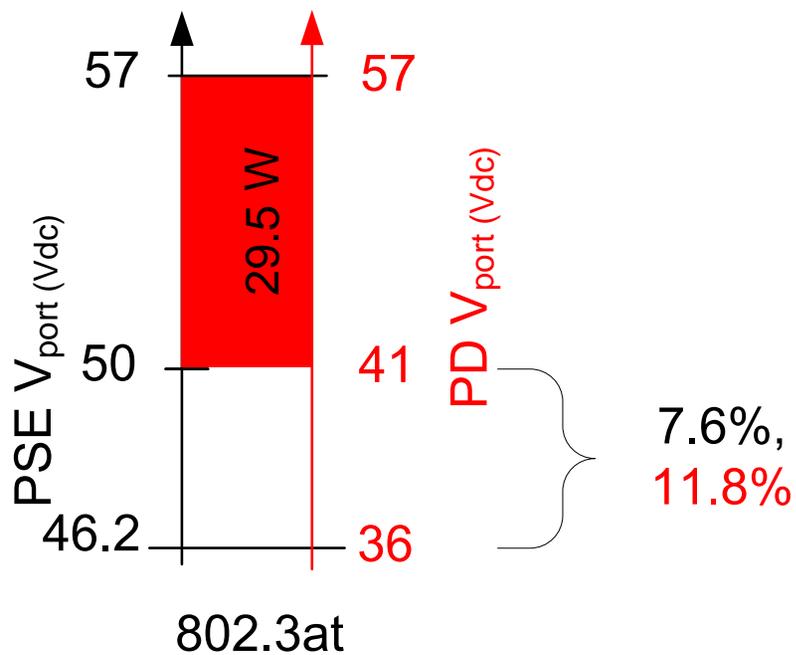
Slow 0 to 100% load change.
~160us transient.



900 W load change.
This is a change in static port voltage.

29.5 W PD

VPSE (Vdc)	VPD (Vdc)	Icable (mA)	Ppd (W)	Ppse (W)	Limit
	40.8	720	29.5	36	wire
50	36	1,120	40.3	56	I _{cut_max}
	35	1,200	42.0	60	I _{lim_min}



This value is 1.7x the maximum average PD power demand.

It is determined using the same method as IEEE 802.3.

The largest droop for the sample PSEs is 6%.