

P1505.3

Submitter Email: mjs@sysintech.com

Type of Project: New IEEE Standard

PAR Request Date: 14-Apr-2011

PAR Approval Date: 16-Jun-2011

PAR Expiration Date: 31-Dec-2015

Status: PAR for a New IEEE Standard

1.1 Project Number: P1505.3

1.2 Type of Document: Standard

1.3 Life Cycle: Full Use

2.1 Title: Standard for the Universal Test Interface Framework and Pin Configuration for Portable/Bench Top Test Requirements Utilizing IEEE 1505 Receiver Fixture Interface Standard

3.1 Working Group: Hardware Interfaces Working Group (SASB/SCC20/HI_WG)

Contact Information for Working Group Chair

Name: MICHAEL STORA

Email Address: mjs@sysintech.com

Phone: 973-299-8321

Contact Information for Working Group Vice-Chair

Name: David Droste

Email Address: ddroste2@comcast.net

Phone: 256-895-2311

3.2 Sponsoring Society and Committee: IEEE-SASB Coordinating Committees/SCC20 - Test and Diagnosis for Electronic Systems (SASB/SCC20)

Contact Information for Sponsor Chair

Name: Mike Seavey

Email Address: michael.seavey@ngc.com

Phone: 224-558-7347

Contact Information for Standards Representative

None

4.1 Type of Ballot: Individual

4.2 Expected Date of submission of draft to the IEEE-SA for Initial Sponsor Ballot: 01/2015

4.3 Projected Completion Date for Submittal to RevCom: 10/2015

5.1 Approximate number of people expected to be actively involved in the development of this project: 15

5.2 Scope: The scope of this standard is the definition of a universal framework/footprint and pin configuration utilizing IEEE 1505 Receiver-Fixture Interface (RFI) framework and connector specifications for portable and bench top test applications. The pin configuration defined within this standard shall apply to commercial, aerospace, and military Automatic Test Equipment (ATE) testing applications.

5.3 Is the completion of this standard dependent upon the completion of another standard: No

5.4 Purpose: To provide a standardized universal test interface input/output (I/O) framework and pin configuration to enable the interoperability of compliant interface fixtures (also known as interface test adapters, interface devices, or interconnection devices) on multiple ATE systems utilizing IEEE 1505 Receiver-Fixture Interface (RFI) in portable and benchtop configurations.

5.5 Need for the Project: There is no current standard (formal or defacto) standard that defines a universal framework and pin configuration focused on portable and/or benchtop test systems. The universal approach is required to support greater pin independence and versatility in how we interface test system resources to the Unit-Under-Test (UUT). Users of ATE Systems also need a definition for portable and benchtop systems that supports migrating test program set software and adapter/fixture hardware between test systems. This project will mitigate rehost difficulties by applying a universal framework and pin configuration based upon IEEE-1505 specifications, that would be matched/reconfigured through means of ATE embedded switching of I/O signals and automated software driven test system tools.

5.6 Stakeholders for the Standard: Primary stakeholders are commercial, military and aerospace owners and suppliers of portable and benchtop ATE Systems.

Intellectual Property

6.1.a. Is the Sponsor aware of any copyright permissions needed for this project?: No

6.1.b. Is the Sponsor aware of possible registration activity related to this project?: No

7.1 Are there other standards or projects with a similar scope?: No

7.2 Joint Development

Is it the intent to develop this document jointly with another organization?: No

8.1 Additional Explanatory Notes (Item Number and Explanation): 5.5: The current P1505.1 is a pin configuration primarily applicable to larger test systems. This project addresses a smaller footprint applicable to portable and benchtop systems. These can be viewed as related, but are for different test system I/O requirements.