February 8th, 2012

Subject: Invitation to ballot on IEEE P1149.1-2012

Dear IEEE-SA member,

IEEE P1149.1-2012 is the result of a major revision and enhancement to IEEE 1149.1. A PAR was approved and the 1149.1 WG was formed in 2009. The new draft standard includes:

- o Six new optional instructions
- o Enhances input and power pin descriptions in BSDL
- o Configuration of programmable I/O pins or other chip conditions
- o Adds test data register fields and mnemonics in the BSDL
- o Supports test data registers across power domains
- o Procedural documentation via the PDL language
- o Enables test definition re-use from IP to IC to board.

We believe this revision will be as important as the original 1149.1 standard was back in 1990. More information on these changes to 1149.1 can be found at http://grouper.ieee.org/groups/1149/1.

If you have any questions or would like to discuss your obligations as a member of the 1149.1 ballot group, please feel free to contact us at cclark@intellitech.com or 603-750-7116.

We thank you in advance for your efforts and contributions.

Sincerely,

CJ Clark
Working Group Chair

Carol Pyron
Working Group Vice-Chair

Carl Barnhart
Editor

Bill Tuthill
Secretary

```
***** SPONSOR BALLOT INVITATION *****
You have indicated an interest in balloting IEEE Standards documents.

IMPORTANT! To participate, you must have access to myProject through an IEEE Web
Account.

If you would like to participate in IEEE Standards Sponsor Ballot P1149.1

Title: Standard Test Access Port and Boundary Scan Architecture you must enroll as a
member of the ballot group by 10-Mar-2012, 11:59 p.m. Eastern Time.

***** IEEE STANDARD DOCUMENT INFORMATION ***** The IEEE Computer Society
Society/Standard Test Access Port and Boundary Scan Architecture invites you to
participate in the New Sponsor Ballot for:
P1149.1

Title: Standard Test Access Port and Boundary Scan Architecture
```

1. Overview

1.1 Scope

This standard defines test logic that can be included in an integrated circuit to provide standardized approaches to:

— Testing the interconnections between integrated circuits once they have been assembled onto a printed circuit board or other substrate;

— Testing the integrated circuit itself; and

— Observing or modifying circuit activity during the component's normal operation.
The test logic consists of a boundary-scan register and other building blocks and is accessed through a Test Access Port (TAP).

```
***** ENROLLMENT INSTRUCTIONS *****

Go directly to:

https://development.standards.ieee.org/my-site/open-ballot-invitations

Or, follow this process:

1. Log onto myProject; click the link for myBallot.
2. Click the link for myBallot (if applicable); click the link for "Show/Join Open
Ballot Invitations":
https://development.standards.ieee.org/my-site/open-ballot-invitations
3.  Scroll down until you find the open ballot group of interest to you; then scroll
over to the right margin and click "join" under the "Actions" column.
4.  Enter or select an affiliation from the drop down menu (appearing as you key in
your affiliation in the Organization window), select your classification category,
and click "OK" at the bottom of the screen.
You will receive an on-screen confirmation that you successfully joined the group.

If you are not an IEEE SA Member, you can either enroll in a single ballot by paying
a per ballot fee or you can join the IEEE SA by choosing an option link below.
```

Joining the IEEE SA entitles you to participate in as many sponsor ballots as you like for the year.

OPTION 1 - I am already an IEEE Member and I want to add Standards Association Membership. Go to:
http://www.ieee.org/membership_services/index.html
OPTION 2 - I am not an IEEE Member but would like to enroll in both IEEE and IEEE Standards Association. Go to:
http://www.ieee.org/membership_services/membership/join/index.html
OPTION 3 - I am not interested in becoming an IEEE Member, however I would like to enroll as a Standards Association Member only. Go to:
http://standards.ieee.org/sa-mem/join.html

===
OBLIGATIONS AND RESPONSIBILITIES OF BALLOTERS
===
Balloting group members have an obligation to respond during the balloting period; failure to return a ballot may disqualify the balloter from participation in future balloting groups.

Once the document is ready to be balloted, you will receive notification via email. You will typically have 30 days to review the document and return the ballot with your vote. By agreeing to participate in this ballot you have an *obligation* to respond. Failure to return a completed ballot may disqualify you from participating in future ballots.

Thank you for your interest in this ballot.