

Date – 08/02/2011

Attendees: CJ Clark, Bill Tuthill, Adam Ley, Brian Turmelle, Carl Barnhart , Roland Latvala, Ted Eaton, Wim Driessen, Craig Stephan, Ken Parker, Carol Pyron, Josh Ferry, Roger Sowada, Dave Dubberke, John Braden, Bill Eklow, Adam Cron, John Seibold, Dharma Konda,

Missing with pre-excuse Heiko Ehrenberg,

Missing: Lee Whetsel, Neil Jacobson, Mike Richetti, Ted Cleggett, Matthias Kamm
Peter Elias, Francisco Russi,

Agenda:

- 1) **11:00** Patent Slides and Rules of Etiquette
- 2) 11:05
- 3) Draft discussion
- 4) Further discussion on mixed R_F/R_A
- 5) Homework assignments
- 6) Reflector Emails

Meeting Called to order at 11:00 am EST

Minutes:

New Members

Roger Sowada – Honeywell
Dharma Konda – Qlogic

Review Patent Slide – Reminder sent out over email during the last week.
Review of Working Group Meeting Guidelines

Draft Discussion

Carl has not received any reviews of substance

Mixed R_F/R_A

John S: sent some examples of

Carl – would be easier for checking purposes if the deferred symbol was a requirement. It would be easy to check in the RegisterAccess statement. Also it makes it more human readable. Prefers having the marker

Ken- would you have a left bracket and integer and right bracket. Or would it have a *

Carl – yes if it was defined than you can have a number.

Ken – proposal then is to have a bracket phrase with integer or *

Carl - For registers that don't have a hard coded length

Ken – can leave bracketed phrase from Init data as it will be defined later in BSDL

CJ – always said that Boundary is done this way why don't we do init data this.

Boundary is always keyword by itself. Not with a bracket.

With init data you have 2 forms.

Adam L- proposal is that it would only appear in the naked form. No option to specify the length.. Attributes would define length elsewhere.

CJ – Correct if we only had the 1 method. Carl said there were 2 methods.

Carl – advantage of having number or * is that if there is a number than you know it is in Register Access or Register Field. The * means that it is deferred

Ted – Why don't we do it the same way as the Boundary Register?

Carl – there are explicit just for the boundary Register, which makes it an exception. We are not just talking about init-data it could be any register that has a deferred length.

Ken – If it is not a standard instruction than it should have a field specifier and if the field specifier has an integer than you know it's the end of discussion. If it has a * than you know it is completed in the RegisterAccess/RegisterFields area

Ken – [*] means that you need to look later for data. [integer] than the value is give and doesn't need to look further. no brackets are all means that it is a standard instruction that doesn't need one

CJ – INIT_DATA is a defined register and it's length's is determined later on. Why does it have brackets.

Ken – init_data if I saw a [integer] , there wouldn't have to be a Register Assembly Init_data. Would encourage people to put an integer there to have it check, but not required.

John B – Likes [*] or [integer]

Dharma – if used a * what does that mean

CJ - * takes on the meaning of deferred. So the length is defined later

Roland – the IC vendor that is generating the BSDL, doesn't seem hard for them to fill in the fixed length.

Carl – purpose of register Assembly was to avoid that.

Roland – IC vendor should be known. Doesn't see big advantage for deferring

Josh – FPGAs are dynamic. So * would be helpful

CJ – would support both cases, so if you know the information you can certainly put it in

Adam C – Register fields and Register Assembly could they be in a package file.

CJ – yes

Adam C - * are there, because they could be in package file.

CJ – Correct. Don't need to go hunting in package file. So the * is useful

Dave – For register access does this section need to be filled out if you use standard instructions and registers.

CJ – yes.. Init-Data is target of Init_Setup

Carl – you don't know if you have an error of omission until you get to the end of the file.

Ken – Register Access is not defined as optional or mandatory

Carl – will have to clean that up

Carl – do we want to make it optional?

Adam c- personally it should be defined

IEEE 1149.1- 2011 Boundary Scan Working Group Minutes

Adam C – moves that it should be done with * or number

Ken – if the register Access is missing it would imply that it is deferred

Motion – If Register_Access exists, than to describe a deferred register lengths use format that is in the current version of draft which is to use the [*] or [integer] syntax
Motion passes unanimously

HomeWork Status

John has passed his examples in to the working group. CJ is running them through the parser.

Carol – is still working on examples

Heiko is still working on examples.

CJ is still working on port assignments

Homework assignments.

Heiko and Carol's assignments are outstanding and will be done for next week's meeting

CJ will have examples of port assignments

Bill E – work on more concrete example and definition of the ESSID register

- **Meeting adjourned: 12:00 EST.**

Next Meeting: 8/9/2011 11:00 AM EST

1 Motion Made

- ✓ If Register_Access exists, than to describe a deferred register lengths use format that is in the current version of draft which is to use the [*] or [integer] syntax

Motion passes unanimously

NOTES:

1149.1 working group website - <http://grouper.ieee.org/groups/1149/1/>

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JOIN the meeting as GUEST – will have to ask to present

Meeting time: Tuesdays 11:00 AM (EST) (Recurring)

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