

**Date – 08/30/2011**

**Attendees:** CJ Clark, Bill Tuthill, Brian Turmelle, Ken Parker, Craig Stephan, Josh Ferry, Roger Sowada, John Braden, Carl Barnhart, John Seibold, Dave Dubberke, Dharma Konda, Carol Pyron, Heiko Ehrenberg, Francisco Russi, Bill Eklow,

**Missing with pre-excuse** Adam Cron, Adam Ley, Ted Eaton, Wim Driessen, Peter Elias,

**Missing:** Lee Whetsel, Neil Jacobson, Mike Richetti, Ted Cleggett, Matthias Kamm , Roland Latvala,

**Agenda:**

1. Review Patent Slides and Rules of Etiquette
2. Finalize <input spec> B.8.14.3.8
3. Carl's proposal for cell types and cell value
  - a. Allows extensions to R\_A and R\_F without re-defining R\_A or R\_F in its entirety
  - b. Should CAPUPD be a standard cell ?
    1. There may be no other test method on the 1149.1 TDRs, capturing the output of the update may be the only way to get visibility. Describing it would help educate.
4. Mixed R\_F/R\_A
5. Homework assignments

**Meeting Called to order at 11:07 am EST**

**Minutes:**

Review Patent Slide – Reminder sent out over email.

Review of Working Group Meeting Guidelines

Review of <input spec> section B.8.14.3.8

Review of <input spec> changes made on Friday's meeting.

Key word "Keeper" was added list of key words

Carl Reviewed Changes he made to the input spec in the current draft

Input spec is only for a cell with an input or clock.

Added Open0/Open1 to section

Weak 0/ Weak 1 is an internal Pull0/Pull1

Keeper is a "Kept" state memory of the last strongly driven logic state

Carol: is there an advantage or syntax for the keywords?

Ken: keywords keeps the input spec separate from a disable spec so there wouldn't need a kludge of an Open1 or Open0 for a single cell BiDir

ken proposes a change for the input spec

Carl's objection: now have 3 different syntaxes for cell line. Input/output/bidir. Seems to add complication as much as remove complication. Adds complexity of 3 possible line formats

CJ: new parsing based on final value on the right hand side.

CJ: currently have a way to describe this. Open1 in the file that we would normally have Z.

CJ: this new change is adding complexity

Francisco: backwards compatibility?

CJ: input spec changes.. output spec stays the same.

Software folks would probably preferred to add a new keyword.

Ken's proposal was postponed to allow a vote to accept the current <input spec>

Carol : makes a motion to accept input spec b.8.14.1 as written with no prejudice for future changes.

Brian T seconds.

No Discussion on motion

Motion passes.

Vote was 0 NO / 14 Yes / 1 Abstain(Bill E.)

The Chair invited Ken to bring hi proposal back to the group after the vote.

Ken chose to wait until a later time to return to the proposal.

Carl presents his proposal for cell types and cell value

Basic idea. 3 types of assignments for RA and RA.

Value -RESETVAL/SAFE/DEFAULT/CAPTURE

Reset what type of reset PORRESET/TAPRESET/CHRESET/

Type - NO PI/ NOPO/NOUPD

Value assignments pretty much cover everything but not sure.

Reset covers everything

Type - there are additional types people have talked about.

So a tool vendor could offer a tool that could use understand additions by using Register cell value or register cell type in a package file

Register Fields and Register Assembly are complicated enough so you don't want to redefine everything.

Two new attributes added, Register\_Cellvalue and Register\_Celltype.

CJ: big advantage is that we can always do BSDL extensions but you would have to redefine registers fields and register assembly all over.

Ken: wants to know what the rational for it was.

Carl: had been discussions about cap\_update and oneshot type boundary cells. So it was clear that there were other things people do that we don't think of so we can allow some extension of the assignments.

CJ: allows a mechanism for people to expand in the future. Doesn't require you to redefine the entire attribute.

Ken: when did the email with this proposal come out?

Carl: a week ago Friday? 8/26 Carl will resend email  
Carl : feels there is value in “user\_type” and but less in “user\_val”  
Register Fields  
It was proposed to Mix register fields in with register assembly  
Carl had put it in the document  
CJ is ok with it. Just not sure if it was nailed down and left open.

#### Input-Keeper

CJ shows his email (input-keeper-for-jtag.pdf)  
Ken: keeper on an input does exist.  
Example- TI was going to put Keepers on bus pins  
Ken : while it does drive back to the board, the logic value can't be trusted. Any glitch  
can change the state.  
Carl: agrees and believes it is covered in the spec already

Ken: is there going to be a September spec out soon  
Carl: shortly. Need to get some data into the spec such as sample relaxation

CJ: one more item. ESSID. Are we going to get some information on this topic?  
Bill E; yes.  
Carl: it was left that you would come up with a proposal.  
Bill E: next week Bill feels he will have something for the working group

#### Notes for ITC-2011 –

CJ has submitted an updated poster session  
Tuesday meeting 9/20 will be canceled during ITC  
Informational meeting for public on 9/20 in the AM

- **Meeting adjourned: 12:10 EST.**  
**Next Meeting: 9/6/2011 11:00 AM EST**

#### 1 Motion Made

**Motion to accept input spec b.8.14.1 as written with no prejudice for future changes.**

Motion passed with 14 yes and 1 Abstain

#### HomeWork Status

John has passed his examples in to the working group. CJ is running them through the parser.

Carol – is still working on examples  
Heiko is still working on examples.  
CJ is still working on port assignments

Homework assignments.

Heiko and Carol's assignments are outstanding and will be done for next week's meeting

CJ will have examples of port assignments

Bill E – work on more concrete example and definition of the ESSID register

NOTES:

1149.1 working group website - <http://grouper.ieee.org/groups/1149/1/>

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**JOIN the meeting as GUEST** – will have to ask to present

Meeting time: Tuesdays 11:00 AM (EST) (Recurring)

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